



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 115

Servidor Itautec MX201 (Intel Xeon E5410)

SPECint_rate_base2006 = 94.1

CPU2006 license: 9001

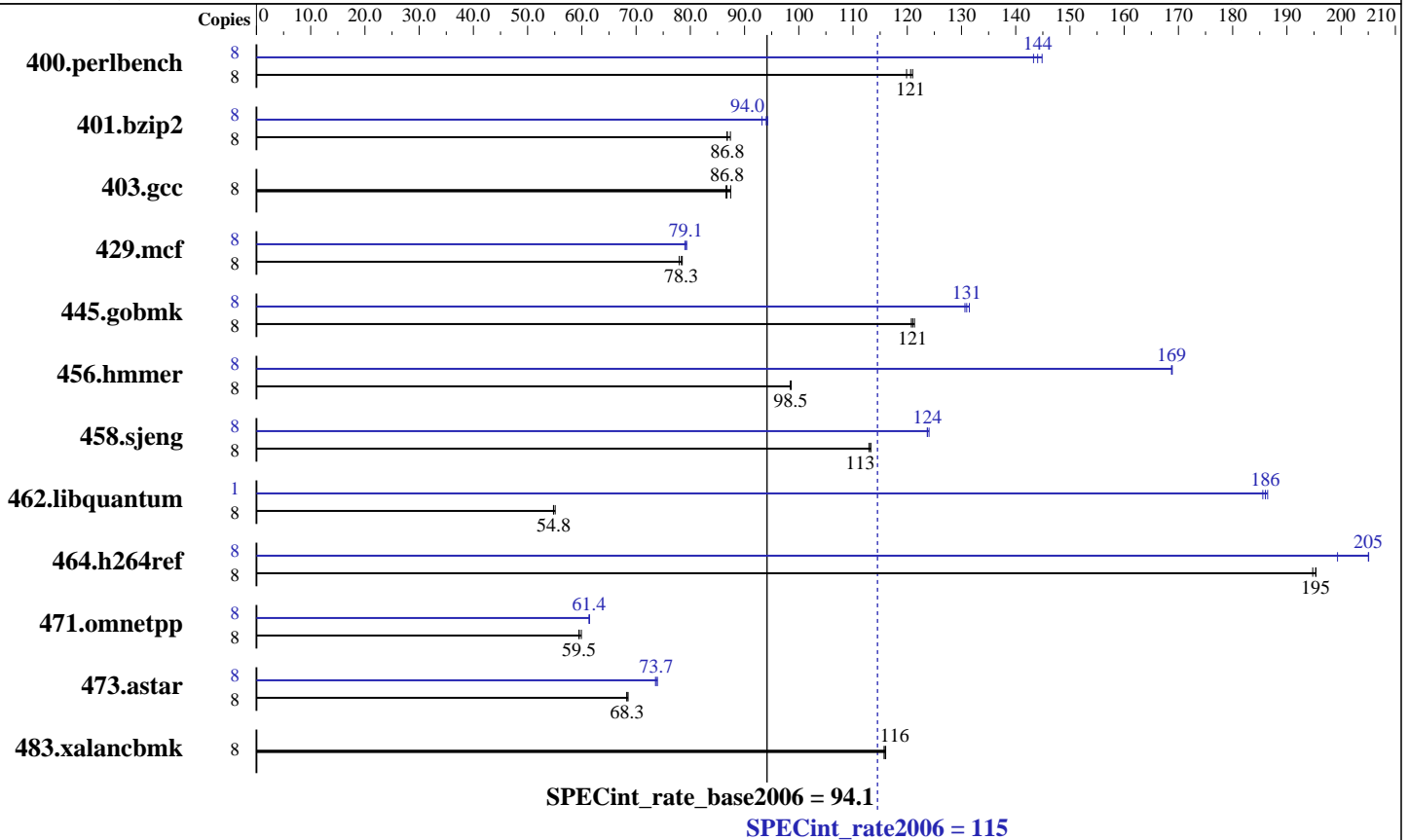
Test date: Apr-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics:
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	646	121	652	120	648	121	8	540	145	543	144	545	143
401.bzip2	8	883	87.4	889	86.8	890	86.8	8	821	94.0	828	93.2	819	94.2
403.gcc	8	742	86.8	737	87.4	744	86.6	8	742	86.8	737	87.4	744	86.6
429.mcf	8	929	78.5	931	78.3	936	77.9	8	922	79.1	920	79.3	924	79.0
445.gobmk	8	692	121	695	121	694	121	8	642	131	641	131	638	131
456.hammer	8	758	98.5	757	98.6	758	98.4	8	442	169	442	169	442	169
458.sjeng	8	855	113	855	113	857	113	8	782	124	781	124	783	124
462.libquantum	8	3009	55.1	3025	54.8	3027	54.8	1	112	186	111	186	111	186
464.h264ref	8	906	195	909	195	906	195	8	864	205	888	199	863	205
471.omnetpp	8	835	59.9	841	59.5	840	59.5	8	814	61.4	815	61.3	815	61.4
473.astar	8	820	68.5	823	68.3	822	68.3	8	760	73.9	763	73.6	762	73.7
483.xalancbmk	8	476	116	476	116	477	116	8	476	116	476	116	477	116

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

Platform Notes

BIOS configuration:
Hardware Prefetch Enabled

General Notes

This result was measured on the Servidor Itautec MX201.
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: basepeak = yes
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 16:48:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 April 2008.