



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 53.7

Servidor Itaotec LX201 (Intel Xeon E5410)

SPECint\_rate\_base2006 = 48.7

CPU2006 license: 9001

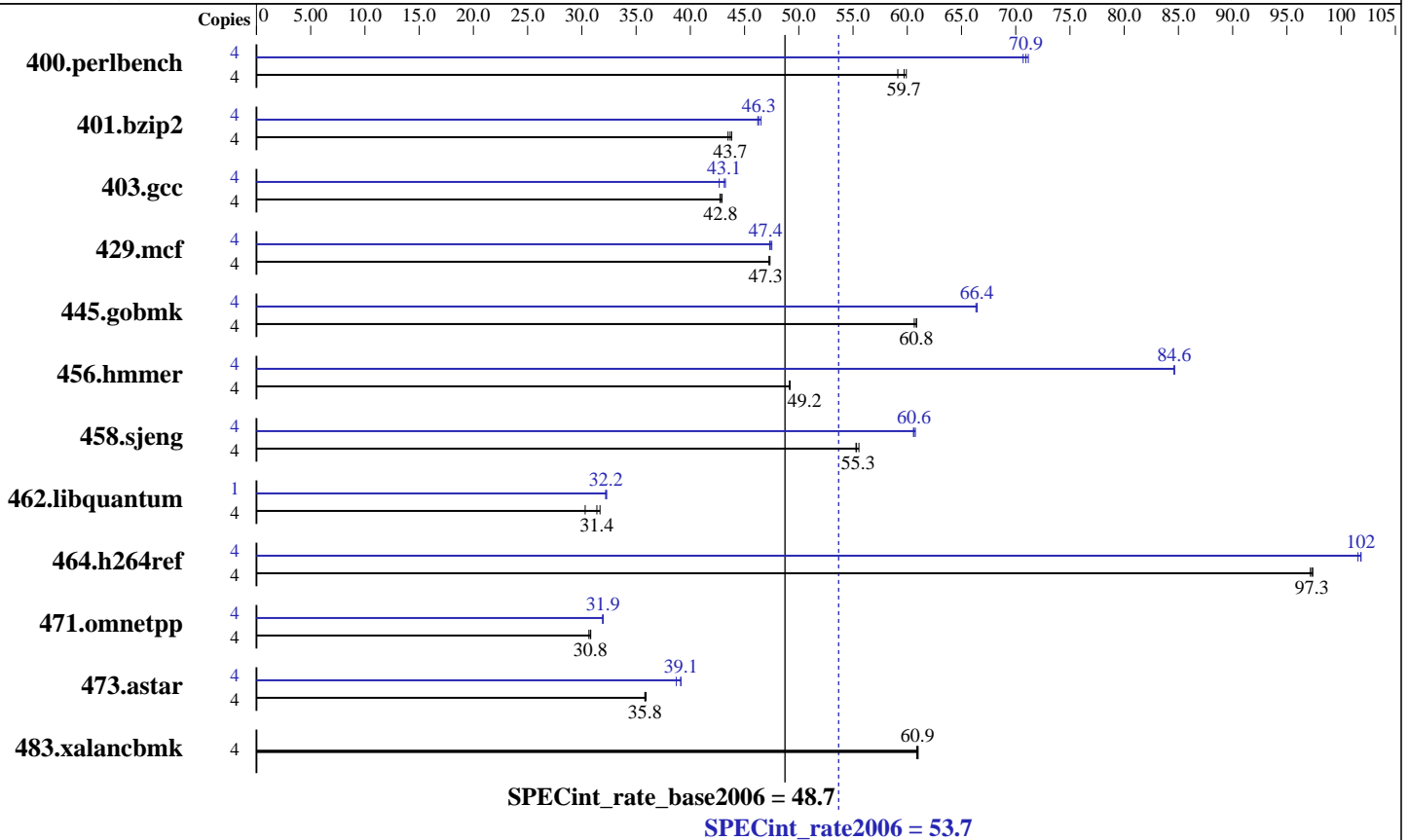
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Apr-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



## Hardware

CPU Name: Intel Xeon E5410  
 CPU Characteristics:  
 CPU MHz: 2330  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8 \* 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SATA-2, 160GB, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	661	59.1	<b>655</b>	<b>59.7</b>	652	59.9	4	549	71.2	<b>551</b>	<b>70.9</b>	553	70.7
401.bzip2	4	888	43.5	881	43.8	<b>884</b>	<b>43.7</b>	4	830	46.5	<b>833</b>	<b>46.3</b>	835	46.2
403.gcc	4	754	42.7	750	42.9	<b>752</b>	<b>42.8</b>	4	745	43.2	<b>747</b>	<b>43.1</b>	755	42.7
429.mcf	4	770	47.3	772	47.3	<b>772</b>	<b>47.3</b>	4	768	47.5	771	47.3	<b>770</b>	<b>47.4</b>
445.gobmk	4	<b>690</b>	<b>60.8</b>	690	60.9	692	60.6	4	633	66.3	<b>632</b>	<b>66.4</b>	632	66.4
456.hammer	4	759	49.1	<b>759</b>	<b>49.2</b>	759	49.2	4	441	84.6	<b>441</b>	<b>84.6</b>	441	84.6
458.sjeng	4	871	55.5	<b>875</b>	<b>55.3</b>	876	55.3	4	799	60.6	<b>798</b>	<b>60.6</b>	797	60.7
462.libquantum	4	2735	30.3	<b>2641</b>	<b>31.4</b>	2617	31.7	1	642	32.3	643	32.2	<b>643</b>	<b>32.2</b>
464.h264ref	4	<b>910</b>	<b>97.3</b>	909	97.4	911	97.2	4	<b>869</b>	<b>102</b>	869	102	872	102
471.omnetpp	4	816	30.6	<b>812</b>	<b>30.8</b>	811	30.8	4	<b>783</b>	<b>31.9</b>	782	32.0	783	31.9
473.astar	4	784	35.8	<b>784</b>	<b>35.8</b>	782	35.9	4	718	39.1	<b>718</b>	<b>39.1</b>	725	38.7
483.xalancbmk	4	453	60.9	452	61.0	<b>453</b>	<b>60.9</b>	4	453	60.9	452	61.0	<b>453</b>	<b>60.9</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Enabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

456.hmmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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