



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 71.8

IBM BladeCenter HS12 (Intel Xeon X3363)

SPECint_rate_base2006 = 59.8

CPU2006 license: 11

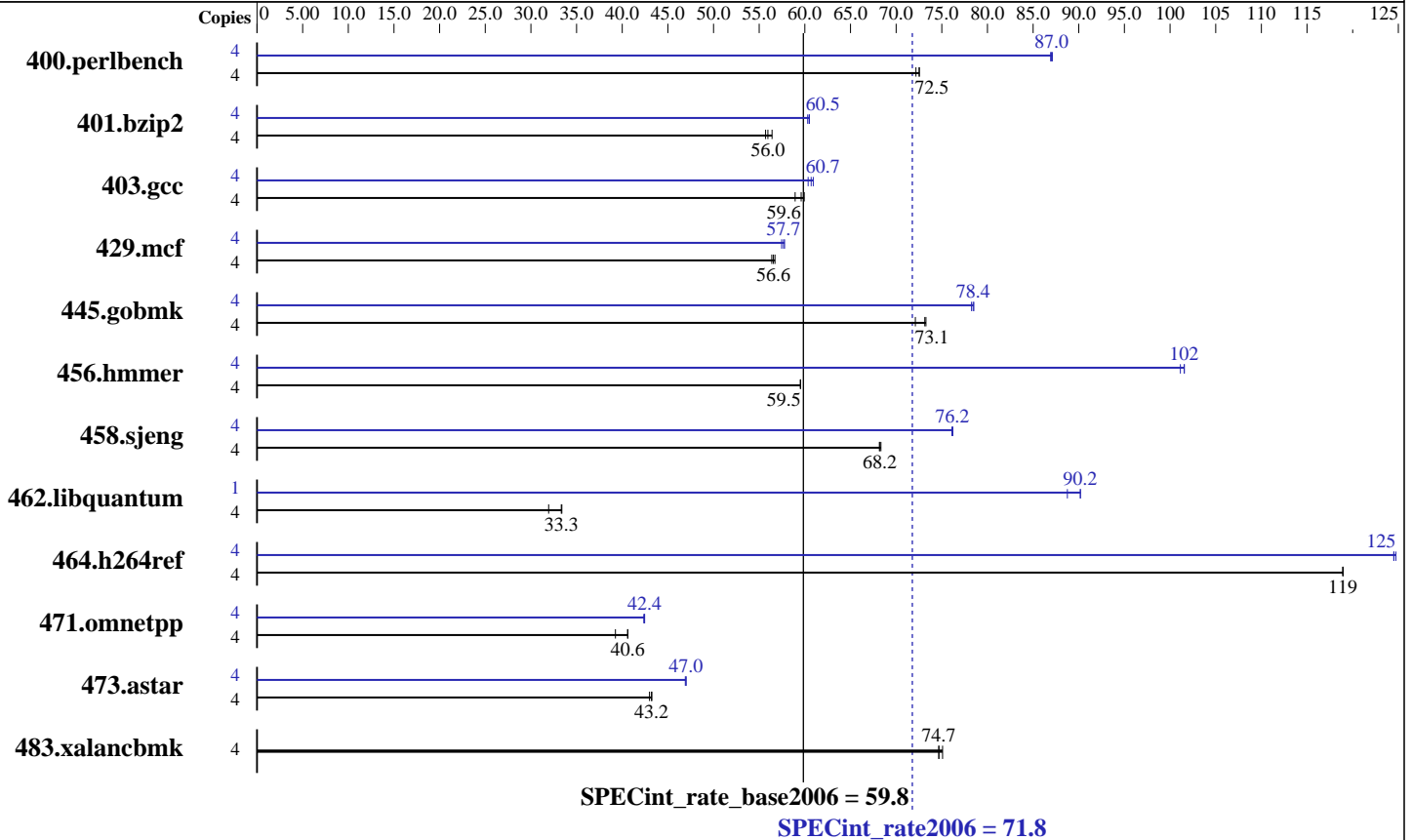
Test date: Mar-2008

Test sponsor: IBM Corporation

Hardware Availability: May-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X3363
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-5300 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>539</u>	<u>72.5</u>	542	72.2	539	72.5	4	450	86.9	449	87.1	<u>449</u>	<u>87.0</u>
401.bzip2	4	693	55.7	684	56.4	<u>690</u>	<u>56.0</u>	4	<u>638</u>	<u>60.5</u>	640	60.3	638	60.5
403.gcc	4	<u>540</u>	<u>59.6</u>	546	58.9	537	59.9	4	<u>531</u>	<u>60.7</u>	529	60.9	533	60.4
429.mcf	4	647	56.4	<u>645</u>	<u>56.6</u>	643	56.7	4	635	57.5	631	57.8	<u>633</u>	<u>57.7</u>
445.gobmk	4	582	72.1	<u>574</u>	<u>73.1</u>	573	73.3	4	<u>535</u>	<u>78.4</u>	536	78.3	534	78.5
456.hammer	4	627	59.5	627	59.5	<u>627</u>	<u>59.5</u>	4	<u>367</u>	<u>102</u>	367	102	369	101
458.sjeng	4	<u>709</u>	<u>68.2</u>	708	68.3	710	68.2	4	636	76.1	635	76.2	<u>635</u>	<u>76.2</u>
462.libquantum	4	2595	31.9	<u>2487</u>	<u>33.3</u>	2482	33.4	1	233	88.8	<u>230</u>	<u>90.2</u>	230	90.2
464.h264ref	4	744	119	<u>744</u>	<u>119</u>	744	119	4	<u>711</u>	<u>125</u>	711	124	710	125
471.omnetpp	4	637	39.3	<u>616</u>	<u>40.6</u>	616	40.6	4	<u>589</u>	<u>42.4</u>	590	42.4	589	42.5
473.astar	4	653	43.0	<u>650</u>	<u>43.2</u>	649	43.2	4	<u>598</u>	<u>47.0</u>	599	46.9	598	47.0
483.xalancbmk	4	<u>370</u>	<u>74.7</u>	370	74.7	368	75.1	4	<u>370</u>	<u>74.7</u>	370	74.7	368	75.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
taskset utility used to bind CPU(s) to processes
Powersaved daemon was disabled in OS

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.00.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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