



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 71.0

PowerEdge R200 (Intel Xeon X3350, 2.66 GHz)

SPECint_rate_base2006 = 61.7

CPU2006 license: 55

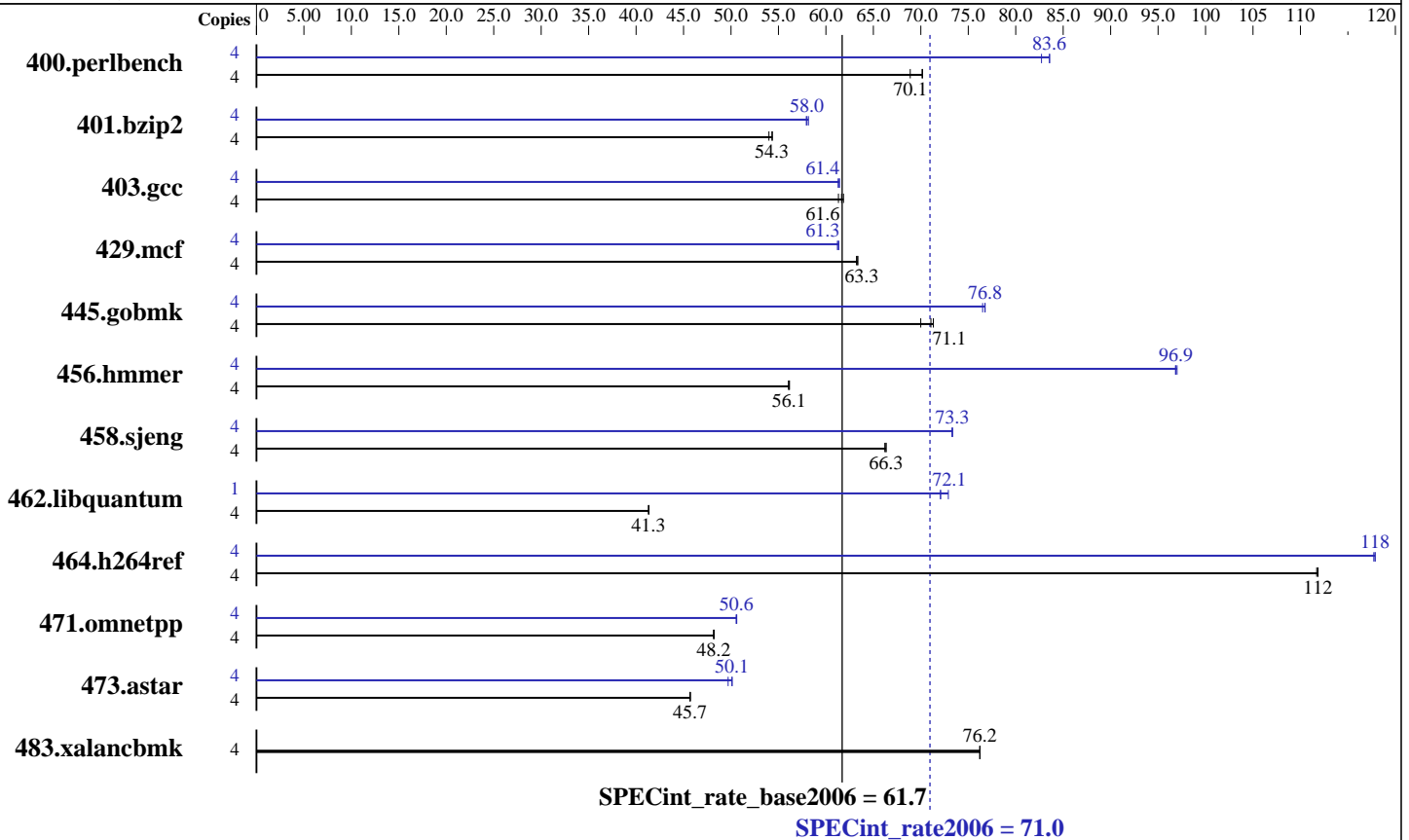
Test date: Mar-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X3350
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2GB 800MHz DDR2)
 Disk Subsystem: 1 x 750 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap library V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	567	68.9	<u>557</u>	<u>70.1</u>	557	70.2	4	468	83.6	<u>468</u>	<u>83.6</u>	472	82.7
401.bzip2	4	715	54.0	<u>711</u>	<u>54.3</u>	710	54.4	4	666	57.9	<u>666</u>	<u>58.0</u>	664	58.2
403.gcc	4	521	61.9	525	61.3	<u>522</u>	<u>61.6</u>	4	525	61.3	<u>524</u>	<u>61.4</u>	524	61.4
429.mcf	4	577	63.2	<u>576</u>	<u>63.3</u>	576	63.4	4	596	61.2	595	61.4	<u>595</u>	<u>61.3</u>
445.gobmk	4	<u>590</u>	<u>71.1</u>	599	70.0	588	71.3	4	548	76.5	<u>547</u>	<u>76.8</u>	547	76.8
456.hammer	4	664	56.2	666	56.1	<u>665</u>	<u>56.1</u>	4	<u>385</u>	<u>96.9</u>	385	96.8	385	97.0
458.sjeng	4	<u>730</u>	<u>66.3</u>	731	66.2	730	66.3	4	660	73.3	660	73.3	<u>660</u>	<u>73.3</u>
462.libquantum	4	2006	41.3	<u>2006</u>	<u>41.3</u>	2005	41.3	1	<u>287</u>	<u>72.1</u>	284	72.9	287	72.1
464.h264ref	4	<u>792</u>	<u>112</u>	792	112	791	112	4	<u>751</u>	<u>118</u>	751	118	752	118
471.omnetpp	4	518	48.2	<u>519</u>	<u>48.2</u>	519	48.2	4	495	50.6	<u>494</u>	<u>50.6</u>	494	50.6
473.astar	4	615	45.7	<u>615</u>	<u>45.7</u>	614	45.7	4	<u>561</u>	<u>50.1</u>	560	50.1	565	49.7
483.xalancbmk	4	362	76.2	<u>362</u>	<u>76.2</u>	362	76.2	4	362	76.2	<u>362</u>	<u>76.2</u>	362	76.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.03.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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