



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Itautec

### SPECcfp<sup>®</sup>\_rate2006 = 37.5

### Servidor Itautec MX201 (Intel Xeon E5410)

### SPECcfp\_rate\_base2006 = 36.4

CPU2006 license: 9001

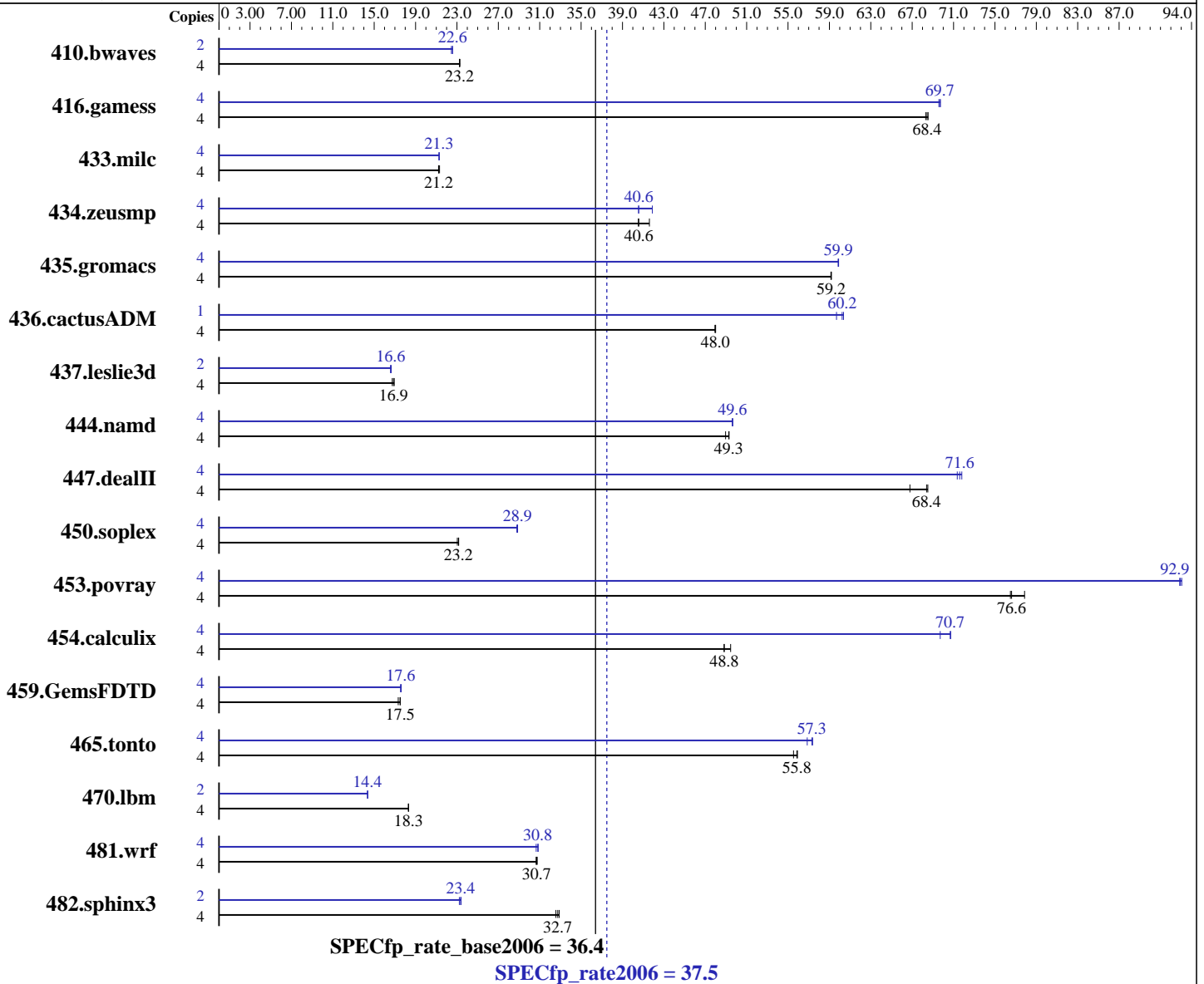
Test date: Mar-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



#### Hardware

CPU Name: Intel Xeon E5410  
 CPU Characteristics:  
 CPU MHz: 2330  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

#### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20080112 Package ID: l\_cc\_p\_10.1.012, l\_fc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp\_rate2006 = 37.5

Servidor Itaotec MX201 (Intel Xeon E5410)

SPECfp\_rate\_base2006 = 36.4

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.17.10.50

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	<u>2339</u>	<u>23.2</u>	2334	23.3	2339	23.2	2	1204	22.6	1210	22.5	<u>1204</u>	<u>22.6</u>
416.gamess	4	1143	68.5	1147	68.3	<u>1144</u>	<u>68.4</u>	4	1123	69.7	1126	69.6	<u>1124</u>	<u>69.7</u>
433.milc	4	1730	21.2	1723	21.3	<u>1729</u>	<u>21.2</u>	4	<u>1726</u>	<u>21.3</u>	1725	21.3	1727	21.3
434.zeusmp	4	875	41.6	898	40.5	<u>897</u>	<u>40.6</u>	4	<u>897</u>	<u>40.6</u>	869	41.9	898	40.6
435.gromacs	4	<u>483</u>	<u>59.2</u>	483	59.1	483	59.2	4	477	59.9	<u>477</u>	<u>59.9</u>	477	59.9
436.cactusADM	4	996	48.0	997	47.9	<u>996</u>	<u>48.0</u>	1	<u>198</u>	<u>60.2</u>	198	60.4	200	59.7
437.leslie3d	4	<u>2230</u>	<u>16.9</u>	2220	16.9	2245	16.7	2	1133	16.6	<u>1133</u>	<u>16.6</u>	1129	16.7
444.namd	4	<u>651</u>	<u>49.3</u>	651	49.3	655	49.0	4	646	49.6	646	49.7	<u>646</u>	<u>49.6</u>
447.dealII	4	<u>669</u>	<u>68.4</u>	685	66.8	668	68.5	4	637	71.8	<u>639</u>	<u>71.6</u>	641	71.4
450.soplex	4	1440	23.2	<u>1441</u>	<u>23.2</u>	1448	23.0	4	1156	28.9	1159	28.8	<u>1156</u>	<u>28.9</u>
453.povray	4	<u>278</u>	<u>76.6</u>	278	76.5	273	77.9	4	229	93.1	229	92.9	<u>229</u>	<u>92.9</u>
454.calculix	4	676	48.8	667	49.5	<u>676</u>	<u>48.8</u>	4	467	70.7	<u>467</u>	<u>70.7</u>	473	69.7
459.GemsFDTD	4	<u>2424</u>	<u>17.5</u>	2450	17.3	2423	17.5	4	2410	17.6	2418	17.6	<u>2415</u>	<u>17.6</u>
465.tonto	4	704	55.9	<u>705</u>	<u>55.8</u>	709	55.5	4	<u>687</u>	<u>57.3</u>	686	57.4	692	56.9
470.lbm	4	<u>3000</u>	<u>18.3</u>	3000	18.3	3001	18.3	2	1914	14.4	1913	14.4	<u>1913</u>	<u>14.4</u>
481.wrf	4	1458	30.6	1453	30.8	<u>1455</u>	<u>30.7</u>	4	1448	30.8	<u>1450</u>	<u>30.8</u>	1457	30.7
482.sphinx3	4	2394	32.6	2371	32.9	<u>2381</u>	<u>32.7</u>	2	1666	23.4	1678	23.2	<u>1669</u>	<u>23.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp\_rate2006 = 37.5

Servidor Itaotec MX201 (Intel Xeon E5410)

SPECfp\_rate\_base2006 = 36.4

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## General Notes

This result was measured on the Servidor Itaotec MX201.  
The Servidor Itaotec MX201 and the Servidor Itaotec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp\_rate2006 = 37.5

Servidor Itautec MX201 (Intel Xeon E5410)

SPECfp\_rate\_base2006 = 36.4

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Base Optimization Flags

C benchmarks:  
-fast  
  
C++ benchmarks:  
-fast  
  
Fortran benchmarks:  
-fast  
  
Benchmarks using both Fortran and C:  
-fast

## Peak Compiler Invocation

C benchmarks (except as noted below):  
/opt/intel/cc/10.1.012/bin/icc -L/opt/intel/cc/10.1.012/lib  
-I/opt/intel/cc/10.1.012/include  
  
433.milc: icc  
  
C++ benchmarks (except as noted below):  
icpc  
  
450.soplex: /opt/intel/cc/10.1.012/bin/icpc -L/opt/intel/cc/10.1.012/lib  
-I/opt/intel/cc/10.1.012/include  
  
Fortran benchmarks (except as noted below):  
ifort  
  
437.leslie3d: /opt/intel/fc/10.1.012/bin/ifort -L/opt/intel/fc/10.1.012/lib  
-I/opt/intel/fc/10.1.012/include  
  
Benchmarks using both Fortran and C:  
icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp\_rate2006 = 37.5

Servidor Itaotec MX201 (Intel Xeon E5410)

SPECfp\_rate\_base2006 = 36.4

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Peak Portability Flags (Continued)

453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32  
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-scalar-rep- -prefetch -opt-malloc-options=3  
482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32  
447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias -scalar-rep-  
450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast  
-opt-malloc-options=3  
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch  
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-ansi-alias -scalar-rep-  
434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast  
437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-opt-malloc-options=3  
459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-prefetch

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp\_rate2006 = 37.5

Servidor Itautec MX201 (Intel Xeon E5410)

SPECfp\_rate\_base2006 = 36.4

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Peak Optimization Flags (Continued)

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090713.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090713.00.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 18:22:08 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 15 April 2008.