



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 50.1

Servidor Itaotec LX211 (Intel Xeon E5405)

SPECint\_rate\_base2006 = 44.3

CPU2006 license: 9001

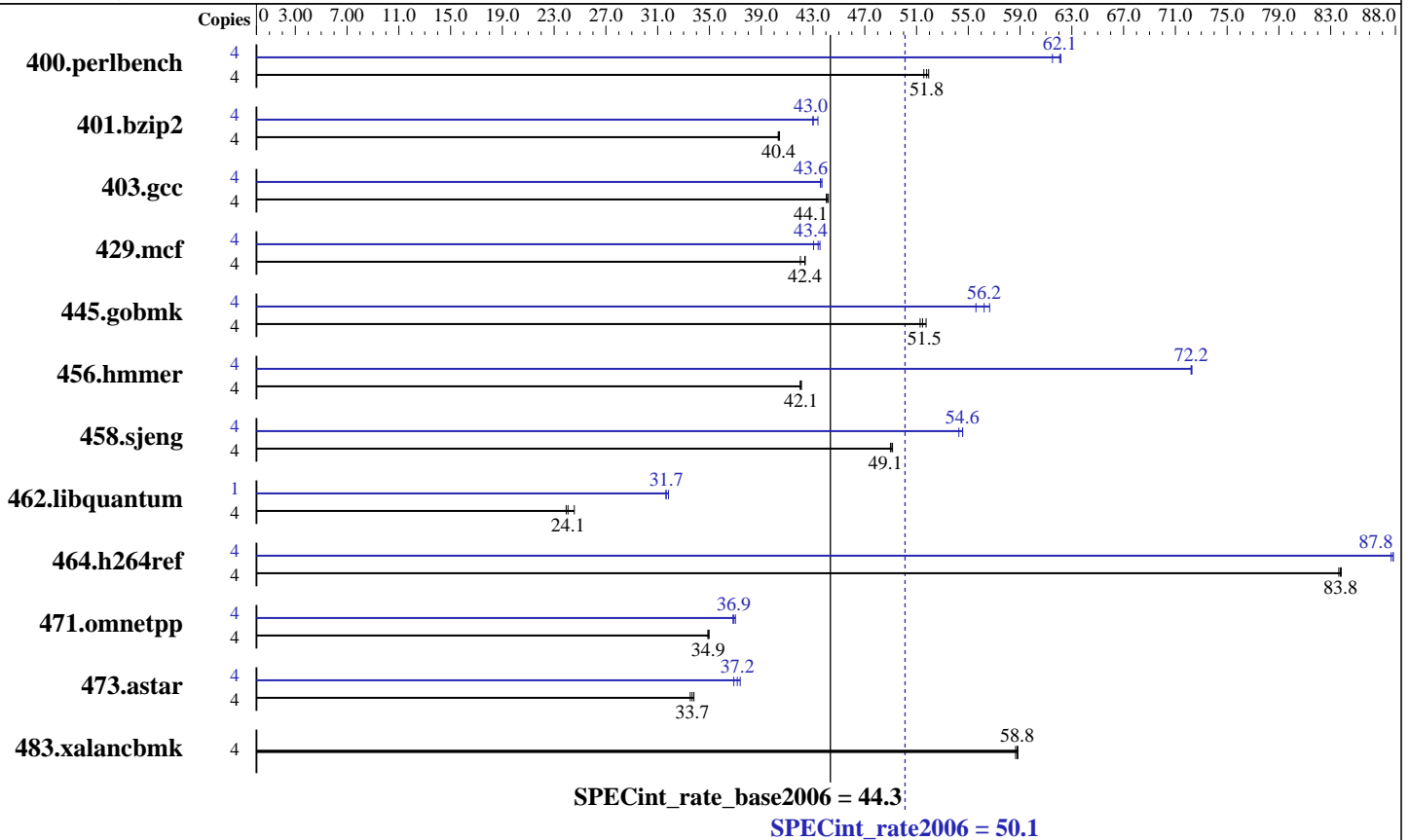
Test date: Mar-2008

Test sponsor: Itaotec

Hardware Availability: Dec-2007

Tested by: Itaotec

Software Availability: Jan-2008



## Hardware

CPU Name: Intel Xeon E5405  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (4 \* 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 10000 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>755</u>	<u>51.8</u>	752	51.9	758	51.6	4	635	61.5	629	62.2	<u>630</u>	<u>62.1</u>
401.bzip2	4	958	40.3	<u>957</u>	<u>40.4</u>	955	40.4	4	890	43.4	<u>897</u>	<u>43.0</u>	898	43.0
403.gcc	4	731	44.0	729	44.2	<u>730</u>	<u>44.1</u>	4	<u>739</u>	<u>43.6</u>	737	43.7	739	43.6
429.mcf	4	868	42.0	860	42.4	<u>861</u>	<u>42.4</u>	4	838	43.5	847	43.0	<u>841</u>	<u>43.4</u>
445.gobmk	4	811	51.7	818	51.3	<u>815</u>	<u>51.5</u>	4	755	55.6	<u>746</u>	<u>56.2</u>	741	56.6
456.hammer	4	886	42.1	888	42.0	<u>887</u>	<u>42.1</u>	4	517	72.2	<u>517</u>	<u>72.2</u>	516	72.3
458.sjeng	4	988	49.0	985	49.1	<u>986</u>	<u>49.1</u>	4	892	54.3	887	54.6	<u>887</u>	<u>54.6</u>
462.libquantum	4	<u>3443</u>	<u>24.1</u>	3463	23.9	3375	24.6	1	<u>654</u>	<u>31.7</u>	655	31.6	651	31.8
464.h264ref	4	1058	83.6	<u>1057</u>	<u>83.8</u>	1056	83.8	4	1010	87.7	1008	87.8	<u>1008</u>	<u>87.8</u>
471.omnetpp	4	715	35.0	<u>716</u>	<u>34.9</u>	717	34.9	4	675	37.0	<u>678</u>	<u>36.9</u>	679	36.8
473.astar	4	<u>834</u>	<u>33.7</u>	831	33.8	838	33.5	4	751	37.4	<u>756</u>	<u>37.2</u>	761	36.9
483.xalancbmk	4	469	58.8	471	58.7	<u>470</u>	<u>58.8</u>	4	469	58.8	471	58.7	<u>470</u>	<u>58.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

456.hmmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 18:17:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 15 April 2008.