



# SPEC® CINT2006 Result

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Itautec

SPECint®\_rate2006 = 93.4

Servidor Itautec LX211 (Intel Xeon E5405)

SPECint\_rate\_base2006 = 79.9

CPU2006 license: 9001

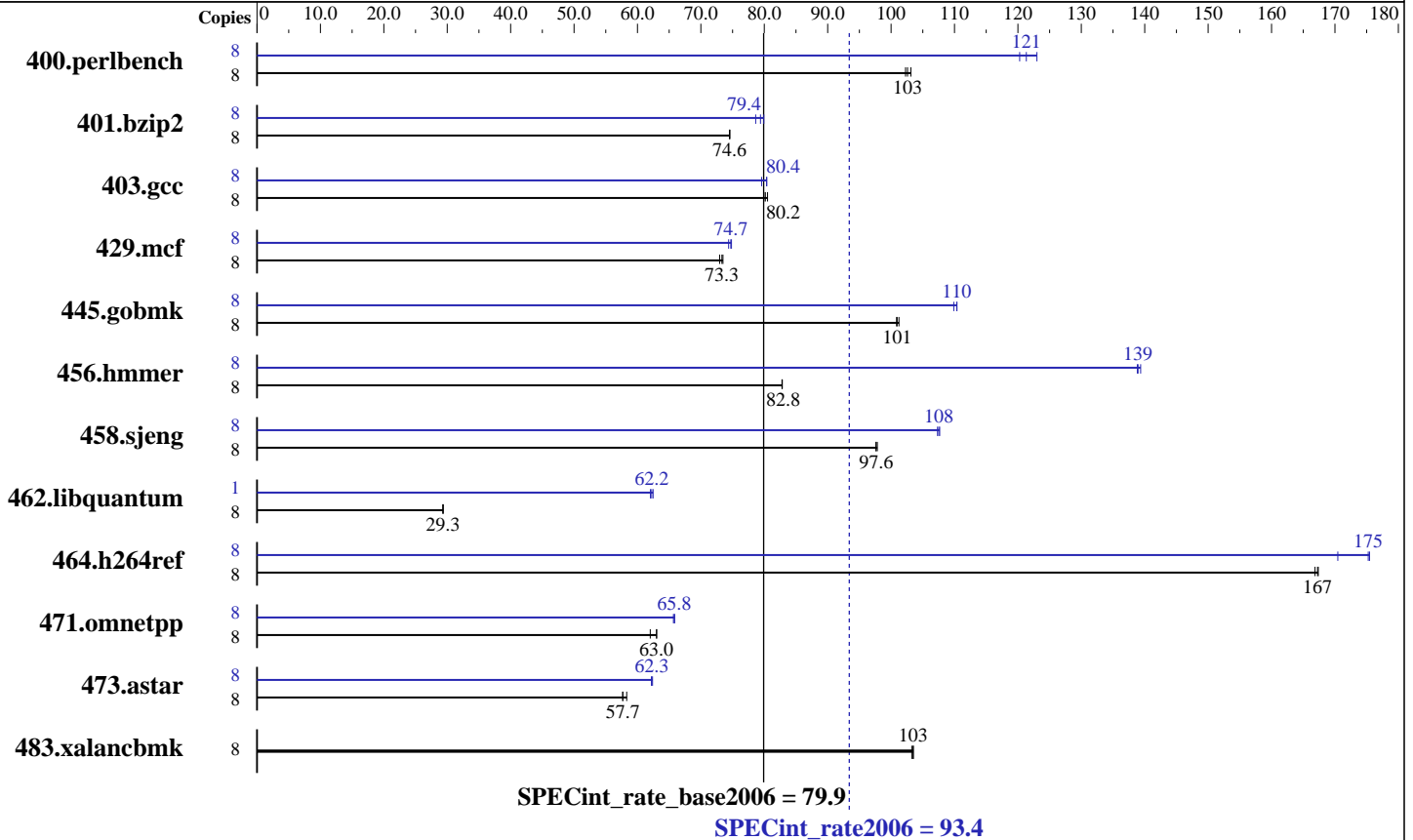
Test date: Mar-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



## Hardware

CPU Name: Intel Xeon E5405  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 12 GB (6 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	764	102	<b><u>762</u></b>	<b><u>103</u></b>	758	103	8	650	120	635	123	<b><u>644</u></b>	<b><u>121</u></b>
401.bzip2	8	1036	74.5	<b><u>1036</u></b>	<b><u>74.6</u></b>	1035	74.6	8	982	78.6	<b><u>972</u></b>	<b><u>79.4</u></b>	966	79.9
403.gcc	8	800	80.5	<b><u>803</u></b>	<b><u>80.2</u></b>	806	79.9	8	801	80.4	809	79.6	<b><u>801</u></b>	<b><u>80.4</u></b>
429.mcf	8	<b><u>995</u></b>	<b><u>73.3</u></b>	993	73.5	1000	73.0	8	981	74.4	<b><u>976</u></b>	<b><u>74.7</u></b>	975	74.8
445.gobmk	8	832	101	<b><u>831</u></b>	<b><u>101</u></b>	829	101	8	760	110	<b><u>760</u></b>	<b><u>110</u></b>	764	110
456.hammer	8	902	82.8	<b><u>901</u></b>	<b><u>82.8</u></b>	901	82.8	8	<b><u>537</u></b>	<b><u>139</u></b>	536	139	538	139
458.sjeng	8	989	97.8	<b><u>991</u></b>	<b><u>97.6</u></b>	992	97.6	8	<b><u>900</u></b>	<b><u>108</u></b>	899	108	902	107
462.libquantum	8	5647	29.4	<b><u>5651</u></b>	<b><u>29.3</u></b>	5653	29.3	1	334	62.0	<b><u>333</u></b>	<b><u>62.2</u></b>	332	62.5
464.h264ref	8	1061	167	<b><u>1058</u></b>	<b><u>167</u></b>	1058	167	8	<b><u>1010</u></b>	<b><u>175</u></b>	1009	175	1039	170
471.omnetpp	8	793	63.0	<b><u>794</u></b>	<b><u>63.0</u></b>	806	62.0	8	759	65.9	761	65.7	<b><u>760</u></b>	<b><u>65.8</u></b>
473.astar	8	963	58.3	<b><u>973</u></b>	<b><u>57.7</u></b>	974	57.7	8	903	62.2	<b><u>902</u></b>	<b><u>62.3</u></b>	900	62.4
483.xalancbmk	8	535	103	533	104	<b><u>533</u></b>	<b><u>103</u></b>	8	535	103	533	104	<b><u>533</u></b>	<b><u>103</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Enabled

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

456.hmmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 18:18:46 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 15 April 2008.