



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 69.0

ProLiant DL160 G5
(3.00 GHz, Intel Xeon X5472)

SPECint_rate_base2006 = 59.0

CPU2006 license: 3

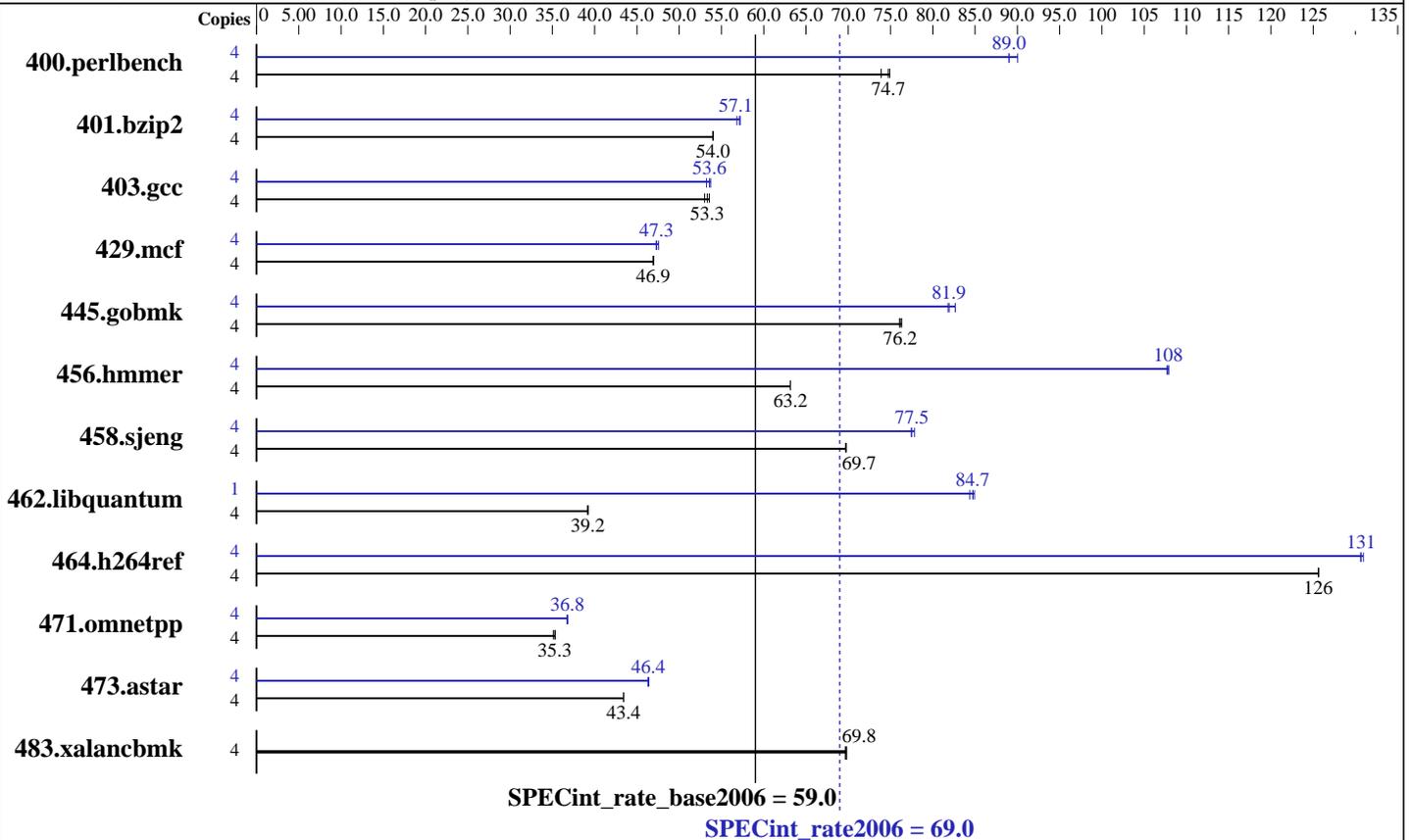
Test date: Mar-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5472
 CPU Characteristics: 3.00 GHz, 2x6 MB L2 shared, 1600 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8x4 GB PC2-5300F CL5)
 Disk Subsystem: 1x160 GB 7.2 K SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	522	74.9	529	73.9	<u>523</u>	<u>74.7</u>	4	434	90.0	439	89.0	<u>439</u>	<u>89.0</u>
401.bzip2	4	<u>715</u>	<u>54.0</u>	715	54.0	715	54.0	4	675	57.2	<u>676</u>	<u>57.1</u>	679	56.8
403.gcc	4	601	53.6	607	53.0	<u>604</u>	<u>53.3</u>	4	<u>601</u>	<u>53.6</u>	605	53.2	599	53.7
429.mcf	4	777	46.9	777	47.0	<u>777</u>	<u>46.9</u>	4	767	47.6	<u>771</u>	<u>47.3</u>	772	47.3
445.gobmk	4	<u>551</u>	<u>76.2</u>	550	76.3	552	76.1	4	513	81.8	508	82.7	<u>512</u>	<u>81.9</u>
456.hammer	4	591	63.2	591	63.1	<u>591</u>	<u>63.2</u>	4	347	108	<u>346</u>	<u>108</u>	346	108
458.sjeng	4	<u>694</u>	<u>69.7</u>	695	69.7	694	69.8	4	<u>624</u>	<u>77.5</u>	622	77.8	625	77.4
462.libquantum	4	2116	39.2	2114	39.2	<u>2114</u>	<u>39.2</u>	1	<u>245</u>	<u>84.7</u>	244	84.9	246	84.4
464.h264ref	4	705	126	<u>705</u>	<u>126</u>	705	126	4	676	131	678	131	<u>677</u>	<u>131</u>
471.omnetpp	4	<u>708</u>	<u>35.3</u>	708	35.3	712	35.1	4	<u>680</u>	<u>36.8</u>	680	36.8	679	36.8
473.astar	4	647	43.4	<u>647</u>	<u>43.4</u>	646	43.4	4	<u>606</u>	<u>46.4</u>	606	46.3	605	46.4
483.xalancbmk	4	396	69.7	395	69.8	<u>396</u>	<u>69.8</u>	4	396	69.7	395	69.8	<u>396</u>	<u>69.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled
Hardware Prefetcher Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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