



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = Not Run

Acer Altos R720 (Intel Xeon X5460)

SPECint_rate_base2006 = 61.2

CPU2006 license: 97

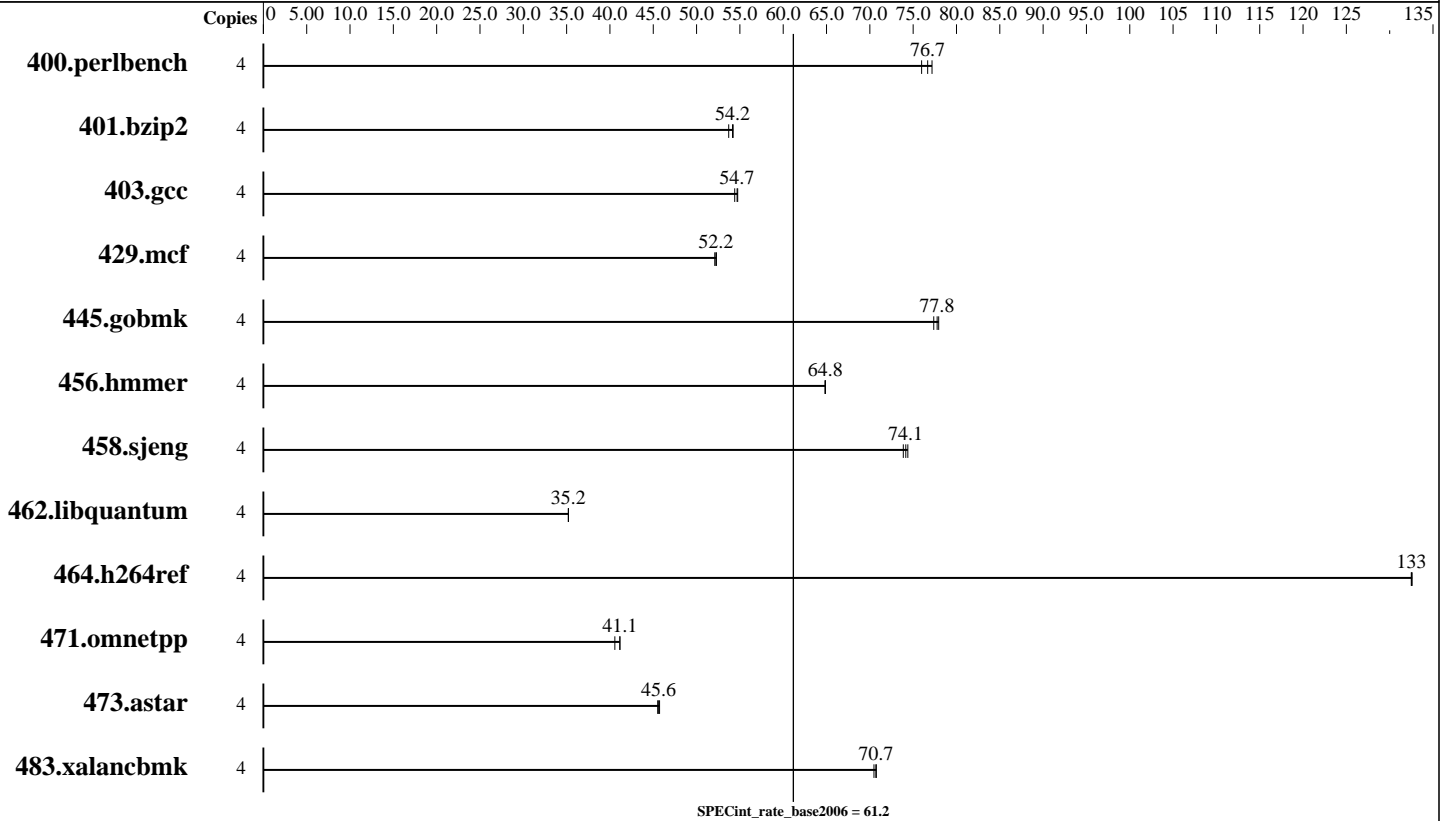
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Mar-2008

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3160
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10K RPM SAS HDD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86_64)
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	514	76.0	506	77.2	510	76.7							
401.bzip2	4	712	54.2	719	53.7	713	54.2							
403.gcc	4	588	54.7	592	54.4	589	54.7							
429.mcf	4	698	52.3	700	52.1	699	52.2							
445.gobmk	4	542	77.4	538	77.9	540	77.8							
456.hammer	4	575	64.9	576	64.8	576	64.8							
458.sjeng	4	651	74.4	655	73.9	653	74.1							
462.libquantum	4	2354	35.2	2354	35.2	2354	35.2							
464.h264ref	4	668	133	668	133	668	132							
471.omnetpp	4	608	41.1	608	41.2	616	40.6							
473.astar	4	617	45.5	615	45.6	614	45.7							
483.xalancbmk	4	390	70.7	390	70.7	392	70.5							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode

BIOS Settings

Hardware Prefetcher set to OFF

Adjacent Cache Line Prefetch set to OFF

All results but peak 462.libquantum used taskset to bind processes to cores.

"ulimit -s unlimited" was set for stacksize unlimited

OMP_NUM_THREADS set to number of cores

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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