



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp®_rate2006 = Not Run

IBM System x3500 (Intel Xeon E5405)

SPECfp_rate_base2006 = 58.0

CPU2006 license: 11

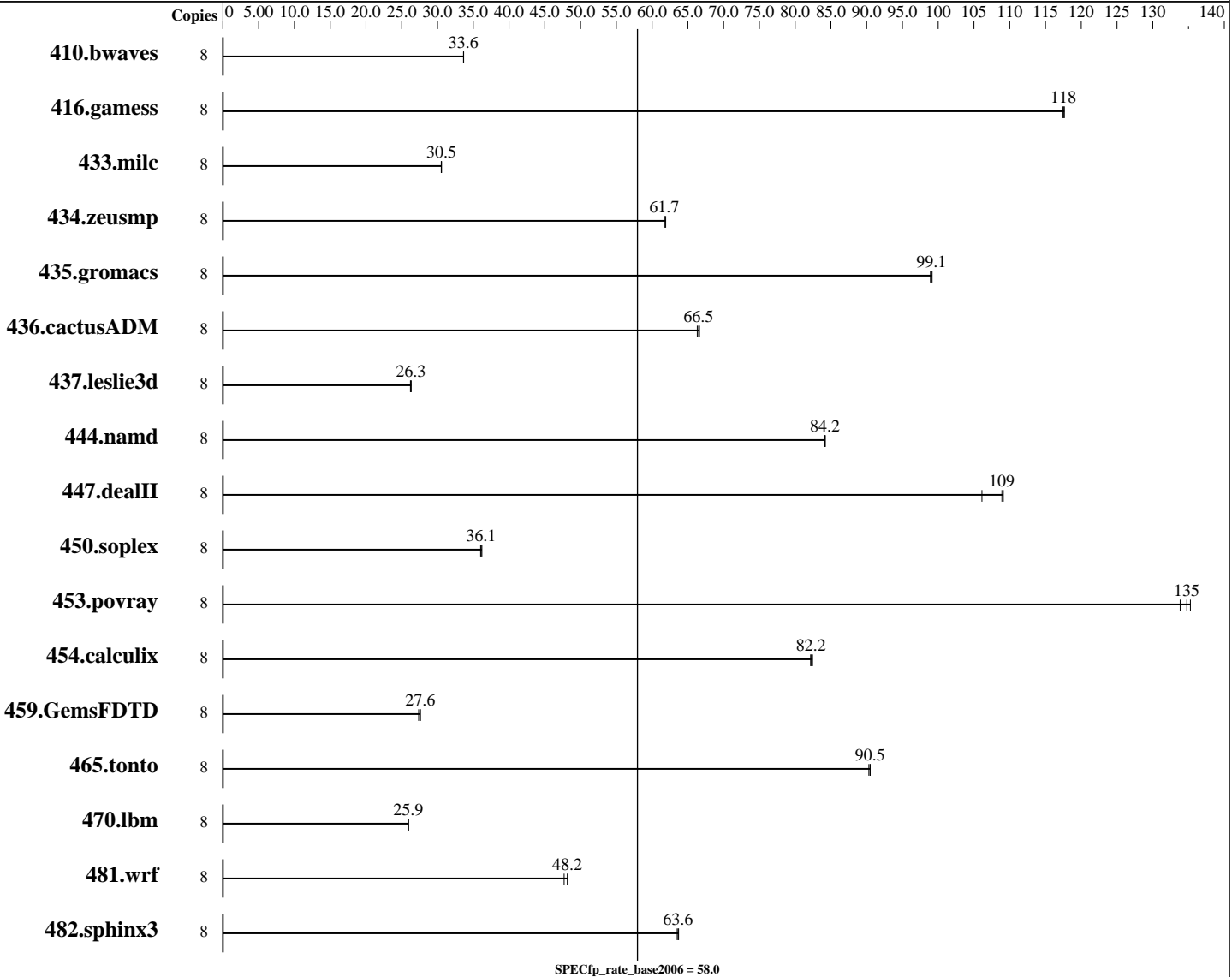
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM System x3500 (Intel Xeon E5405)

SPECfp_rate_base2006 = 58.0

CPU2006 license: 11

Test date: Mar-2008

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	3230	33.7	<u>3232</u>	<u>33.6</u>	3232	33.6									
416.gamess	8	1334	117	1331	118	<u>1333</u>	<u>118</u>									
433.milc	8	2406	30.5	2403	30.6	<u>2404</u>	<u>30.5</u>									
434.zeusmp	8	<u>1179</u>	<u>61.7</u>	1180	61.7	1176	61.9									
435.gromacs	8	<u>577</u>	<u>99.1</u>	577	98.9	576	99.2									
436.cactusADM	8	1435	66.6	<u>1438</u>	<u>66.5</u>	1442	66.3									
437.leslie3d	8	<u>2862</u>	<u>26.3</u>	2869	26.2	2857	26.3									
444.namd	8	762	84.2	762	84.2	<u>762</u>	<u>84.2</u>									
447.dealII	8	<u>840</u>	<u>109</u>	862	106	839	109									
450.soplex	8	1843	36.2	1852	36.0	<u>1847</u>	<u>36.1</u>									
453.povray	8	<u>316</u>	<u>135</u>	318	134	315	135									
454.calculix	8	801	82.4	803	82.2	<u>803</u>	<u>82.2</u>									
459.GemsFDTD	8	3102	27.4	3076	27.6	<u>3076</u>	<u>27.6</u>									
465.tonto	8	870	90.5	872	90.3	<u>870</u>	<u>90.5</u>									
470.lbm	8	4241	25.9	<u>4239</u>	<u>25.9</u>	4239	25.9									
481.wrf	8	1854	48.2	<u>1855</u>	<u>48.2</u>	1873	47.7									
482.sphinx3	8	2447	63.7	2455	63.5	<u>2450</u>	<u>63.6</u>									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
taskset utility used to bind CPU(s) to processes
This result is measured on an IBM System x 3500 Server. Note
that the IBM System x 3500 and IBM System x 3400 are electrically
equivalent.

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM System x3500 (Intel Xeon E5405)

SPECfp_rate_base2006 = 58.0

CPU2006 license: 11

Test date: Mar-2008

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM System x3500 (Intel Xeon E5405)

SPECfp_rate_base2006 = 58.0

CPU2006 license: 11

Test date: Mar-2008

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:44:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 April 2008.