



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 66.6

### CELSIUS M460, Intel Core 2 Quad Q9550

### SPECint\_rate\_base2006 = 61.2

CPU2006 license: 22

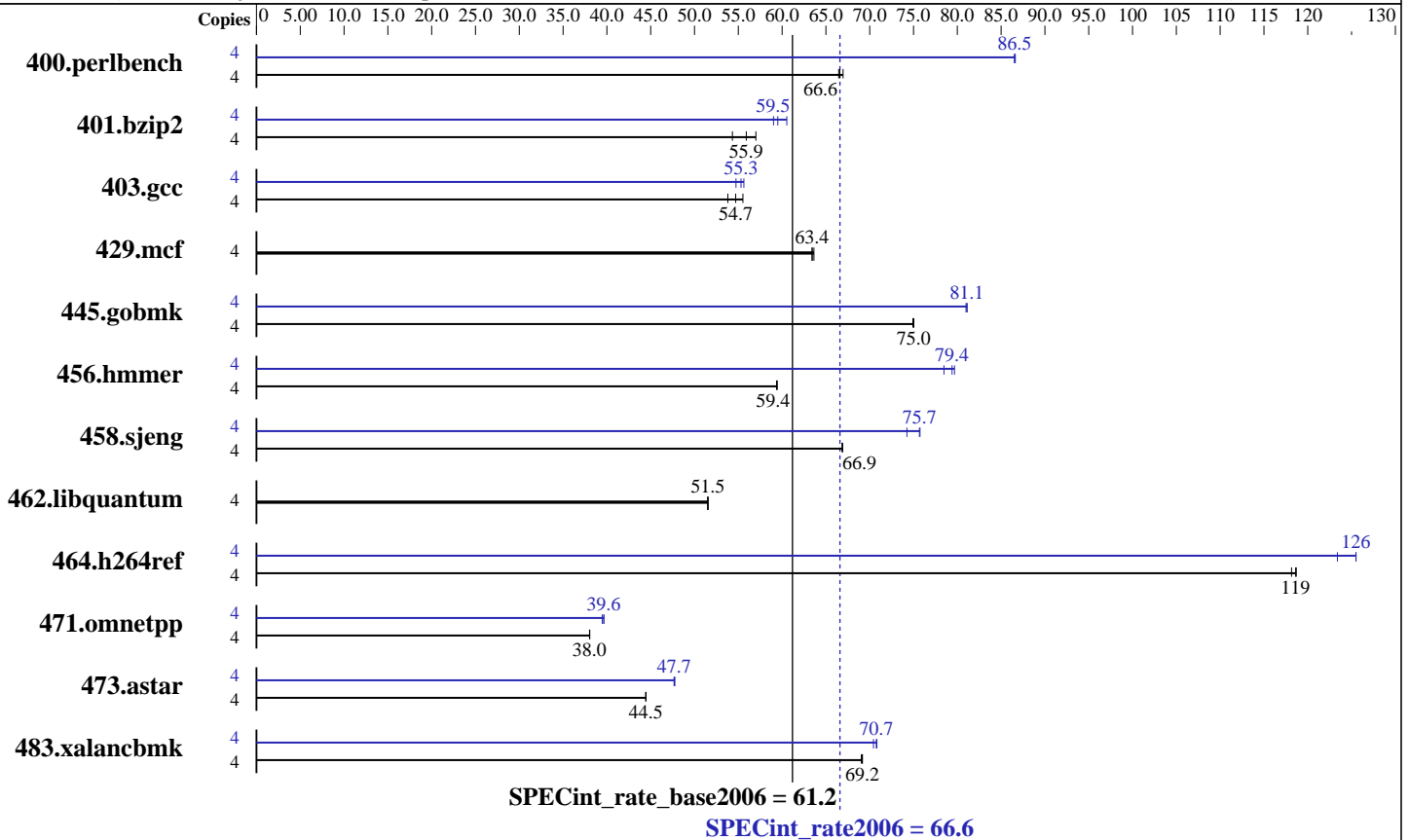
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Mar-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007



#### Hardware

CPU Name: Intel Core 2 Quad Q9550  
 CPU Characteristics: 2833  
 CPU MHz: 2833  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)  
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM  
 Other Hardware: None

#### Software

Operating System: Windows Vista64 Ultimate  
 Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20070913  
 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913  
 Microsoft Visual Studio 2005 with SP1 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint\_rate2006 = 66.6

CELSIUS M460, Intel Core 2 Quad Q9550

SPECint\_rate\_base2006 = 61.2

CPU2006 license: 22

Test date: Mar-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Mar-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007

## Results Table

| Benchmark      | Base   |            |             |            |             |             |             | Peak   |            |             |            |             |             |             |
|----------------|--------|------------|-------------|------------|-------------|-------------|-------------|--------|------------|-------------|------------|-------------|-------------|-------------|
|                | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds     | Ratio       | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds     | Ratio       |
| 400.perlbench  | 4      | 584        | 67.0        | 588        | 66.5        | <b>587</b>  | <b>66.6</b> | 4      | <b>452</b> | <b>86.5</b> | 451        | 86.6        | 452         | 86.5        |
| 401.bzip2      | 4      | 710        | 54.3        | 677        | 57.0        | <b>690</b>  | <b>55.9</b> | 4      | 638        | 60.5        | 654        | 59.0        | <b>649</b>  | <b>59.5</b> |
| 403.gcc        | 4      | 598        | 53.8        | 580        | 55.5        | <b>589</b>  | <b>54.7</b> | 4      | <b>582</b> | <b>55.3</b> | 588        | 54.7        | 579         | 55.6        |
| 429.mcf        | 4      | <b>575</b> | <b>63.4</b> | 575        | 63.4        | 573         | 63.6        | 4      | <b>575</b> | <b>63.4</b> | 575        | 63.4        | 573         | 63.6        |
| 445.gobmk      | 4      | 559        | 75.0        | <b>559</b> | <b>75.0</b> | 560         | 74.9        | 4      | 517        | 81.1        | <b>517</b> | <b>81.1</b> | 518         | 81.0        |
| 456.hmmmer     | 4      | 629        | 59.4        | 628        | 59.4        | <b>628</b>  | <b>59.4</b> | 4      | <b>470</b> | <b>79.4</b> | 468        | 79.7        | 476         | 78.5        |
| 458.sjeng      | 4      | 724        | 66.8        | 723        | 66.9        | <b>724</b>  | <b>66.9</b> | 4      | <b>640</b> | <b>75.7</b> | 639        | 75.7        | 652         | 74.2        |
| 462.libquantum | 4      | 1609       | 51.5        | 1608       | 51.6        | <b>1609</b> | <b>51.5</b> | 4      | 1609       | 51.5        | 1608       | 51.6        | <b>1609</b> | <b>51.5</b> |
| 464.h264ref    | 4      | 746        | 119         | 749        | 118         | <b>746</b>  | <b>119</b>  | 4      | <b>705</b> | <b>126</b>  | 705        | 126         | 717         | 123         |
| 471.omnetpp    | 4      | <b>657</b> | <b>38.0</b> | 658        | 38.0        | 657         | 38.0        | 4      | <b>631</b> | <b>39.6</b> | 630        | 39.7        | 633         | 39.5        |
| 473.astar      | 4      | 632        | 44.4        | <b>632</b> | <b>44.5</b> | 631         | 44.5        | 4      | 588        | 47.8        | <b>589</b> | <b>47.7</b> | 589         | 47.7        |
| 483.xalancbmk  | 4      | 399        | 69.2        | 400        | 69.0        | <b>399</b>  | <b>69.2</b> | 4      | <b>390</b> | <b>70.7</b> | 390        | 70.8        | 392         | 70.4        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

BIOS default settings have been used.

## General Notes

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com/>

## Base Compiler Invocation

C benchmarks:  
icl -Qvc8 -Qc99

C++ benchmarks:  
icl -Qvc8



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint\_rate2006 = 66.6

CELSIUS M460, Intel Core 2 Quad Q9550

SPECint\_rate\_base2006 = 61.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Mar-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
483.xalancbmk: -Qoption, cpp, --no\_wchar\_t\_keyword

## Base Optimization Flags

C benchmarks:  
-fast -Qvec-guard-write -F512000000  
C++ benchmarks:  
-fast -Qcxx\_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icl -Qvc8 -Qc99  
401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe  
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include  
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"  
456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe  
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include  
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"  
C++ benchmarks:  
icl -Qvc8

## Peak Portability Flags

401.bzip2: -DSPEC\_CPU\_P64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint\_rate2006 = 66.6

CELSIUS M460, Intel Core 2 Quad Q9550

SPECint\_rate\_base2006 = 61.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Mar-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007

## Peak Portability Flags (Continued)

403.gcc: -DSPEC\_CPU\_WIN32  
 456.hmmer: -DSPEC\_CPU\_P64  
 464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
 483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
 -Qprefetch -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qprefetch  
 -F512000000

403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qipo  
 -Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
 -Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
 -F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
 -Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
 -Qopt-ra-region-strategy=block -Qcxx\_features -F512000000  
 shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
 -Qopt-ra-region-strategy=routine -Qcxx\_features -F512000000  
 shlW32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
 -Qcxx\_features -F512000000 shlW32M.lib  
 -link -FORCE:MULTIPLE



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint\_rate2006 = 66.6

CELSIUS M460, Intel Core 2 Quad Q9550

SPECint\_rate\_base2006 = 61.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Mar-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090713.02.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090713.02.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.xml)

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 17:58:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 3 April 2008.