SPEC® CFP2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX300 S4, Intel Xeon E5420, 2.50 GHz

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 63.8
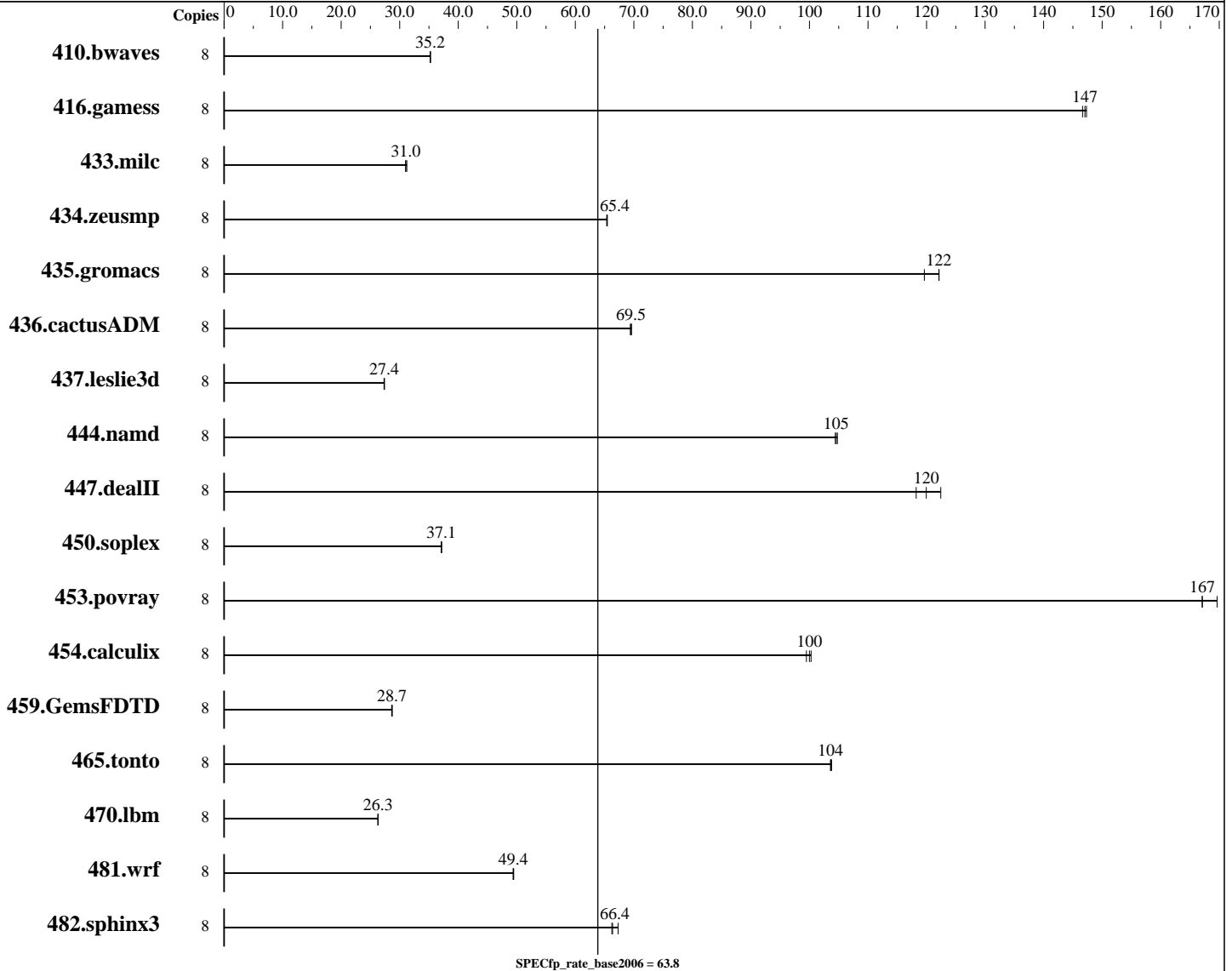
CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007

Copies		
410.bwaves	8	35.2
416.gamess	8	147
433.milc	8	31.0
434.zeusmp	8	65.4
435.gromacs	8	122
436.cactusADM	8	69.5
437.leslie3d	8	27.4
444.namd	8	105
447.dealII	8	120
450.soplex	8	37.1
453.povray	8	167
454.calculix	8	100
459.GemsFDTD	8	28.7
465.tonto	8	104
470.lbm	8	26.3
481.wrf	8	49.4
482.sphinx3	8	66.4

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Hardware

CPU Name: Intel Xeon E5420
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel: No
File System: ext2
System State: Multi-User Run Level 3
Base Pointers: 64-bit

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L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
Disk Subsystem:	1x SAS, 73 GB, 15000 rpm
Other Hardware:	None

Peak Pointers:	Not Applicable
Other Software:	binutils-2.17.50.0.5-0.1.x86_64

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3086	35.2	3085	35.2	**3086**	**35.2**							
416.gamess	8	1068	147	1063	147	**1065**	**147**							
433.milc	8	2353	31.2	**2366**	**31.0**	2368	31.0							
434.zeusmp	8	1112	65.5	1114	65.4	**1113**	**65.4**							
435.gromacs	8	**468**	**122**	478	120	468	122							
436.cactusADM	8	1374	69.6	1378	69.4	**1375**	**69.5**							
437.leslie3d	8	2752	27.3	**2747**	**27.4**	2746	27.4							
444.namd	8	615	104	612	105	**613**	**105**							
447.dealII	8	**763**	**120**	748	122	774	118							
450.soplex	8	**1798**	**37.1**	1795	37.2	1799	37.1							
453.povray	8	255	167	**255**	**167**	251	170							
454.calculix	8	658	100	663	99.5	**660**	**100**							
459.GemsFDTD	8	**2962**	**28.7**	2954	28.7	2965	28.6							
465.tonto	8	760	104	759	104	**760**	**104**							
470.lbm	8	4182	26.3	**4180**	**26.3**	4179	26.3							
481.wrf	8	1806	49.5	**1809**	**49.4**	1809	49.4							
482.sphinx3	8	2316	67.3	**2349**	**66.4**	2354	66.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

General Notes

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com

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Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

          410.bwaves: -DSPEC_CPU_LP64
          416.gamess: -DSPEC_CPU_LP64
           433.milc: -DSPEC_CPU_LP64
          434.zeusmp: -DSPEC_CPU_LP64
         435.gromacs: -DSPEC_CPU_LP64 -nofor_main
       436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
         437.leslie3d: -DSPEC_CPU_LP64
           444.namd: -DSPEC_CPU_LP64
          447.dealII: -DSPEC_CPU_LP64
          450.soplex: -DSPEC_CPU_LP64
          453.povray: -DSPEC_CPU_LP64
         454.calculix: -DSPEC_CPU_LP64 -nofor_main
        459.GemsFDTD: -DSPEC_CPU_LP64
           465.tonto: -DSPEC_CPU_LP64
           470.lbm: -DSPEC_CPU_LP64
            481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
         482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -fast

Fortran benchmarks:
    -fast

Benchmarks using both Fortran and C:
    -fast

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The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.20090713.xml

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For other inquiries, please contact webmaster@spec.org.

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