



SPEC® CINT2006 Result

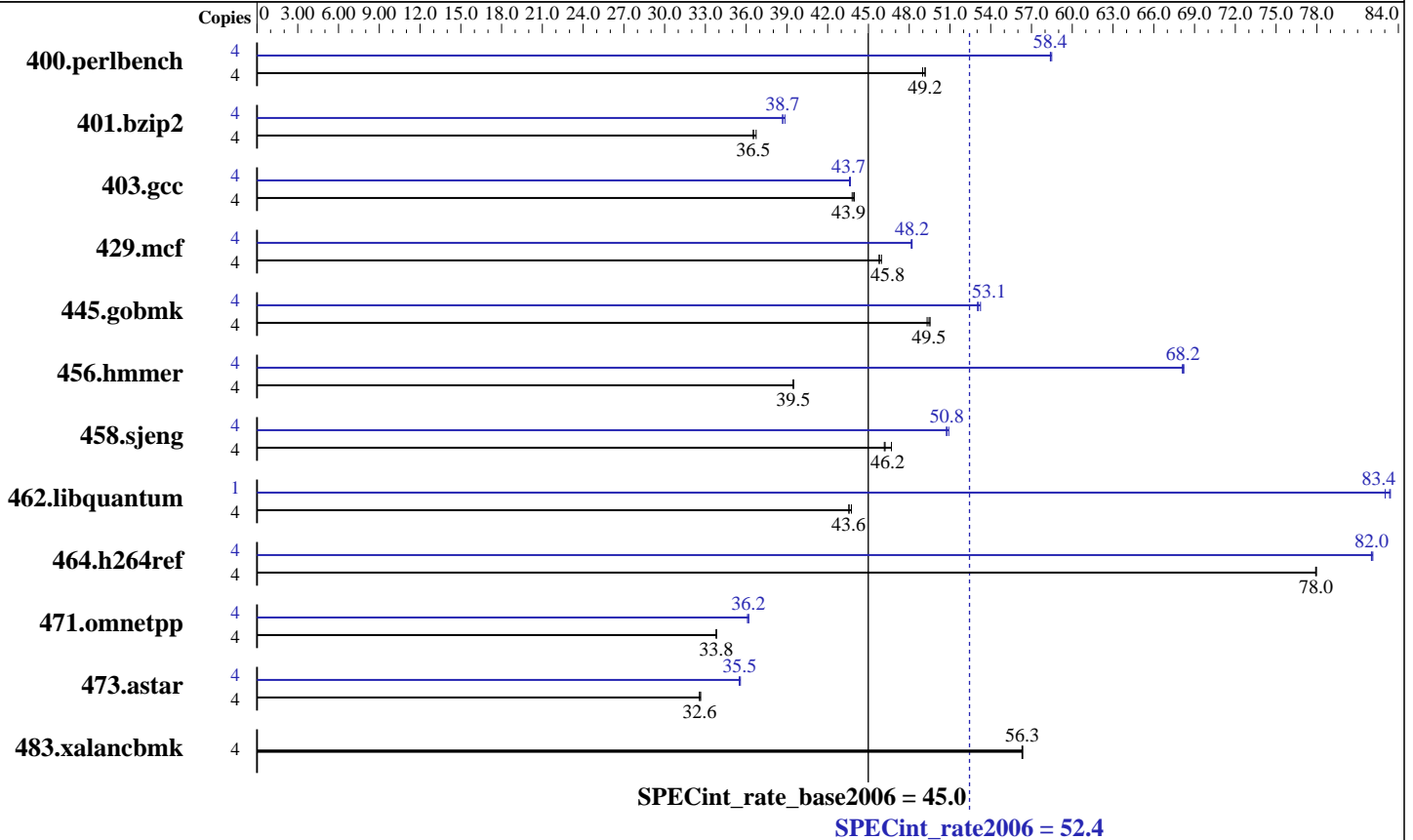
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Supermicro Motherboard X7DBT-INF

SPECint®_rate2006 = 52.4
SPECint_rate_base2006 = 45.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5205
 CPU Characteristics: Dual Core, 1.86GHz
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
 Disk Subsystem: Western Digital WD1600YS-01SHB1 160GB SATA II, 7200RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, kernel 2.6.16.46-0.12-default
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	798	49.0	795	49.2	<u>795</u>	<u>49.2</u>	4	669	58.4	668	58.5	<u>669</u>	<u>58.4</u>
401.bzip2	4	1051	36.7	1057	36.5	<u>1057</u>	<u>36.5</u>	4	993	38.9	<u>998</u>	<u>38.7</u>	998	38.7
403.gcc	4	735	43.8	<u>733</u>	<u>43.9</u>	732	44.0	4	<u>738</u>	<u>43.7</u>	737	43.7	739	43.6
429.mcf	4	794	46.0	797	45.8	<u>797</u>	<u>45.8</u>	4	757	48.2	<u>757</u>	<u>48.2</u>	758	48.1
445.gobmk	4	847	49.5	<u>848</u>	<u>49.5</u>	851	49.3	4	788	53.2	<u>790</u>	<u>53.1</u>	791	53.0
456.hammer	4	<u>945</u>	<u>39.5</u>	946	39.5	945	39.5	4	547	68.2	<u>548</u>	<u>68.2</u>	548	68.1
458.sjeng	4	1037	46.7	<u>1047</u>	<u>46.2</u>	1048	46.2	4	951	50.9	954	50.7	<u>953</u>	<u>50.8</u>
462.libquantum	4	<u>1902</u>	<u>43.6</u>	1903	43.6	1895	43.7	1	<u>249</u>	<u>83.4</u>	250	83.0	248	83.4
464.h264ref	4	1135	78.0	<u>1136</u>	<u>78.0</u>	1136	77.9	4	1078	82.1	1079	82.0	<u>1079</u>	<u>82.0</u>
471.omnetpp	4	739	33.8	<u>740</u>	<u>33.8</u>	740	33.8	4	<u>691</u>	<u>36.2</u>	693	36.1	691	36.2
473.astar	4	<u>862</u>	<u>32.6</u>	862	32.6	860	32.7	4	789	35.6	791	35.5	<u>791</u>	<u>35.5</u>
483.xalancbmk	4	<u>490</u>	<u>56.3</u>	490	56.3	490	56.3	4	<u>490</u>	<u>56.3</u>	490	56.3	490	56.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-808T-980 case,
To ensure system stability, a 550W (minimum) ATX power supply
[4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
<http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBT-INF.cfm>
The system bus runs at 1066 MHz
The "taskset" command was used to bind benchmark copies to cores
except for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 17:04:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 May 2008.