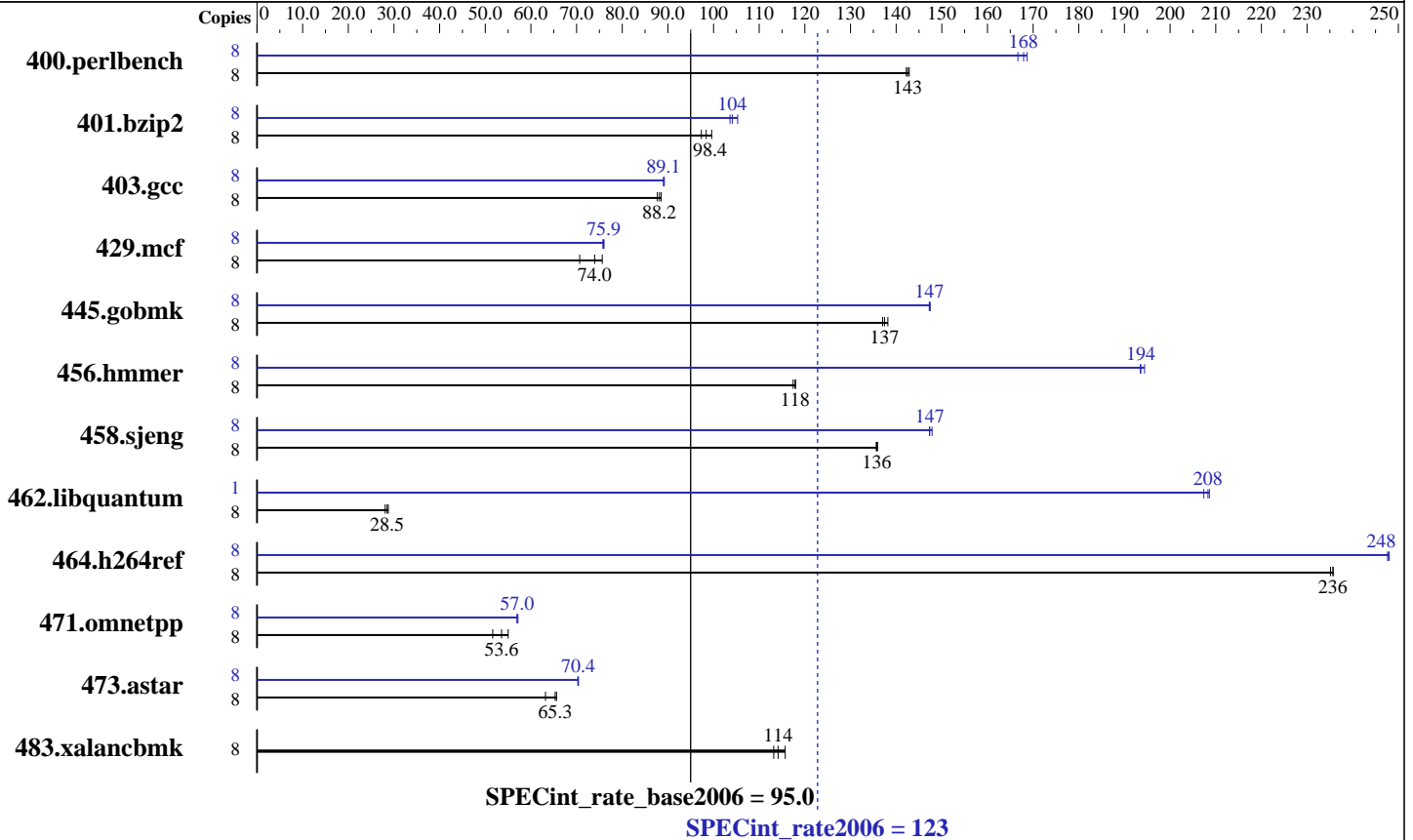# SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro
Motherboard X7DVA-8

SPECint®_rate2006 =　123

SPECint_rate_base2006 =　95.0

CPU2006 license: 001176　　　　　　　　　　　　　　　**Test date:** Nov-2007
Test sponsor: Supermicro　　　　　　　　　　　　　　**Hardware Availability:** Nov-2007
Tested by: Supermicro　　　　　　　　　　　　　　　**Software Availability:** Nov-2007

Benchmark results chart (Copies = 8 unless noted):

Benchmark	Peak	Base
400.perlbench	168	143
401.bzip2	104	98.4
403.gcc	89.1	88.2
429.mcf	75.9	74.0
445.gobmk	147	137
456.hmmer	194	118
458.sjeng	147	136
462.libquantum	208 (1 copy)	28.5
464.h264ref	248	236
471.omnetpp	57.0	53.6
473.astar	70.4	65.3
483.xalancbmk		114

SPECint_rate_base2006 = 95.0

SPECint_rate2006 = 123

Hardware

CPU Name: Intel Xeon E5440
CPU Characteristics: Quad Core, 2.83GHz
CPU MHz: 2833
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 12 GB (6 X 2GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem: Western DigitalD WD1600YS-01SHB1 160GB SATA II, 7200RPM
Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, kernel 2.6.16.46-0.12-default
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DVA-8

SPECint_rate2006 =   123

SPECint_rate_base2006 = 95.0

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**548**	**143**	550	142	547	143	8	469	167	**465**	**168**	463	169
401.bzip2	8	793	97.3	**785**	**98.4**	775	99.6	8	745	104	**742**	**104**	733	105
403.gcc	8	734	87.7	**731**	**88.2**	727	88.5	8	**723**	**89.1**	723	89.0	722	89.2
429.mcf	8	1032	70.7	**986**	**74.0**	965	75.6	8	963	75.8	960	76.0	**961**	**75.9**
445.gobmk	8	**611**	**137**	612	137	607	138	8	569	147	570	147	**570**	**147**
456.hmmer	8	636	117	633	118	**633**	**118**	8	384	194	386	193	**386**	**194**
458.sjeng	8	714	136	712	136	**713**	**136**	8	657	147	**657**	**147**	655	148
462.libquantum	8	5905	28.1	**5815**	**28.5**	5765	28.8	1	99.9	207	**99.5**	**208**	99.3	209
464.h264ref	8	751	236	753	235	**751**	**236**	8	**714**	**248**	715	248	714	248
471.omnetpp	8	968	51.6	**933**	**53.6**	909	55.0	8	875	57.2	878	57.0	**877**	**57.0**
473.astar	8	889	63.2	**860**	**65.3**	856	65.6	8	798	70.4	**798**	**70.4**	799	70.3
483.xalancbmk	8	488	113	**484**	**114**	477	116	8	488	113	**484**	**114**	477	116

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-811S-520 case,
To ensure system stability, a 500W (minimum) ATX power supply
[4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon1333/5000V/X7DVA-8.cfm
The system bus runs at 1333 MHz
The "taskset" command was used to bind benchmark copies to cores
except for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
  462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro
Motherboard X7DVA-8

SPECint_rate2006 =   123

SPECint_rate_base2006 = 95.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DVA-8

SPECint_rate2006 = 123

SPECint_rate_base2006 = 95.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DVA-8

SPECint_rate2006 = 123

SPECint_rate_base2006 = 95.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 16:58:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 May 2008.