



SPEC® CFP2006 Result

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NEC Corporation

Express5800/120Bb-6
(Intel Xeon processor 5160)

SPECfp_rate2006 = 44.0

SPECfp_rate_base2006 = 42.6

CPU2006 license: 9006

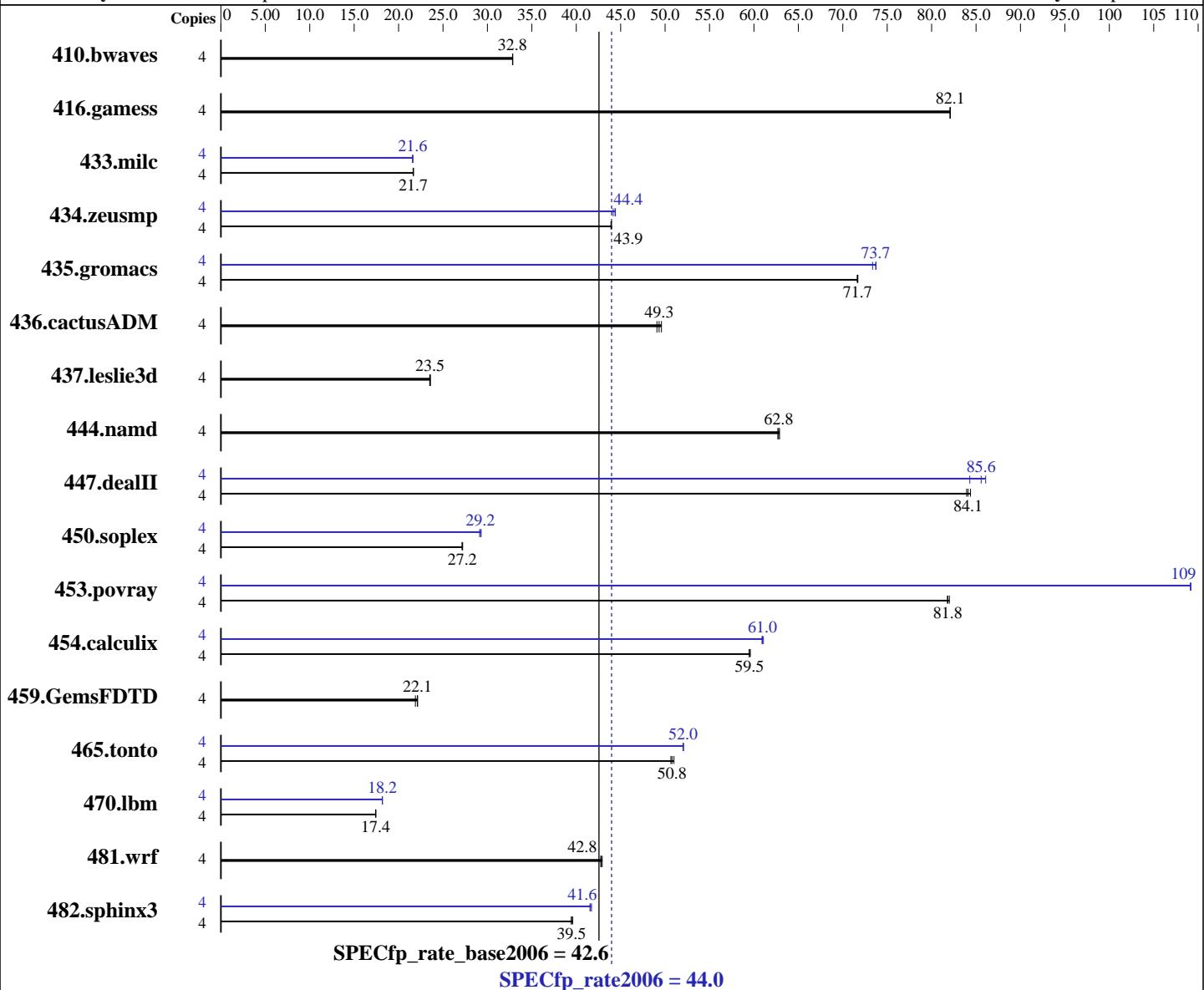
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Oct-2007

Hardware Availability: May-2007

Software Availability: Apr-2007



Hardware

CPU Name: Intel Xeon 5160
CPU Characteristics: 3.00 GHz, 4 MB L2, 1333 MHz bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070320, Package-ID: l_cc_c_9.1.049
Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070320, Package ID: l_fc_c_9.1.045
Auto Parallel: No
File System: ext2

Continued on next page

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x146.5 GB SAS, 10000RPM
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1656	32.8	1654	32.9	1656	32.8	4	1656	32.8	1654	32.9	1656	32.8
416.gamess	4	954	82.1	954	82.1	954	82.1	4	954	82.1	954	82.1	954	82.1
433.milc	4	1695	21.7	1697	21.6	1694	21.7	4	1700	21.6	1702	21.6	1700	21.6
434.zeusmp	4	828	44.0	829	43.9	829	43.9	4	824	44.2	820	44.4	820	44.4
435.gromacs	4	399	71.6	398	71.7	399	71.7	4	389	73.4	387	73.8	388	73.7
436.cactusADM	4	974	49.1	964	49.6	969	49.3	4	974	49.1	964	49.6	969	49.3
437.leslie3d	4	1594	23.6	1600	23.5	1597	23.5	4	1594	23.6	1600	23.5	1597	23.5
444.namd	4	511	62.8	512	62.7	510	62.9	4	511	62.8	512	62.7	510	62.9
447.dealII	4	542	84.4	545	83.9	544	84.1	4	543	84.3	535	85.6	532	86.1
450.soplex	4	1229	27.2	1228	27.2	1226	27.2	4	1142	29.2	1145	29.1	1139	29.3
453.povray	4	260	82.0	260	81.8	260	81.8	4	195	109	195	109	195	109
454.calculix	4	554	59.6	555	59.5	555	59.5	4	542	60.9	540	61.1	541	61.0
459.GemsFDTD	4	1918	22.1	1918	22.1	1938	21.9	4	1918	22.1	1918	22.1	1938	21.9
465.tonto	4	777	50.7	772	51.0	775	50.8	4	757	52.0	756	52.1	757	52.0
470.lbm	4	3154	17.4	3153	17.4	3154	17.4	4	3024	18.2	3024	18.2	3024	18.2
481.wrf	4	1044	42.8	1045	42.8	1041	42.9	4	1044	42.8	1045	42.8	1041	42.9
482.sphinx3	4	1976	39.5	1976	39.5	1969	39.6	4	1877	41.5	1869	41.7	1874	41.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1333 MHz
All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



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Peak Compiler Invocation

C benchmarks:

```
/opt/intel/cc/9.1.049/bin/icc -I/opt/intel/cc/9.1.049/include  
-L/opt/intel/cc/9.1.049/lib
```

C++ benchmarks (except as noted below):

```
icpc
```

```
450.soplex: /opt/intel/cc/9.1.049/bin/icpc  
-I/opt/intel/cc/9.1.049/include -L/opt/intel/cc/9.1.049/lib
```

Fortran benchmarks (except as noted below):

```
ifort
```

```
434.zeusmp: /opt/intel/fc/9.1.045/bin/ifort  
-I/opt/intel/fc/9.1.045/include -L/opt/intel/fc/9.1.045/lib
```

Benchmarks using both Fortran and C:

```
icc ifort
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast
```

```
470.lbm: Same as 433.milc
```

```
482.sphinx3: -fast
```

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:

435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast

436.cactusADM: basepeak = yes

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic91-FP-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic91-FP-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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