



# SPEC® CINT2006 Result

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## Sun Microsystems Sun Fire X4150

**SPECint®2006 = 22.0**  
**SPECint\_base2006 = 19.0**

CPU2006 license: 6

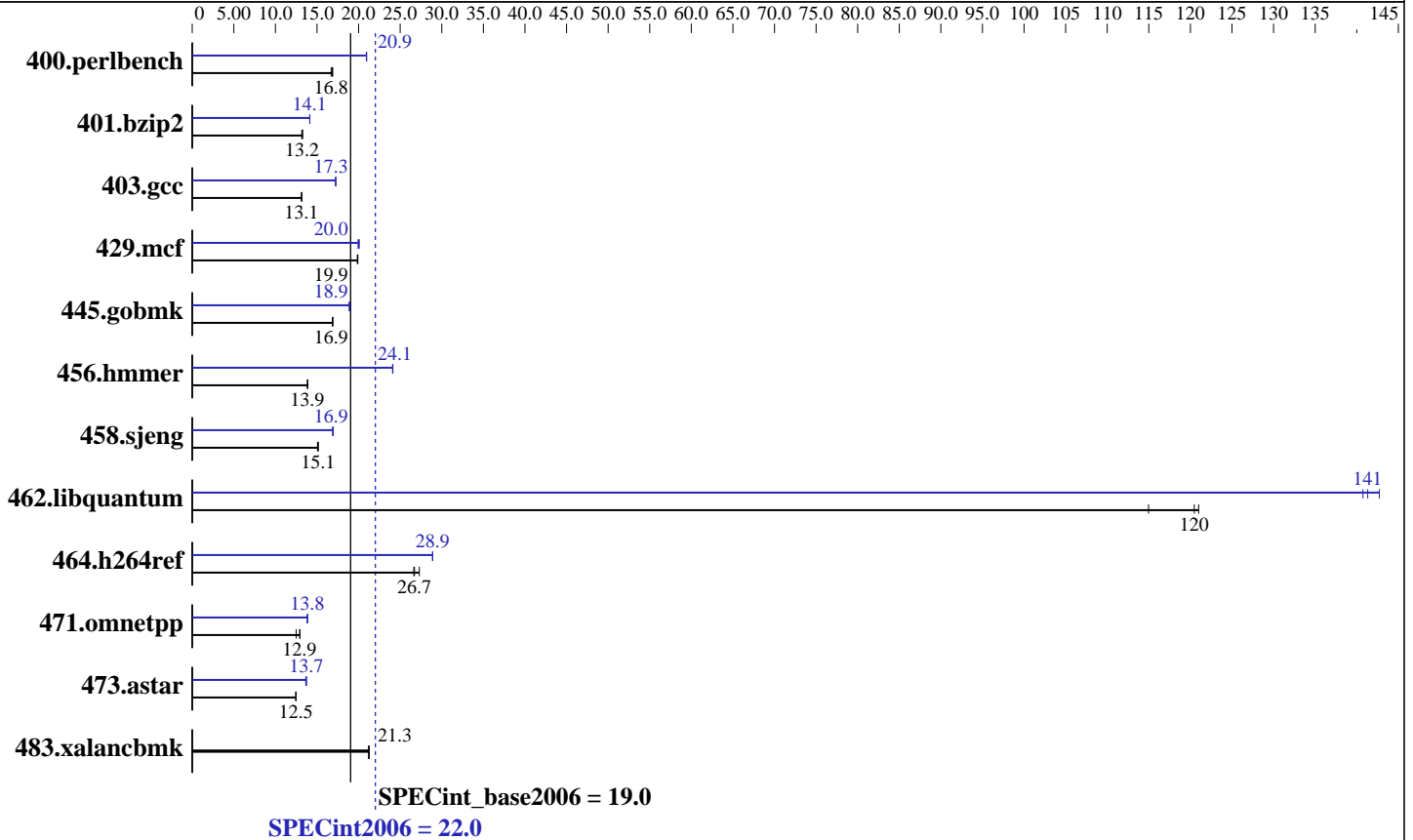
Test sponsor: Sun Microsystems

Tested by: Sun Microsystems

Test date: Oct-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: Quad Core, 2.667 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 (order by number of chips)  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2GB DDR2 PC2-5300F 2rank CAS 5-5-5 with ECC)  
 Disk Subsystem: SAS, 72 GB, 10K RPM  
 Other Hardware: None

### Software

Operating System: SUSE LINUX Enterprise Server 10 SP1 for x86\_64  
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070824  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	585	16.7	<b>580</b>	<b>16.8</b>	579	16.9	<b>466</b>	<b>20.9</b>	466	21.0	467	20.9
401.bzip2	<b>729</b>	<b>13.2</b>	727	13.3	733	13.2	682	14.1	683	14.1	<b>683</b>	<b>14.1</b>
403.gcc	613	13.1	<b>612</b>	<b>13.1</b>	611	13.2	466	17.3	467	17.2	<b>467</b>	<b>17.3</b>
429.mcf	<b>459</b>	<b>19.9</b>	459	19.9	458	19.9	457	19.9	<b>456</b>	<b>20.0</b>	454	20.1
445.gobmk	622	16.9	<b>621</b>	<b>16.9</b>	621	16.9	555	18.9	555	18.9	<b>555</b>	<b>18.9</b>
456.hmmer	<b>673</b>	<b>13.9</b>	673	13.9	672	13.9	<b>387</b>	<b>24.1</b>	387	24.1	387	24.1
458.sjeng	798	15.2	<b>800</b>	<b>15.1</b>	804	15.1	717	16.9	714	17.0	<b>716</b>	<b>16.9</b>
462.libquantum	180	115	171	121	<b>172</b>	<b>120</b>	145	143	<b>147</b>	<b>141</b>	147	141
464.h264ref	811	27.3	831	26.6	<b>828</b>	<b>26.7</b>	<b>766</b>	<b>28.9</b>	766	28.9	765	28.9
471.omnetpp	483	12.9	<b>484</b>	<b>12.9</b>	500	12.5	451	13.9	452	13.8	<b>451</b>	<b>13.8</b>
473.astar	564	12.4	<b>563</b>	<b>12.5</b>	562	12.5	<b>512</b>	<b>13.7</b>	514	13.6	511	13.7
483.xalancbmk	<b>325</b>	<b>21.3</b>	324	21.3	326	21.2	<b>325</b>	<b>21.3</b>	324	21.3	326	21.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited  
OMP\_NUM\_THREADS set to 8  
KMP\_AFFINITY set to physical,0

## Platform Notes

Default BIOS configuration was used.

## General Notes

All benchmarks were compiled in 32-bit mode except 401.bzip2 and 456.hmmer for peak were compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -vec-guard-write -parallel -par-runtime-control  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include  
456.hmmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include  
C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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