SPEC® CFP2006 Result

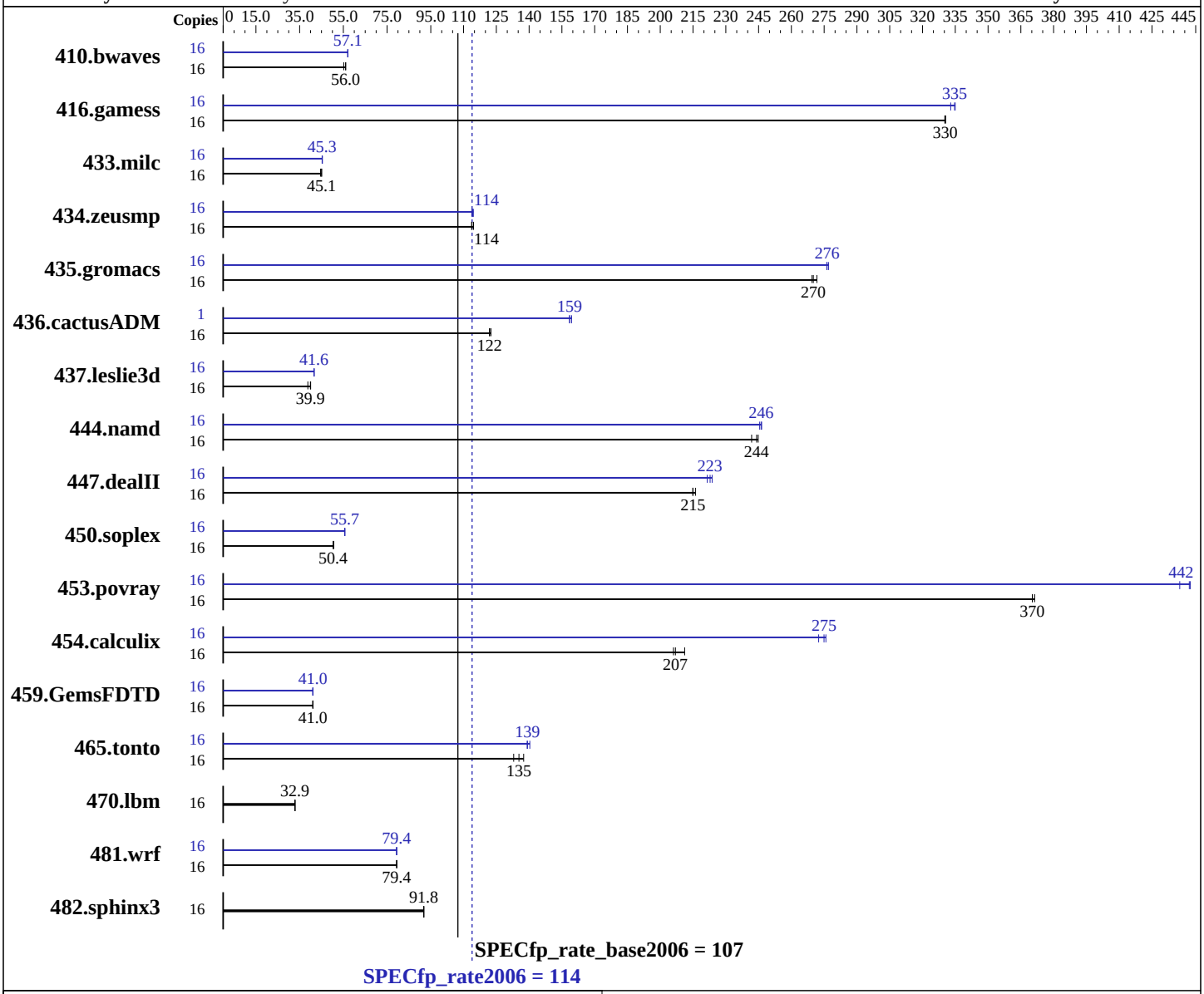
Sun Microsystems
Sun Fire X4450

SPECfp®_rate2006 = 114

SPECfp_rate_base2006 = 107

CPU2006 license: 6
Test sponsor: Sun Microsystems
Tested by: Sun Microsystems

Test date: Oct-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X7350
CPU Characteristics:	Quad Core, 2.93 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	2,4 (order by # of chips)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 for x86_64
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

Continued on next page

SPEC CFP2006 Result

Sun Microsystems
Sun Fire X4450

SPECfp_rate2006 = 114

SPECfp_rate_base2006 = 107

CPU2006 license: 6
Test sponsor: Sun Microsystems
Tested by: Sun Microsystems

Test date: Oct-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2GB DDR2 PC2-5300F 2rank CAS 5-5-5 with ECC)
Disk Subsystem: SAS, 73 GB, 10K RPM
Other Hardware: None

Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	3939	55.2	**3880**	**56.0**	3877	56.1	16	3817	57.0	**3809**	**57.1**	3808	57.1
416.gamess	16	949	330	948	331	**948**	**330**	16	941	333	**936**	**335**	935	335
433.milc	16	3296	44.6	3260	45.1	**3260**	**45.1**	16	3244	45.3	3233	45.4	**3241**	**45.3**
434.zeusmp	16	1281	114	1271	115	**1278**	**114**	16	1280	114	**1275**	**114**	1272	114
435.gromacs	16	420	272	**423**	**270**	424	269	16	**413**	**276**	412	277	414	276
436.cactusADM	16	1560	123	**1568**	**122**	1570	122	1	**75.4**	**159**	75.4	159	75.0	159
437.leslie3d	16	3881	38.8	3756	40.0	**3767**	**39.9**	16	3617	41.6	3613	41.6	**3614**	**41.6**
444.namd	16	524	245	**526**	**244**	531	242	16	523	246	**521**	**246**	521	246
447.dealII	16	847	216	852	215	**851**	**215**	16	818	224	826	221	**822**	**223**
450.soplex	16	2652	50.3	2637	50.6	**2648**	**50.4**	16	2401	55.6	2394	55.7	**2398**	**55.7**
453.povray	16	230	370	229	371	**230**	**370**	16	192	443	**193**	**442**	194	438
454.calculix	16	625	211	641	206	**638**	**207**	16	484	272	**480**	**275**	479	276
459.GemsFDTD	16	4130	41.1	4138	41.0	**4137**	**41.0**	16	4133	41.1	**4137**	**41.0**	4137	41.0
465.tonto	16	**1163**	**135**	1145	138	1184	133	16	1132	139	**1131**	**139**	1122	140
470.lbm	16	6684	32.9	**6688**	**32.9**	6691	32.9	16	6684	32.9	**6688**	**32.9**	6691	32.9
481.wrf	16	**2251**	**79.4**	2250	79.4	2251	79.4	16	**2252**	**79.4**	2252	79.4	2251	79.4
482.sphinx3	16	3396	91.8	3398	91.8	**3396**	**91.8**	16	3396	91.8	3398	91.8	**3396**	**91.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Processes were bound to cores using "submit" and "taskset".

'ulimit -s unlimited' was used to set the stacksize to unlimited
OMP_NUM_THREADS set to 16
KMP_STACKSIZE set to 64M
KMP_AFFINITY set to physical,0
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Disable; Adjacent Sector Prefetch = Disable
```

General Notes

```
All benchmarks were compiled in 64-bit mode except 437.leslie3d and
450.soplex for peak were compiled in 32-bit mode
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:
`-fast`

Benchmarks using both Fortran and C:
`-fast`

Peak Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icpc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/ifort -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

447.dealII: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias -scalar-rep-`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `-fast -prefetch`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

437.leslie3d: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -opt-malloc-options=3`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -prefetch`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -prefetch -parallel -auto-ilp32`

454.calculix: `-fast -unroll-aggressive -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: `-fast -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:11:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 30 October 2007.