SPEC® CINT2006 Result
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ACTION S.A.

ACTINA SOLAR 202 X2 (Intel Xeon processor E5310, 1.60GHz)

$SPECint^®\_rate2006 =$ 59.2
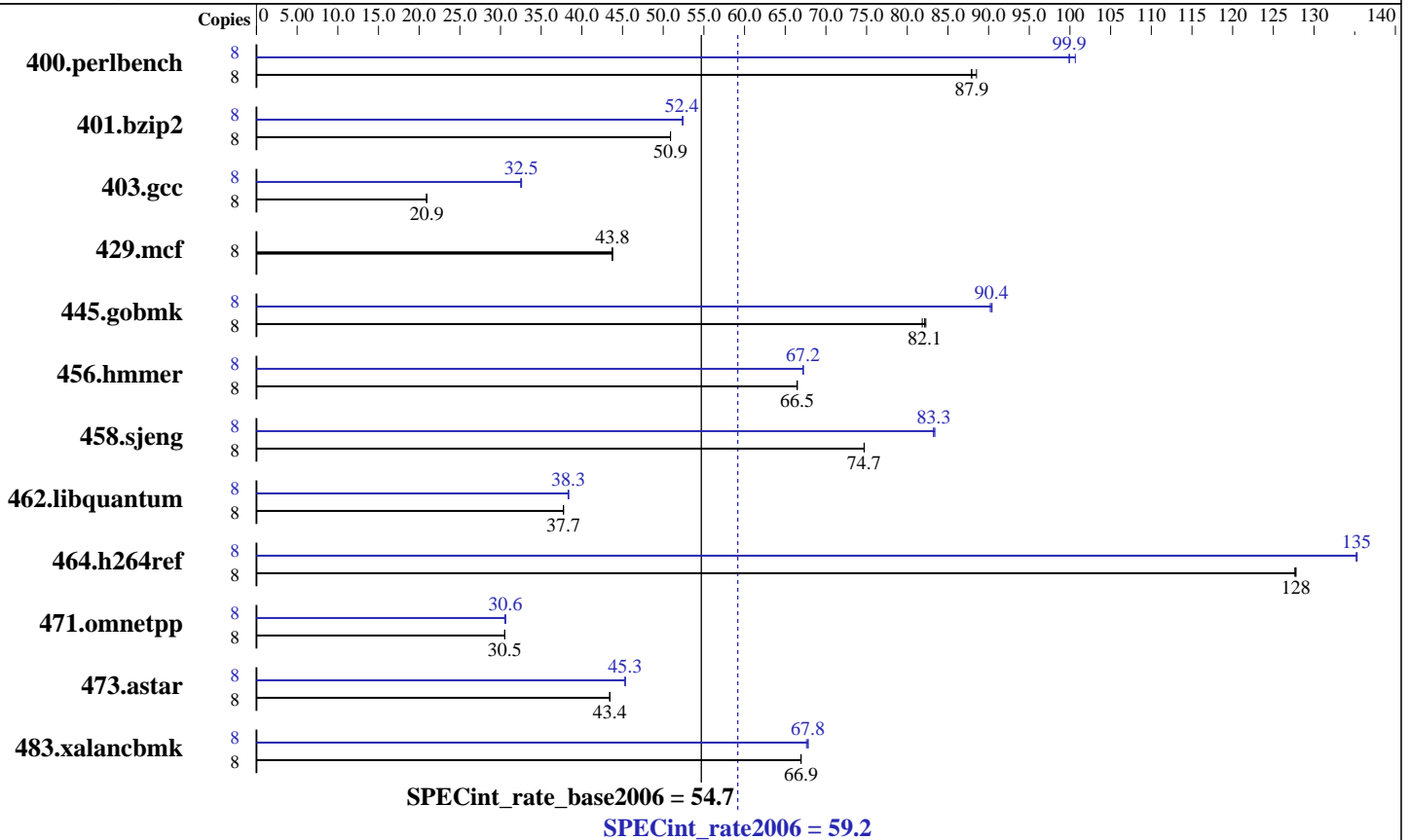
SPECint_rate_base2006 = 54.7

CPU2006 license: 3388
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Aug-2007
Hardware Availability: Jun-2007
Software Availability: Jun-2007

Copies 0 5.00 10.0 15.0 20.0 25.0 30.0 35.0 40.0 45.0 50.0 55.0 60.0 65.0 70.0 75.0 80.0 85.0 90.0 95.0 100 105 110 115 120 125 130 140

Benchmark	Copies	Result
400.perlbench	8	99.9
	8	87.9
401.bzip2	8	52.4
	8	50.9
403.gcc	8	32.5
	8	20.9
429.mcf	8	43.8
445.gobmk	8	90.4
	8	82.1
456.hmmer	8	67.2
	8	66.5
458.sjeng	8	83.3
	8	74.7
462.libquantum	8	38.3
	8	37.7
464.h264ref	8	135
	8	128
471.omnetpp	8	30.6
	8	30.5
473.astar	8	45.3
	8	43.4
483.xalancbmk	8	67.8
	8	66.9

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SPECint_rate2006 = 59.2

Hardware

CPU Name: Intel Xeon E5310
CPU Characteristics: 1066 MHz system bus
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem: 80 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Windows 2003 Server Enterprise Edition Service Pack 1
Compiler: Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	889	87.9	883	88.5	**889**	**87.9**	8	782	99.9	777	101	**782**	**99.9**
401.bzip2	8	1517	50.9	**1517**	**50.9**	1517	50.9	8	**1474**	**52.4**	1474	52.4	1473	52.4
403.gcc	8	3078	20.9	**3078**	**20.9**	3076	20.9	8	1976	32.6	**1979**	**32.5**	1982	32.5
429.mcf	8	**1667**	**43.8**	1667	43.8	1668	43.7	8	**1667**	**43.8**	1667	43.8	1668	43.7
445.gobmk	8	**1022**	**82.1**	1025	81.8	1020	82.3	8	930	90.2	928	90.4	**929**	**90.4**
456.hmmer	8	1123	66.5	**1123**	**66.5**	1123	66.5	8	1111	67.2	1111	67.2	**1111**	**67.2**
458.sjeng	8	**1296**	**74.7**	1295	74.7	1296	74.7	8	**1163**	**83.3**	1161	83.4	1163	83.2
462.libquantum	8	4393	37.7	4392	37.7	**4393**	**37.7**	8	4326	38.3	4318	38.4	**4323**	**38.3**
464.h264ref	8	1386	128	1387	128	**1386**	**128**	8	1308	135	**1309**	**135**	1309	135
471.omnetpp	8	**1638**	**30.5**	1639	30.5	1638	30.5	8	**1633**	**30.6**	1633	30.6	1637	30.5
473.astar	8	**1293**	**43.4**	1293	43.4	1293	43.4	8	1240	45.3	**1239**	**45.3**	1239	45.3
483.xalancbmk	8	825	66.9	**825**	**66.9**	824	67.0	8	814	67.8	**814**	**67.8**	816	67.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
    Product description located as of 8/2007:
    http://www.actina.pl
    Binaries were built on Windows XP Professional SP2
    start command was used to bind processes to CPUs
```

Base Compiler Invocation

C benchmarks:
```
    icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
    icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

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Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
          -Qprefetch /F512000000 shlw32m.lib
                    -link /FORCE:MULTIPLE

  401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
          shlw32m.lib            -link /FORCE:MULTIPLE

  403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
                    -link /FORCE:MULTIPLE

  429.mcf: basepeak = yes
```

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Peak Optimization Flags (Continued)

```
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
           -Qprec_div- -Qansi-alias /F512000000
                      -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
           -Qansi-alias /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           /F512000000 shlw32m.lib           -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
           shlw32m.lib           -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer
```

C++ benchmarks:
```
    -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
    -Qcxx_features /F512000000 shlw32m.lib
                   -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
      403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
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