



SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor E5345, 2.33 GHz)

SPECint®_rate2006 = 102

SPECint_rate_base2006 = 85.7

CPU2006 license: 13

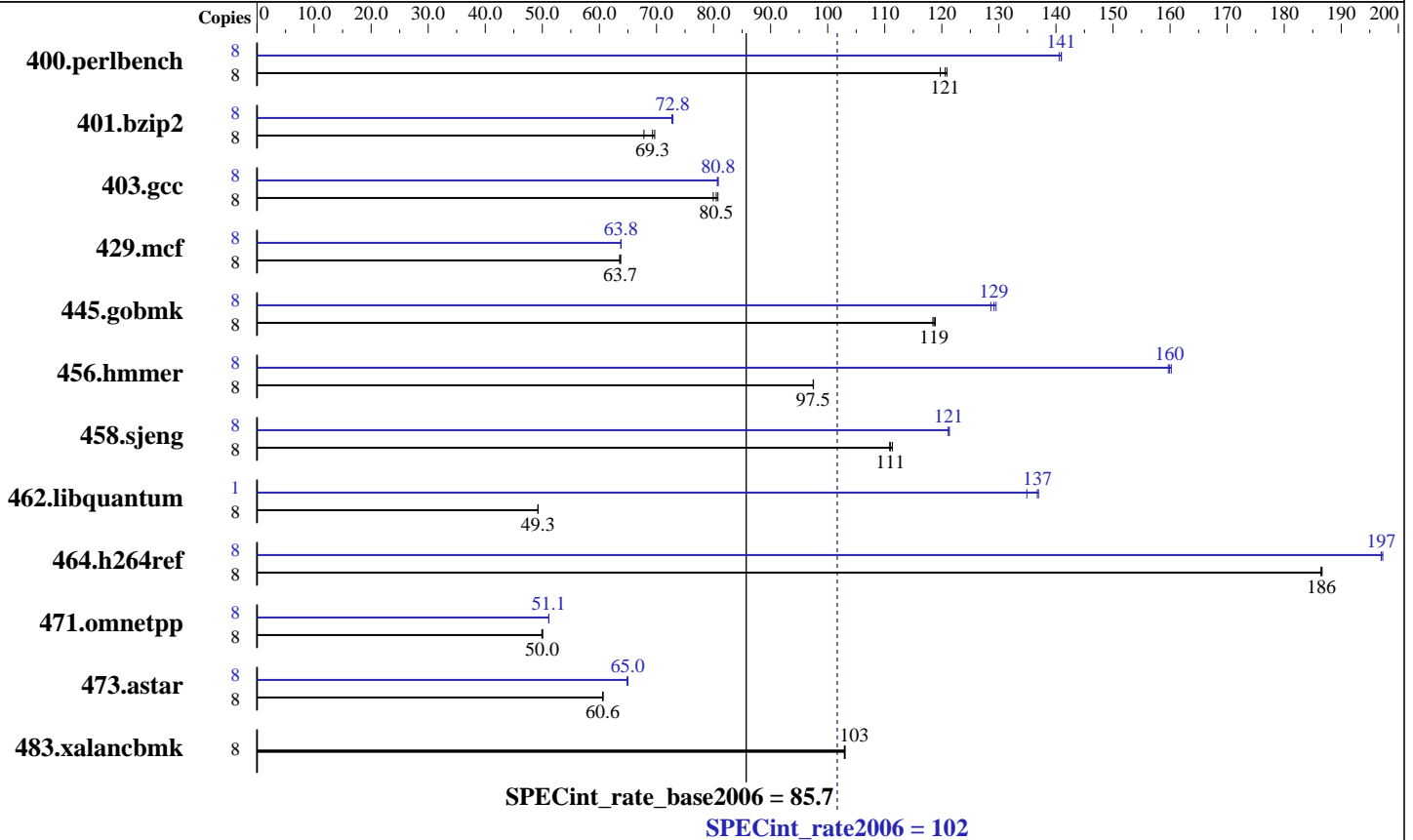
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Jul-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: Quad Core, 2.33 GHz
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1, Kernel linux-cbmg 2.6.16.43-0.5-smp for x86_64
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	648	121	647	121	653	120	8	555	141	556	141	554	141
401.bzip2	8	1108	69.7	1139	67.8	1114	69.3	8	1060	72.8	1062	72.7	1059	72.9
403.gcc	8	805	80.0	797	80.8	800	80.5	8	798	80.7	797	80.8	797	80.8
429.mcf	8	1145	63.7	1148	63.5	1146	63.7	8	1144	63.8	1144	63.8	1144	63.8
445.gobmk	8	706	119	707	119	709	118	8	650	129	648	130	653	129
456.hmmer	8	765	97.5	766	97.5	766	97.5	8	467	160	467	160	466	160
458.sjeng	8	873	111	869	111	872	111	8	799	121	799	121	798	121
462.libquantum	8	3365	49.3	3367	49.2	3365	49.3	1	154	135	152	137	151	137
464.h264ref	8	949	186	949	187	949	186	8	899	197	899	197	897	197
471.omnetpp	8	999	50.1	1002	49.9	1000	50.0	8	978	51.1	978	51.1	978	51.1
473.astar	8	927	60.6	926	60.6	927	60.6	8	866	64.9	864	65.0	864	65.0
483.xalanbmk	8	536	103	536	103	536	103	8	536	103	536	103	536	103

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Disabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.xml>

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