



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor X5365, 3.00 GHz)

**SPECint®\_rate2006 = 116**

**SPECint\_rate\_base2006 = 97.9**

CPU2006 license: 13

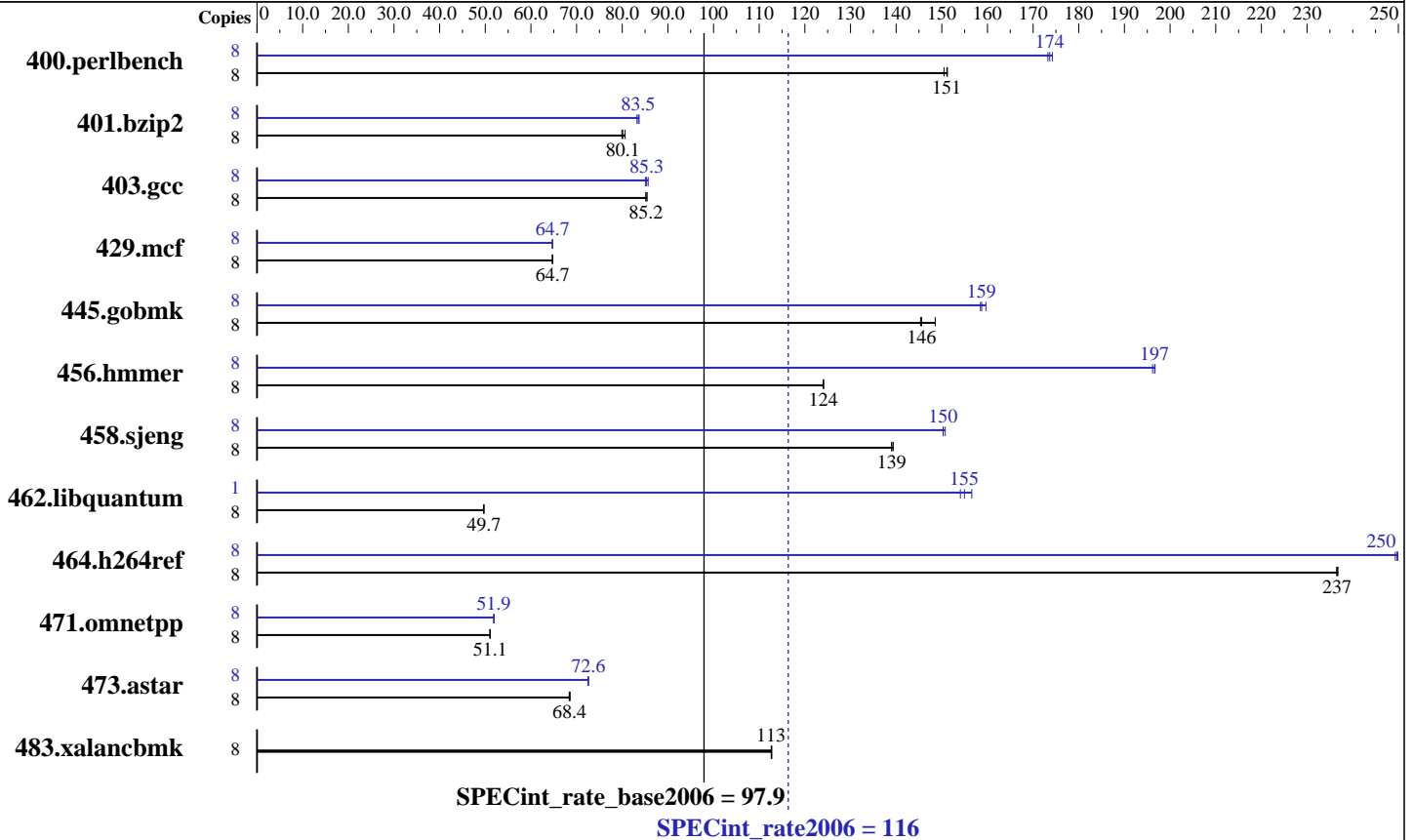
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5365  
 CPU Characteristics: Quad Core, 3.0 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1, Kernel linux-cbmg 2.6.16.43-0.5-smp for x86\_64  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor X5365,  
3.00 GHz)

SPECint\_rate2006 = 116

SPECint\_rate\_base2006 = 97.9

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>517</b>	<b>151</b>	517	151	519	150	8	451	173	449	174	<b>450</b>	<b>174</b>
401.bzip2	8	<b>964</b>	<b>80.1</b>	957	80.6	966	79.9	8	<b>924</b>	<b>83.5</b>	922	83.7	928	83.2
403.gcc	8	754	85.5	<b>756</b>	<b>85.2</b>	756	85.2	8	757	85.1	<b>755</b>	<b>85.3</b>	752	85.7
429.mcf	8	1127	64.8	1128	64.7	<b>1127</b>	<b>64.7</b>	8	1127	64.7	<b>1127</b>	<b>64.7</b>	1128	64.7
445.gobmk	8	577	145	<b>576</b>	<b>146</b>	565	149	8	530	158	526	160	<b>529</b>	<b>159</b>
456.hmmer	8	<b>602</b>	<b>124</b>	601	124	602	124	8	379	197	380	196	<b>380</b>	<b>197</b>
458.sjeng	8	696	139	<b>696</b>	<b>139</b>	695	139	8	642	151	644	150	<b>644</b>	<b>150</b>
462.libquantum	8	<b>3336</b>	<b>49.7</b>	3337	49.7	3336	49.7	1	<b>134</b>	<b>155</b>	134	154	132	157
464.h264ref	8	749	236	<b>748</b>	<b>237</b>	748	237	8	710	249	709	250	<b>709</b>	<b>250</b>
471.omnetpp	8	<b>979</b>	<b>51.1</b>	980	51.0	979	51.1	8	964	51.9	<b>964</b>	<b>51.9</b>	964	51.9
473.astar	8	<b>820</b>	<b>68.4</b>	819	68.6	821	68.4	8	<b>774</b>	<b>72.6</b>	775	72.5	773	72.6
483.xalanbmk	8	<b>490</b>	<b>113</b>	490	113	490	113	8	<b>490</b>	<b>113</b>	490	113	490	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Disabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalanbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor X5365, 3.00 GHz)

SPECint\_rate2006 = 116

SPECint\_rate\_base2006 = 97.9

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007

## Base Optimization Flags

C benchmarks:

-fast -inline-alloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor X5365,  
3.00 GHz)

SPECint\_rate2006 = 116

SPECint\_rate\_base2006 = 97.9

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007

## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.html>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor X5365, 3.00 GHz)

**SPECint\_rate2006 = 116**

**SPECint\_rate\_base2006 = 97.9**

**CPU2006 license:** 13

**Test sponsor:** Intel Corporation

**Tested by:** Intel Corporation

**Test date:** Aug-2007

**Hardware Availability:** Sep-2007

**Software Availability:** Nov-2007

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 13:10:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 4 September 2007.