SPEC® CINT2006 Result
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ASUS Computer International
(Test Sponsor: Intel Corporation)

Asus G2S (Intel Core 2 Duo X7800)

$SPECint^®\_rate2006 =$ 30.0

SPECint_rate_base2006 = 26.8
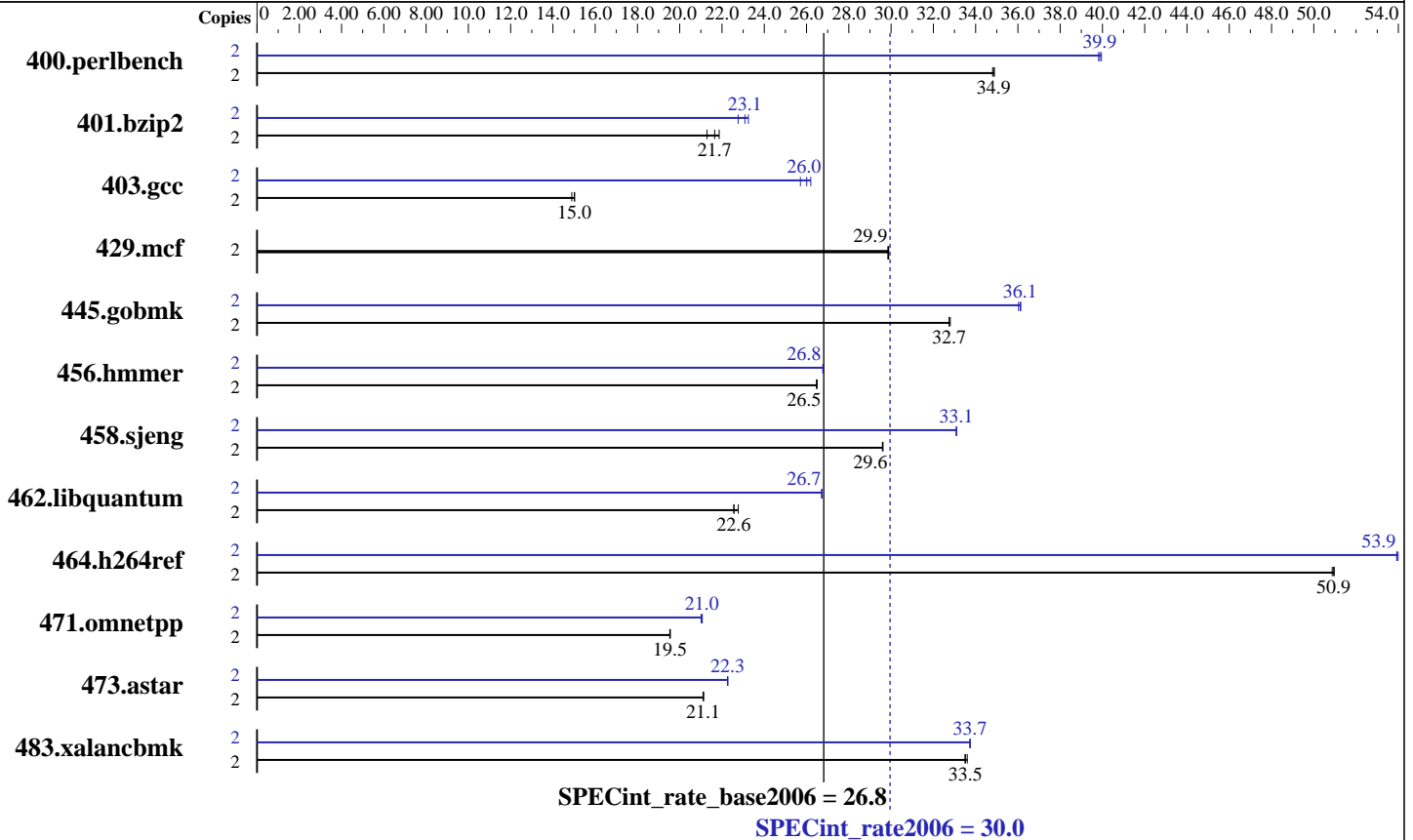
CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: May-2007

Copies 0 2.00 4.00 6.00 8.00 10.0 12.0 14.0 16.0 18.0 20.0 22.0 24.0 26.0 28.0 30.0 32.0 34.0 36.0 38.0 40.0 42.0 44.0 46.0 48.0 50.0 54.0

Benchmark	Copies	Value
400.perlbench	2	39.9
	2	34.9
401.bzip2	2	23.1
	2	21.7
403.gcc	2	26.0
	2	15.0
429.mcf	2	29.9
445.gobmk	2	36.1
	2	32.7
456.hmmer	2	26.8
	2	26.5
458.sjeng	2	33.1
	2	29.6
462.libquantum	2	26.7
	2	22.6
464.h264ref	2	53.9
	2	50.9
471.omnetpp	2	21.0
	2	19.5
473.astar	2	22.3
	2	21.1
483.xalancbmk	2	33.7
	2	33.5

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Hardware
CPU Name: Intel Core 2 Duo X7800
CPU Characteristics: 2.60 GHz, 4MB L2, 800 MHz bus
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem: 160GB Hitachi SATA, 5400RPM
Other Hardware: None

Software
Operating System: Windows Vista32 Ultimate
Compiler: Intel C++ Compiler for IA32 version 10.0
Build 20070426 Package ID: W_CC_P_10.0.025
Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None
SmartHeap Library Version 8.0 from
http://www.microquill.com/

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**561**	**34.9**	560	34.9	561	34.8	2	**490**	**39.9**	489	39.9	491	39.8
401.bzip2	2	**891**	**21.7**	883	21.9	906	21.3	2	848	22.8	**836**	**23.1**	830	23.2
403.gcc	2	1071	15.0	**1072**	**15.0**	1081	14.9	2	626	25.7	**619**	**26.0**	615	26.2
429.mcf	2	610	29.9	**611**	**29.9**	611	29.8	2	610	29.9	**611**	**29.9**	611	29.8
445.gobmk	2	**641**	**32.7**	640	32.8	641	32.7	2	**581**	**36.1**	582	36.0	581	36.1
456.hmmer	2	705	26.5	704	26.5	**705**	**26.5**	2	697	26.8	697	26.8	**697**	**26.8**
458.sjeng	2	817	29.6	**817**	**29.6**	818	29.6	2	731	33.1	**731**	**33.1**	731	33.1
462.libquantum	2	1837	22.6	**1836**	**22.6**	1820	22.8	2	1551	26.7	1550	26.7	**1551**	**26.7**
464.h264ref	2	**869**	**50.9**	870	50.9	868	51.0	2	**821**	**53.9**	821	53.9	820	54.0
471.omnetpp	2	**640**	**19.5**	639	19.6	640	19.5	2	593	21.1	595	21.0	**594**	**21.0**
473.astar	2	**665**	**21.1**	665	21.1	664	21.1	2	630	22.3	**631**	**22.3**	631	22.3
483.xalancbmk	2	**412**	**33.5**	411	33.6	412	33.5	2	409	33.7	409	33.7	**409**	**33.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
    The system bus runs at 667 MHz
    System was configured with an nVIDIA 8600M GT graphics card
    Binaries were built on Windows XP Professional SP2
    The start command with the /affinity switch was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
```
    icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
    icl -Qvc7.1
```

Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
    -fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
    -fast -Qcxx_features /F512000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
      403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
    icl -Qvc7.1
```

Peak Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
  464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
            -Qprefetch /F512000000 shlw32m.lib
                    -link /FORCE:MULTIPLE

    401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
            shlw32m.lib              -link /FORCE:MULTIPLE

      403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
                    -link /FORCE:MULTIPLE

      429.mcf: basepeak = yes

   445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
            -Qprec_div- -Qansi-alias /F512000000
                    -link /FORCE:MULTIPLE
```

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Test sponsor: Intel Corporation		**Hardware Availability:**	Jun-2007
Tested by: Intel Corporation		**Software Availability:**	May-2007

Peak Optimization Flags (Continued)

```
 456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
            -Qansi-alias /F512000000 shlw32m.lib
                        -link /FORCE:MULTIPLE

  458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
            /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
            -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
            shlw32m.lib          -link /FORCE:MULTIPLE

 464.h264ref: Same as 456.hmmer

C++ benchmarks:
    -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
    -Qcxx_features /F512000000 shlw32m.lib
                -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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