



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 23.6

ProLiant DL580 G4
(3.2 GHz, Intel Xeon processor 7130M)

SPECint_rate_base2006 = 21.9

CPU2006 license: 3

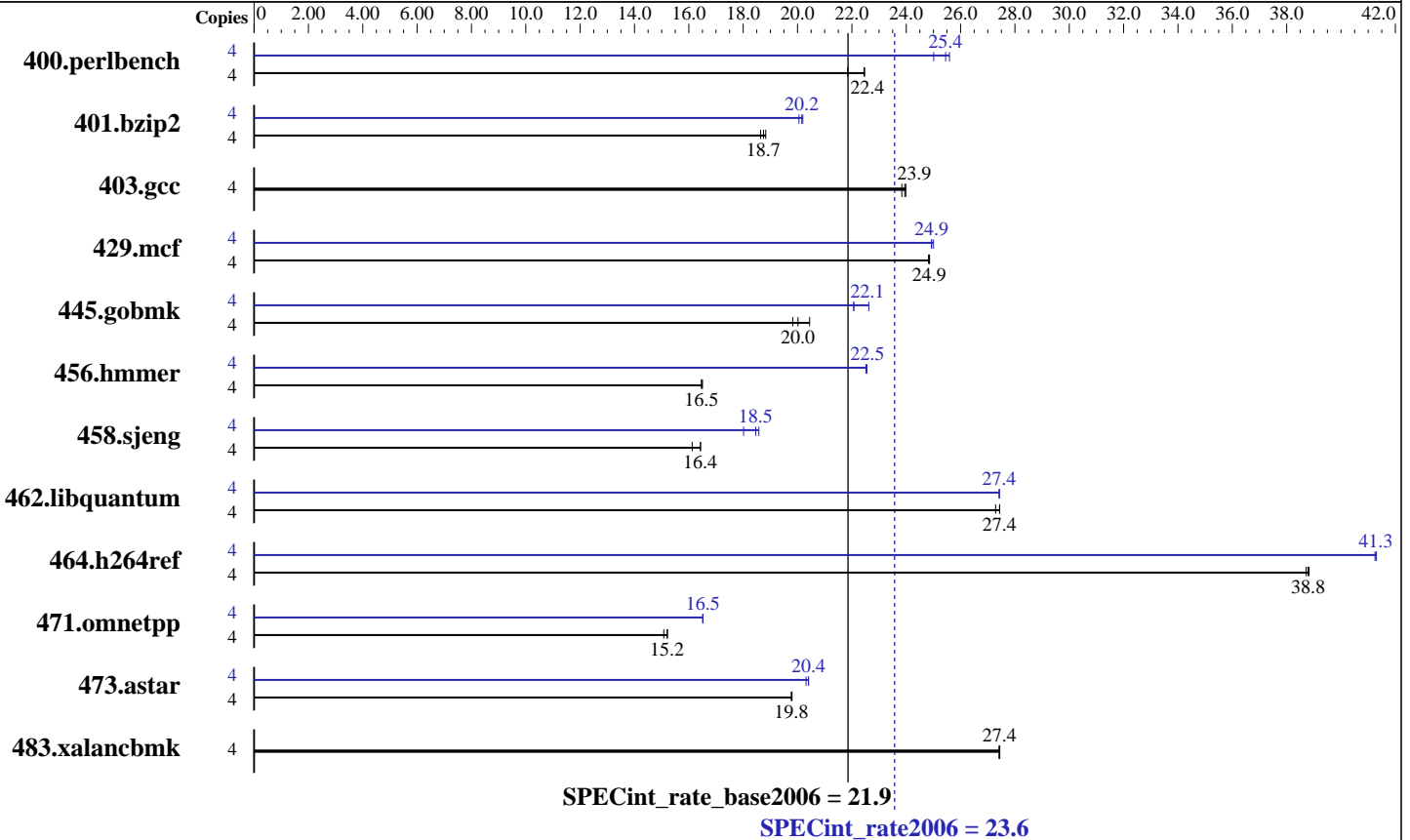
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Jul-2007

Hardware Availability: Aug-2006

Software Availability: May-2007



Hardware

CPU Name: Intel Xeon 7130M
 CPU Characteristics: 3.2 GHz, 800 MHz system bus
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16x2 GB PC2-3200R)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
 kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T
 applications, Version 10.0
 Build 20070426 Package ID: 1_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 libraries



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	4	1739	22.5	1789	21.8	1741	22.4	4	1527	25.6	1536	25.4	1563	25.0		
401.bzip2	4	2050	18.8	2059	18.7	2071	18.6	4	1925	20.1	1911	20.2	1915	20.2		
403.gcc	4	1343	24.0	1345	23.9	1351	23.8	4	1343	24.0	1345	23.9	1351	23.8		
429.mcf	4	1467	24.9	1468	24.9	1470	24.8	4	1463	24.9	1459	25.0	1463	24.9		
445.gobmk	4	2052	20.4	2096	20.0	2116	19.8	4	1901	22.1	1902	22.1	1855	22.6		
456.hammer	4	2266	16.5	2266	16.5	2262	16.5	4	1656	22.5	1655	22.6	1658	22.5		
458.sjeng	4	2947	16.4	3002	16.1	2946	16.4	4	2622	18.5	2687	18.0	2605	18.6		
462.libquantum	4	3037	27.3	3020	27.4	3021	27.4	4	3023	27.4	3022	27.4	3022	27.4		
464.h264ref	4	2282	38.8	2286	38.7	2280	38.8	4	2144	41.3	2143	41.3	2146	41.3		
471.omnetpp	4	1645	15.2	1643	15.2	1658	15.1	4	1514	16.5	1514	16.5	1514	16.5		
473.astar	4	1420	19.8	1420	19.8	1418	19.8	4	1376	20.4	1376	20.4	1382	20.3		
483.xalancbmk	4	1006	27.4	1007	27.4	1006	27.4	4	1006	27.4	1007	27.4	1006	27.4		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Regulator Disabled
Adjacent Sector Prefetch Enabled
Hardware Prefetcher Enabled
Hyper-Threading Technology Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xP -ipo -O3 -static -no-prec-div

C++ benchmarks:

-xP -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3
-no-prec-div -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3
-no-prec-div

403.gcc: basepeak = yes

429.mcf: -xP -ipo -O3 -static -no-prec-div -prefetch
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xP -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3 -static
-no-prec-div -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3 -static
-no-prec-div -unroll4

462.libquantum: Same as 458.sjeng

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xP -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.03.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.03.xml>



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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:24:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.