SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

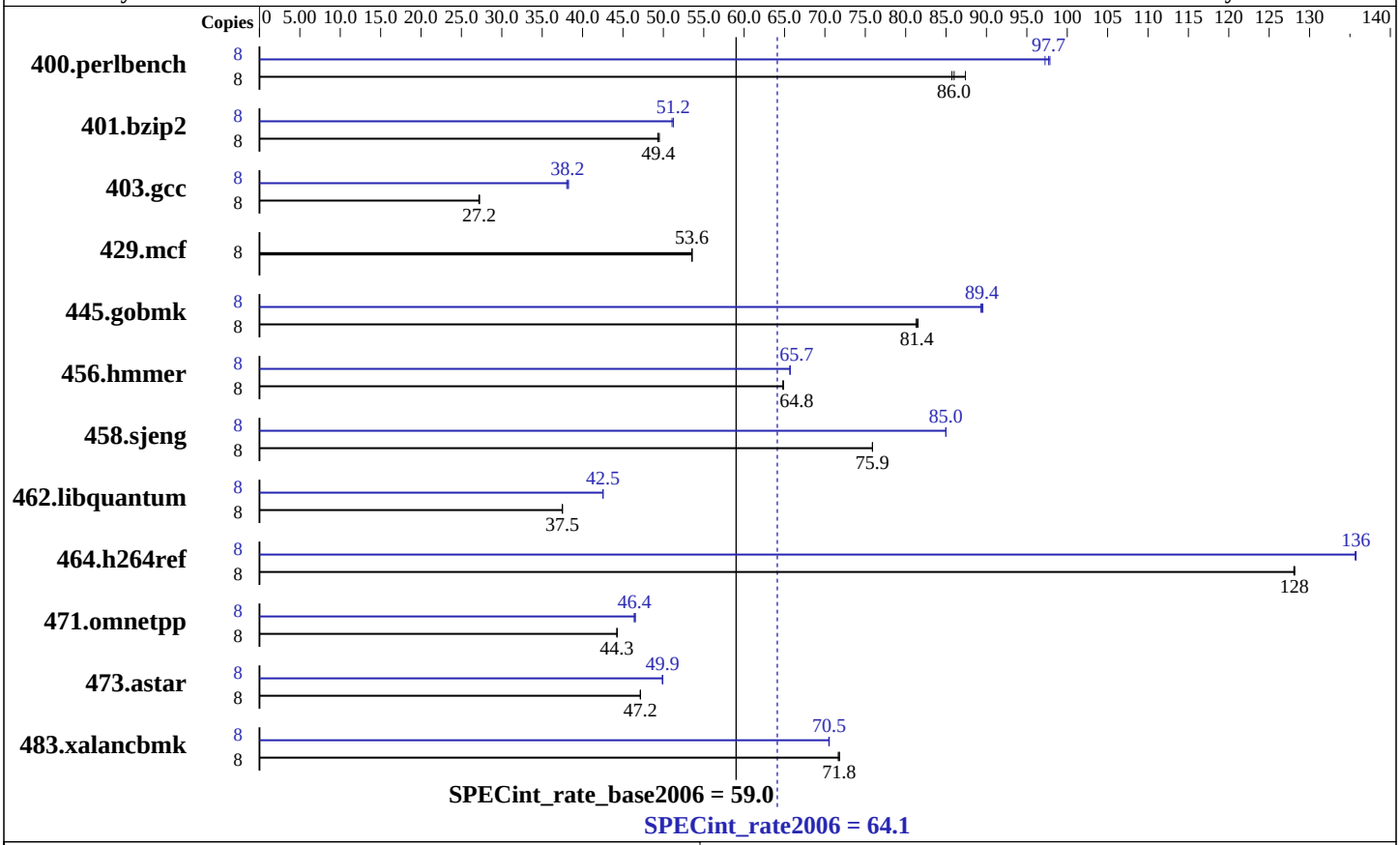
PowerEdge 2900 (Intel Xeon E5310, 1.60 GHz)

SPECint®_rate2006 = 64.1

SPECint_rate_base2006 = 59.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2007
Hardware Availability: Dec-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066 MHz Bus Speed
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 x 1 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem:	1 x 146 GB SAS, 15KRPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 2900 (Intel Xeon E5310, 1.60 GHz)

SPECint_rate2006 = 64.1

SPECint_rate_base2006 = 59.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2007
Hardware Availability: Dec-2006
Software Availability: Jun-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	894	87.4	912	85.7	**909**	**86.0**	8	799	97.9	**800**	**97.7**	804	97.3
401.bzip2	8	1566	49.3	**1564**	**49.4**	1559	49.5	8	**1507**	**51.2**	1512	51.1	1507	51.2
403.gcc	8	2364	27.2	2371	27.2	**2367**	**27.2**	8	1683	38.3	**1688**	**38.2**	1693	38.0
429.mcf	8	**1362**	**53.6**	1363	53.5	1362	53.6	8	**1362**	**53.6**	1363	53.5	1362	53.6
445.gobmk	8	**1031**	**81.4**	1032	81.3	1029	81.5	8	937	89.6	**938**	**89.4**	940	89.3
456.hmmer	8	**1151**	**64.8**	1151	64.8	1151	64.8	8	1136	65.7	1136	65.7	**1136**	**65.7**
458.sjeng	8	**1275**	**75.9**	1276	75.9	1275	75.9	8	1139	85.0	1139	85.0	**1139**	**85.0**
462.libquantum	8	**4421**	**37.5**	4423	37.5	4419	37.5	8	3896	42.5	3900	42.5	**3897**	**42.5**
464.h264ref	8	1381	128	**1382**	**128**	1382	128	8	1304	136	1305	136	**1304**	**136**
471.omnetpp	8	1130	44.3	**1130**	**44.3**	1129	44.3	8	1077	46.4	**1077**	**46.4**	1074	46.5
473.astar	8	**1191**	**47.2**	1191	47.2	1191	47.1	8	**1125**	**49.9**	1126	49.9	1125	49.9
483.xalancbmk	8	769	71.8	**769**	**71.8**	770	71.7	8	**783**	**70.5**	783	70.5	783	70.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
"start /b /wait /affinity" used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 -link /FORCE:MULTIPLE`

429.mcf: `basepeak = yes`

445.gobmk: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxT -O2 -Qipo -Qprec_div- -Qansi-alias /F512000000 -link /FORCE:MULTIPLE`

456.hmmer: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

458.sjeng: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

462.libquantum: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:21:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.