



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 28.2

PRIMERGY TX200 S3, Intel Xeon processor 5140,
2.33 GHz

SPECint_rate_base2006 = 26.1

CPU2006 license: 22

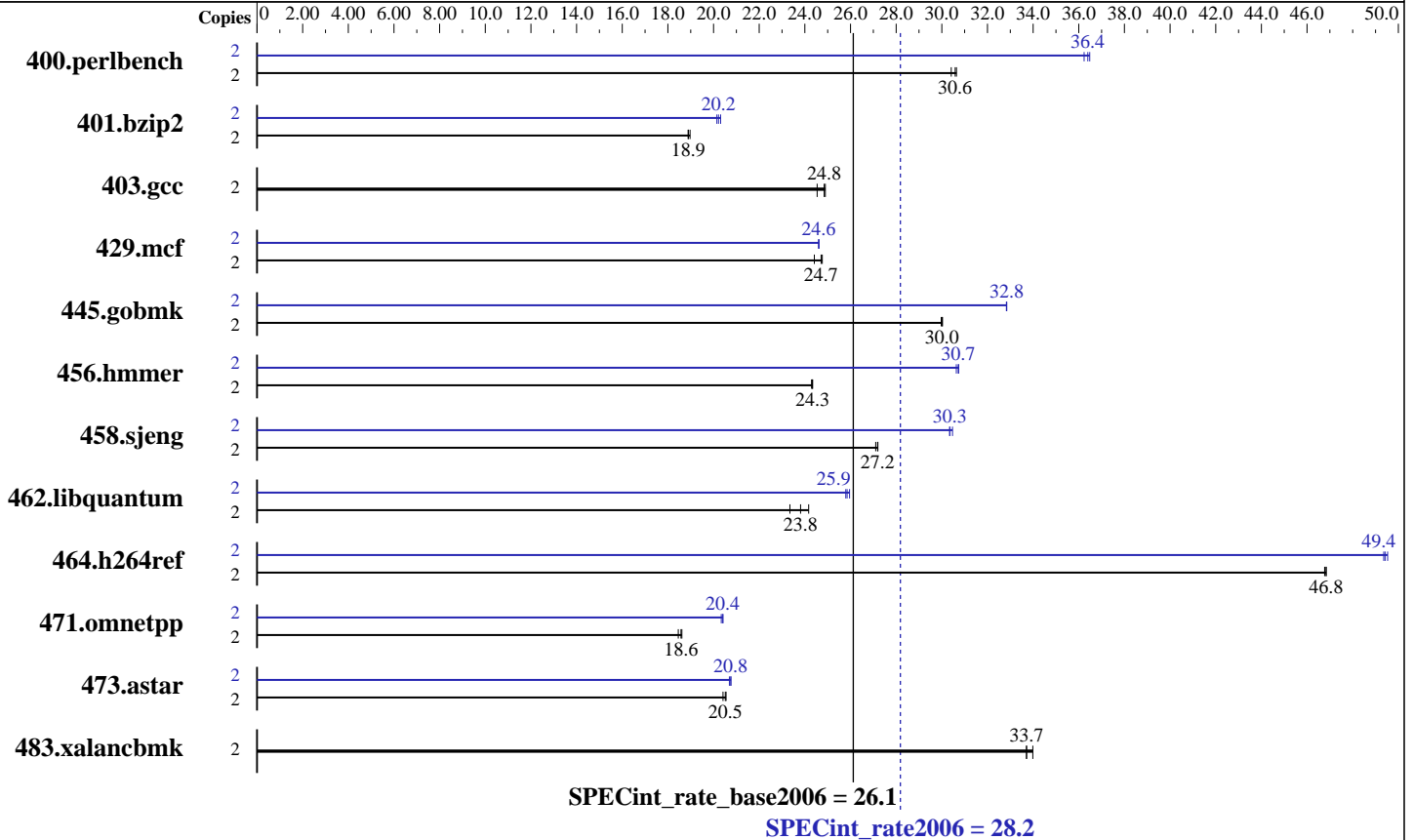
Test date: Jul-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Jul-2006

Tested by: Fujitsu Siemens Computers

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon 5140
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	639	30.6	643	30.4	638	30.6	2	539	36.2	537	36.4	536	36.5
401.bzip2	2	1022	18.9	1021	18.9	1017	19.0	2	950	20.3	955	20.2	958	20.1
403.gcc	2	656	24.5	648	24.8	647	24.9	2	656	24.5	648	24.8	647	24.9
429.mcf	2	747	24.4	737	24.8	738	24.7	2	741	24.6	741	24.6	741	24.6
445.gobmk	2	700	30.0	699	30.0	699	30.0	2	639	32.8	639	32.8	639	32.8
456.hmmmer	2	767	24.3	768	24.3	767	24.3	2	609	30.6	607	30.7	607	30.7
458.sjeng	2	890	27.2	893	27.1	890	27.2	2	794	30.5	797	30.3	797	30.3
462.libquantum	2	1775	23.3	1740	23.8	1715	24.2	2	1607	25.8	1603	25.9	1596	26.0
464.h264ref	2	946	46.8	945	46.8	945	46.8	2	897	49.4	896	49.4	894	49.5
471.omnetpp	2	677	18.5	673	18.6	672	18.6	2	615	20.3	613	20.4	612	20.4
473.astar	2	688	20.4	683	20.5	684	20.5	2	676	20.8	679	20.7	677	20.8
483.xalancbmk	2	409	33.7	409	33.7	406	34.0	2	409	33.7	409	33.7	406	34.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml



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