



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

SPECfp[®]_rate2006 = 54.7

BladeSymphony BS320 (Intel Xeon E5345)

SPECfp_rate_base2006 = 54.0

CPU2006 license: 872

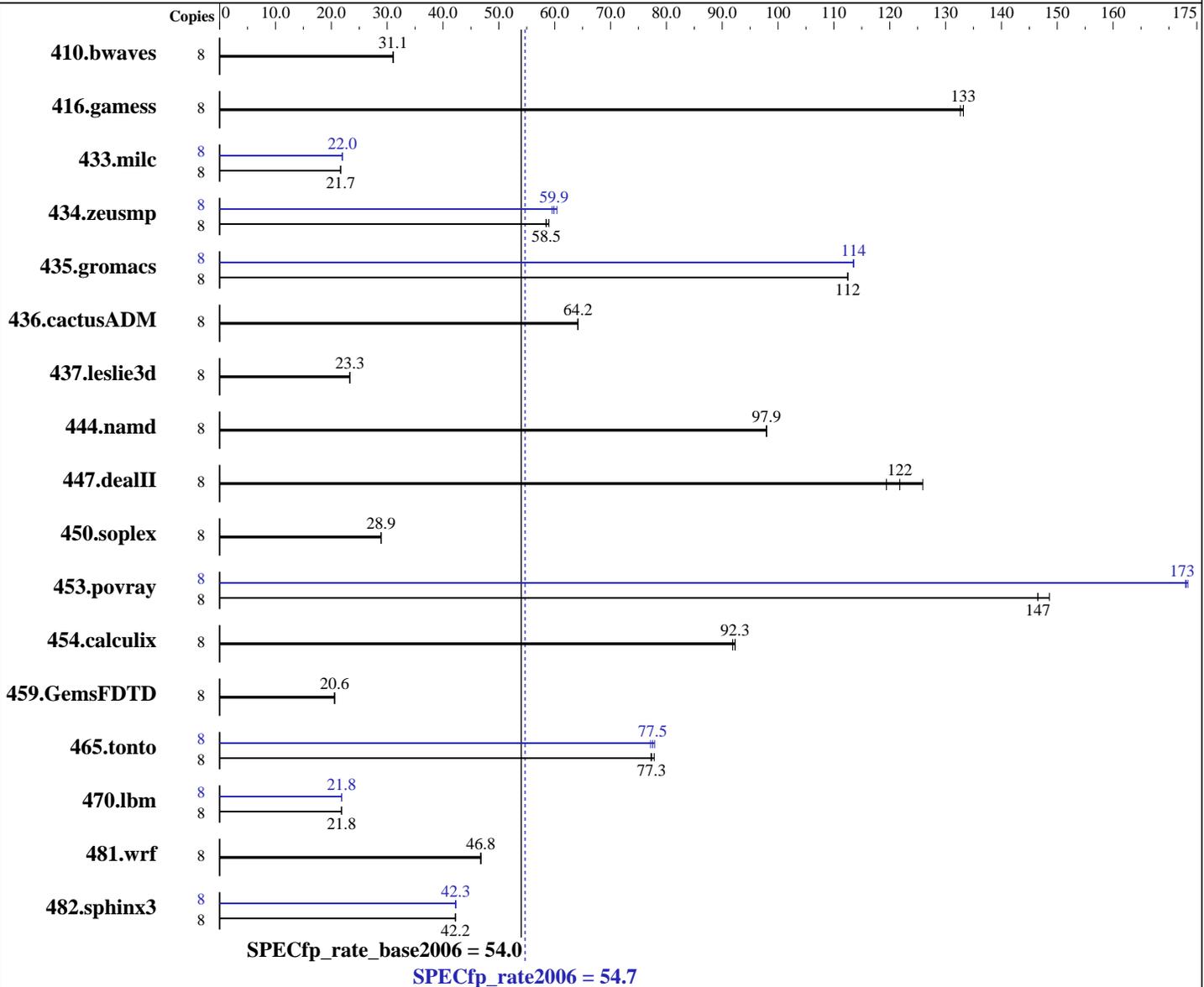
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System: Red Hat Enterprise Linux
 ES release 4 (Nahant Update 3)
 Kernel 2.6.9-34.ELsmp on an x86_64
 Compiler: Intel C++ Compiler for EM64T
 version 10.0 build 20070426
 Intel Fortran Compiler for EM64T
 version 10.0 build 20070426
 Auto Parallel: No

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 54.7

BladeSymphony BS320 (Intel Xeon E5345)

SPECfp_rate_base2006 = 54.0

CPU2006 license: 872

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

L3 Cache: None
Other Cache: None
Memory: 8 GB(4 x 2 GB PC2-5300F CAS 5-5-5)
Disk Subsystem: 2 x 73GB 10000rpm SAS
Other Hardware: None

File System: ext3
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3499	31.1	3497	31.1	3497	31.1	8	3499	31.1	3497	31.1	3497	31.1
416.gamess	8	1176	133	1181	133	1176	133	8	1176	133	1181	133	1176	133
433.milc	8	3386	21.7	3388	21.7	3390	21.7	8	3348	21.9	3337	22.0	3337	22.0
434.zeusmp	8	1245	58.5	1235	59.0	1245	58.5	8	1216	59.9	1205	60.4	1223	59.5
435.gromacs	8	508	112	508	112	508	113	8	503	114	503	113	503	114
436.cactusADM	8	1489	64.2	1491	64.1	1489	64.2	8	1489	64.2	1491	64.1	1489	64.2
437.leslie3d	8	3221	23.3	3226	23.3	3225	23.3	8	3221	23.3	3226	23.3	3225	23.3
444.namd	8	655	97.9	655	97.9	655	97.9	8	655	97.9	655	97.9	655	97.9
447.dealII	8	751	122	766	119	727	126	8	751	122	766	119	727	126
450.soplex	8	2311	28.9	2308	28.9	2305	28.9	8	2311	28.9	2308	28.9	2305	28.9
453.povray	8	290	147	290	147	286	149	8	246	173	245	173	246	173
454.calculix	8	715	92.3	715	92.3	718	91.9	8	715	92.3	715	92.3	718	91.9
459.GemsFDTD	8	4123	20.6	4127	20.6	4124	20.6	8	4123	20.6	4127	20.6	4124	20.6
465.tonto	8	1019	77.3	1011	77.8	1018	77.3	8	1020	77.2	1015	77.5	1011	77.9
470.lbm	8	5034	21.8	5034	21.8	5035	21.8	8	5031	21.8	5031	21.8	5031	21.8
481.wrf	8	1913	46.7	1909	46.8	1911	46.8	8	1913	46.7	1909	46.8	1911	46.8
482.sphinx3	8	3692	42.2	3697	42.2	3690	42.3	8	3686	42.3	3693	42.2	3689	42.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 54.7

BladeSymphony BS320 (Intel Xeon E5345)

SPECfp_rate_base2006 = 54.0

CPU2006 license: 872

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 54.7

BladeSymphony BS320 (Intel Xeon E5345)

SPECfp_rate_base2006 = 54.0

CPU2006 license: 872

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

-prof_gen(pass 1) -prof_use(pass 2) -fast

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof_gen(pass 1) -prof_use(pass 2) -fast

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -prof_gen(pass 1) -prof_use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: Same as 434.zeusmp

Benchmarks using both Fortran and C:

435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast

436.cactusADM: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

HITACHI

SPECfp_rate2006 = 54.7

BladeSymphony BS320 (Intel Xeon E5345)

SPECfp_rate_base2006 = 54.0

CPU2006 license: 872

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/ic100_fp.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/ic100_fp.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 12:23:25 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.