SPEC® CINT2006 Result
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HITACHI

$SPECint^®\_rate2006 =$ 81.2

BladeSymphony BS320 (Intel Xeon E5345)

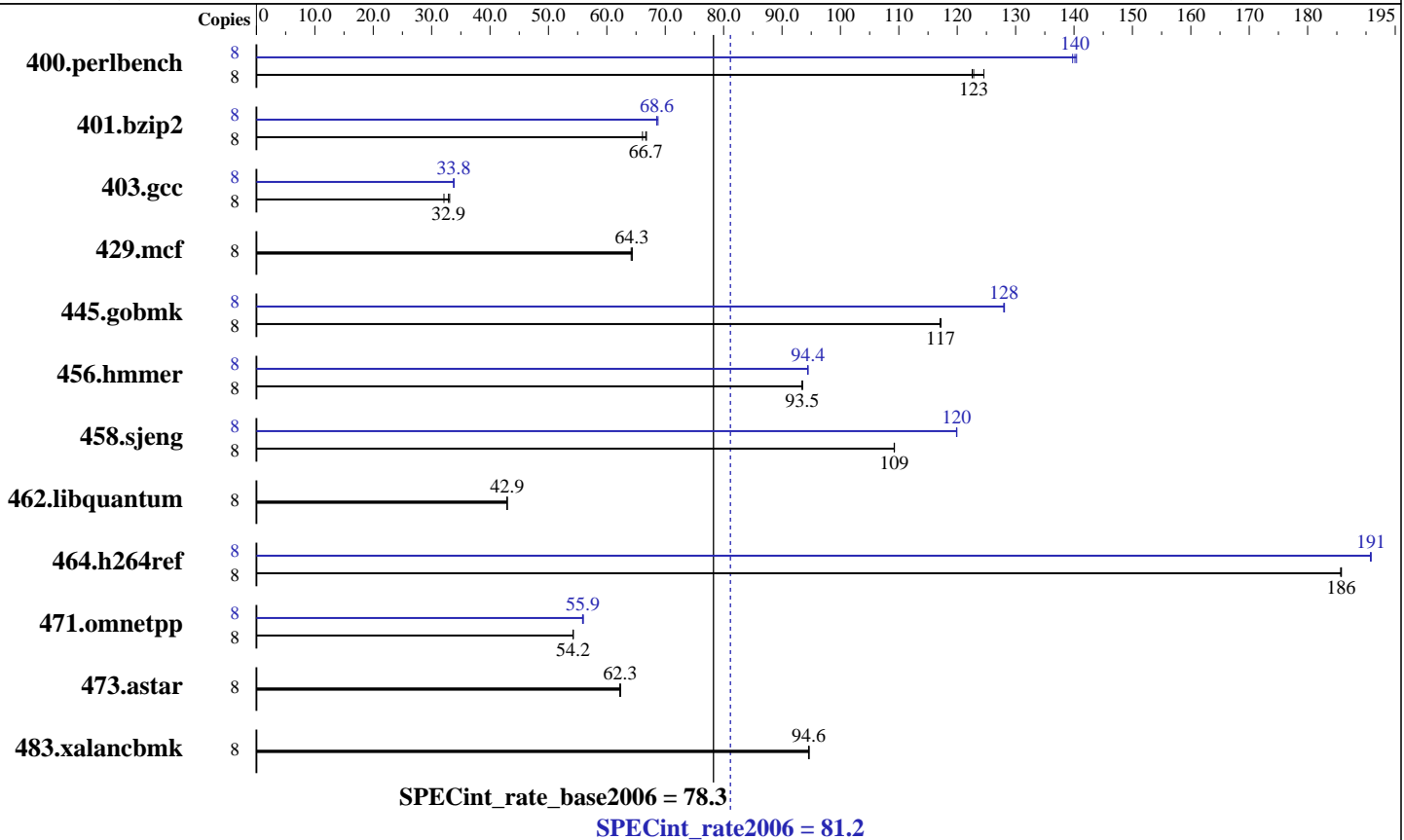$SPECint\_rate\_base2006 =$ 78.3

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jun-2007
Hardware Availability: Jan-2007
Software Availability: Jun-2007

Benchmark	Copies	Base	Peak
400.perlbench	8	123	140
401.bzip2	8	66.7	68.6
403.gcc	8	32.9	33.8
429.mcf	8	64.3	
445.gobmk	8	117	128
456.hmmer	8	93.5	94.4
458.sjeng	8	109	120
462.libquantum	8	42.9	
464.h264ref	8	186	191
471.omnetpp	8	54.2	55.9
473.astar	8	62.3	
483.xalancbmk	8	94.6	

SPECint_rate_base2006 = 78.3
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Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB(4 x 2 GB PC2-5300F CAS 5-5-5)
Disk Subsystem:	2 x 73 GB 10000rpm SAS
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 R2, Enterprise x64 Edition
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library, Version 8.0

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	627	125	638	123	**636**	**123**	8	557	140	**558**	**140**	559	140
401.bzip2	8	**1158**	**66.7**	1168	66.1	1156	66.8	8	1126	68.5	**1126**	**68.6**	1123	68.8
403.gcc	8	**1958**	**32.9**	1946	33.1	2005	32.1	8	1904	33.8	1908	33.8	**1905**	**33.8**
429.mcf	8	1134	64.3	1137	64.2	**1134**	**64.3**	8	1134	64.3	1137	64.2	**1134**	**64.3**
445.gobmk	8	**716**	**117**	717	117	716	117	8	**655**	**128**	655	128	656	128
456.hmmer	8	798	93.5	799	93.4	**799**	**93.5**	8	**790**	**94.4**	791	94.4	790	94.4
458.sjeng	8	886	109	**886**	**109**	886	109	8	808	120	807	120	**807**	**120**
462.libquantum	8	3862	42.9	3860	42.9	**3861**	**42.9**	8	3862	42.9	3860	42.9	**3861**	**42.9**
464.h264ref	8	953	186	954	186	**953**	**186**	8	928	191	**928**	**191**	928	191
471.omnetpp	8	**922**	**54.2**	922	54.2	922	54.3	8	895	55.8	**895**	**55.9**	893	56.0
473.astar	8	901	62.3	**902**	**62.3**	902	62.2	8	901	62.3	**902**	**62.3**	902	62.2
483.xalancbmk	8	584	94.5	**584**	**94.6**	583	94.7	8	584	94.5	**584**	**94.6**	583	94.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
        -link /FORCE:MULTIPLE
```

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Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
> `icl -Qvc7.1 -Qc99`

C++ benchmarks:
> `icl -Qvc7.1`

Peak Portability Flags

> 403.gcc: `-DSPEC_CPU_WIN32`
> 464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

> 400.perlbench: `ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast`
> `/F512000000 shlw32m.lib          -link /FORCE:MULTIPLE`

> 401.bzip2: `Same as 400.perlbench`

> 403.gcc: `Same as 400.perlbench`

> 429.mcf: `basepeak = yes`

> 445.gobmk: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000`
> `shlw32m.lib               -link /FORCE:MULTIPLE`

> 456.hmmer: `Same as 400.perlbench`

> 458.sjeng: `Same as 400.perlbench`

> 462.libquantum: `basepeak = yes`

> 464.h264ref: `Same as 400.perlbench`

C++ benchmarks:

Continued on next page

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Tested by: HITACHI		**Software Availability:**	Jun-2007

Peak Optimization Flags (Continued)

```
471.omnetpp: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
             -Qcxx_features /F512000000 shlw32m.lib
                       -link /FORCE:MULTIPLE
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/ic100.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/ic100.xml

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