SPEC® CFP2006 Result

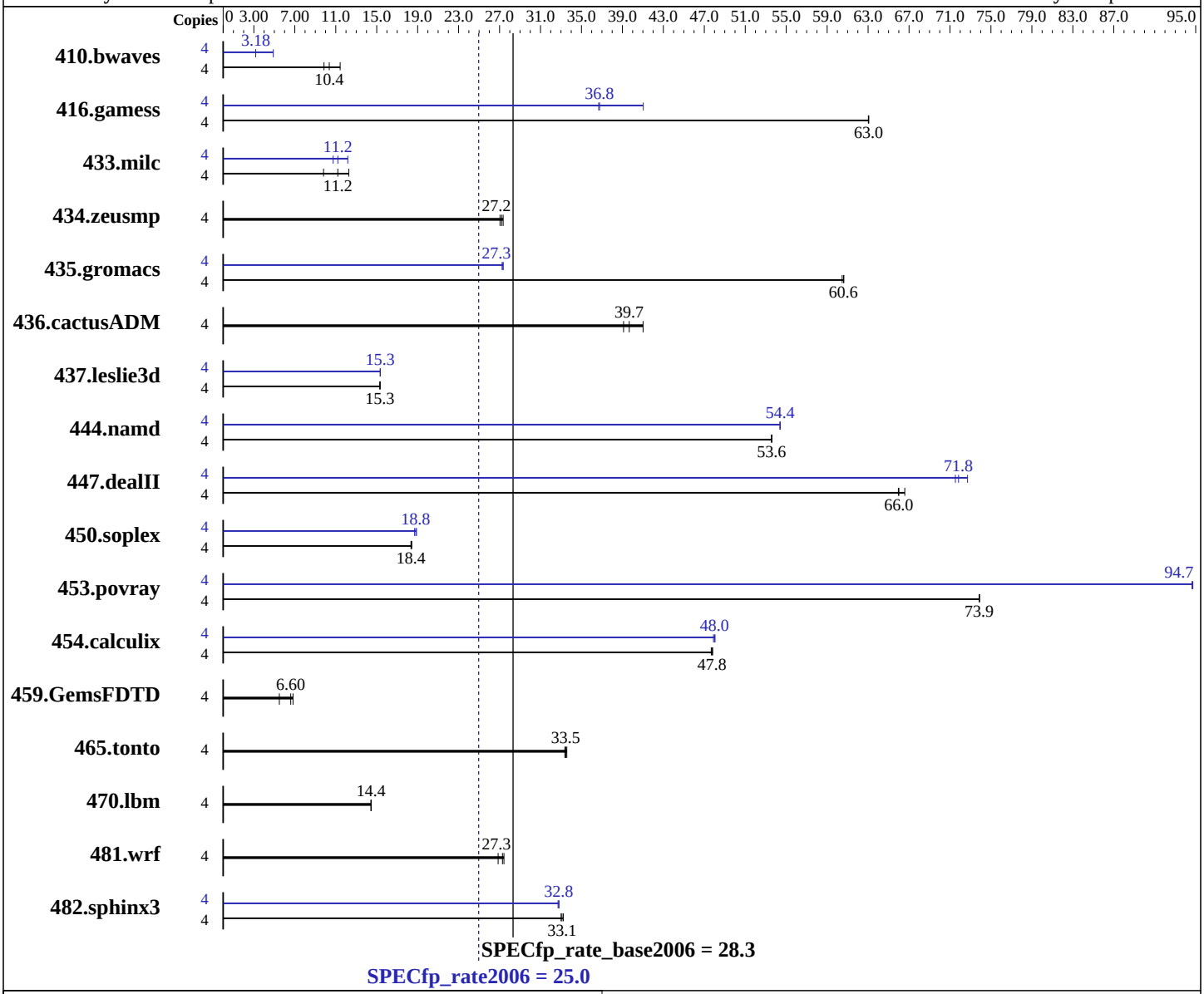
Supermicro Motherboard PDSMI+

SPECfp®_rate2006 = 25.0

SPECfp_rate_base2006 = 28.3

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jul-2007
Hardware Availability: May-2007
Software Availability: Apr-2007



Hardware

CPU Name:	Intel Core 2 Quad Q6700
CPU Characteristics:	2.67GHz 1066 MHz FSB
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition W/ SP1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z Intel Fortran Compiler for IA32 version 9.1 Build no 20070322Z Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2 X 1GB ECC PC2-5300, CL5, DDR2)
Disk Subsystem: ST3750640AS 750GB SATA II, 7200RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	**5244**	**10.4**	4756	11.4	5518	9.85	4	**17090**	**3.18**	17107	3.18	11103	4.90
416.gamess	4	**1242**	**63.0**	1242	63.1	1243	63.0	4	1908	41.0	**2130**	**36.8**	2135	36.7
433.milc	4	**3278**	**11.2**	3744	9.81	2990	12.3	4	**3276**	**11.2**	3418	10.7	3017	12.2
434.zeusmp	4	**1338**	**27.2**	1331	27.4	1346	27.0	4	**1338**	**27.2**	1331	27.4	1346	27.0
435.gromacs	4	471	60.6	**471**	**60.6**	472	60.5	4	**1045**	**27.3**	1044	27.4	1048	27.3
436.cactusADM	4	**1205**	**39.7**	1222	39.1	1165	41.0	4	**1205**	**39.7**	1222	39.1	1165	41.0
437.leslie3d	4	2453	15.3	**2453**	**15.3**	2457	15.3	4	2451	15.3	2448	15.4	**2450**	**15.3**
444.namd	4	**599**	**53.6**	599	53.6	599	53.6	4	590	54.4	590	54.4	**590**	**54.4**
447.dealII	4	694	66.0	687	66.6	**693**	**66.0**	4	640	71.5	**637**	**71.8**	629	72.7
450.soplex	4	**1817**	**18.4**	1818	18.4	1811	18.4	4	1784	18.7	1766	18.9	**1773**	**18.8**
453.povray	4	**288**	**73.9**	288	73.9	288	73.8	4	225	94.7	**225**	**94.7**	225	94.7
454.calculix	4	690	47.8	692	47.7	**691**	**47.8**	4	**688**	**48.0**	687	48.1	689	47.9
459.GemsFDTD	4	7732	5.49	**6428**	**6.60**	6217	6.83	4	7732	5.49	**6428**	**6.60**	6217	6.83
465.tonto	4	1179	33.4	1172	33.6	**1176**	**33.5**	4	1179	33.4	1172	33.6	**1176**	**33.5**
470.lbm	4	3802	14.5	**3804**	**14.4**	3804	14.4	4	3802	14.5	**3804**	**14.4**	3804	14.4
481.wrf	4	1629	27.4	1663	26.9	**1638**	**27.3**	4	1629	27.4	1663	26.9	**1638**	**27.3**
482.sphinx3	4	2346	33.2	2362	33.0	**2355**	**33.1**	4	**2378**	**32.8**	2383	32.7	2377	32.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with SC816S-R700 case,
To ensure system stability,
a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon3000/3000/PDSMi+.cfm
The system bus runs at 1066 MHz
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: basepeak = yes

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxB -Qipo -O3
-Qprec-div- /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
libguide.lib libguide40.lib -link /FORCE:MULTIPLE

416.gamess: Same as 410.bwaves

434.zeusmp: basepeak = yes

437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
-link /FORCE:MULTIPLE

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib libguide.lib libguide40.lib -link /FORCE:MULTIPLE`

436.cactusADM: `basepeak = yes`

454.calculix: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000 -link /FORCE:MULTIPLE`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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