



SPEC[®] CFP2006 Result

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Supermicro Motherboard PDSMI+

SPECfp[®]2006 = 15.6

SPECfp_base2006 = 15.0

CPU2006 license: 001176

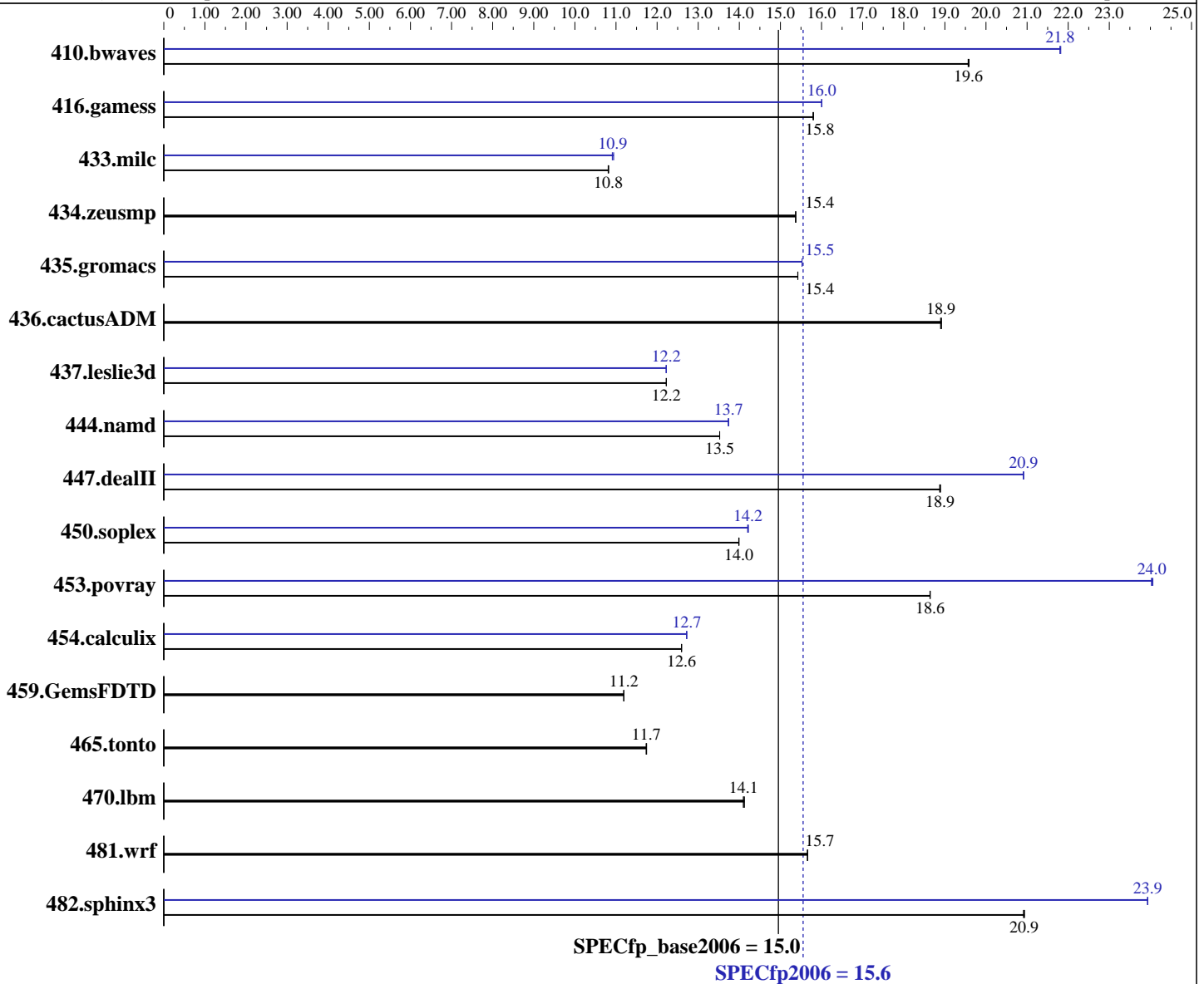
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jul-2007

Hardware Availability: May-2007

Software Availability: Apr-2007



Hardware

CPU Name: Intel Core 2 Quad Q6700
 CPU Characteristics: 2.67GHz 1066 MHz FSB
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: Windows Server 2003 Enterprise Edition W/ SP1
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20070322Z
 Intel Fortran Compiler for IA32 version 9.1
 Build no 20070322Z
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2 X 1GB ECC PC2-5300, CL5, DDR2)
Disk Subsystem: ST3750640AS 750GB SATA II, 7200RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	695	19.6	694	19.6	694	19.6	623	21.8	623	21.8	623	21.8
416.gamess	1240	15.8	1240	15.8	1239	15.8	1223	16.0	1224	16.0	1223	16.0
433.milc	849	10.8	849	10.8	848	10.8	839	10.9	841	10.9	841	10.9
434.zeusmp	592	15.4	592	15.4	592	15.4	592	15.4	592	15.4	592	15.4
435.gromacs	463	15.4	463	15.4	463	15.4	460	15.5	460	15.5	460	15.5
436.cactusADM	632	18.9	632	18.9	632	18.9	632	18.9	632	18.9	632	18.9
437.leslie3d	769	12.2	769	12.2	769	12.2	769	12.2	769	12.2	769	12.2
444.namd	593	13.5	593	13.5	593	13.5	584	13.7	584	13.7	584	13.7
447.dealII	606	18.9	606	18.9	605	18.9	547	20.9	547	20.9	547	20.9
450.soplex	596	14.0	596	14.0	596	14.0	587	14.2	586	14.2	587	14.2
453.povray	285	18.6	285	18.6	285	18.6	221	24.1	221	24.0	221	24.0
454.calculix	655	12.6	655	12.6	655	12.6	648	12.7	649	12.7	649	12.7
459.GemsFDTD	948	11.2	948	11.2	948	11.2	948	11.2	948	11.2	948	11.2
465.tonto	838	11.7	838	11.7	838	11.7	838	11.7	838	11.7	838	11.7
470.lbm	973	14.1	974	14.1	974	14.1	973	14.1	974	14.1	974	14.1
481.wrf	713	15.7	713	15.7	713	15.7	713	15.7	713	15.7	713	15.7
482.sphinx3	931	20.9	931	20.9	932	20.9	814	23.9	814	23.9	815	23.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC816S-R700 case,
To ensure system stability,
a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
<http://www.supermicro.com/products/motherboard/Xeon3000/3000/PDSMI+.cfm>
The system bus runs at 1066 MHz

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

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Base Compiler Invocation (Continued)

C++ benchmarks:
 icl -Qvc7.1

Fortran benchmarks:
 ifort

Benchmarks using both Fortran and C:
 icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
 444.namd: -TP
 447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 -DBOOST_NO_INTRINSIC_WCHAR_T
 453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
 481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
 -fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
 -fast -Qcxx_features /F950000000 shlw32m.lib
 -link /FORCE:MULTIPLE

Fortran benchmarks:
 -fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
 -fast /F950000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
 icl -Qvc7.1 -Qc99

C++ benchmarks:
 icl -Qvc7.1

Fortran benchmarks:
 ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: basepeak = yes

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxB -Qipo -O3
-Qprec-div- /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
libguide.lib libguide40.lib -link /FORCE:MULTIPLE

416.gamess: Same as 410.bwaves

434.zeusmp: basepeak = yes

437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
-link /FORCE:MULTIPLE

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
             shlw32m.lib libguide.lib libguide40.lib
             -link /FORCE:MULTIPLE
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
             -link /FORCE:MULTIPLE
```

```
481.wrf: basepeak = yes
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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