



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint®_rate2006 = 63.8

SPECint_rate_base2006 = 58.9

CPU2006 license: 22

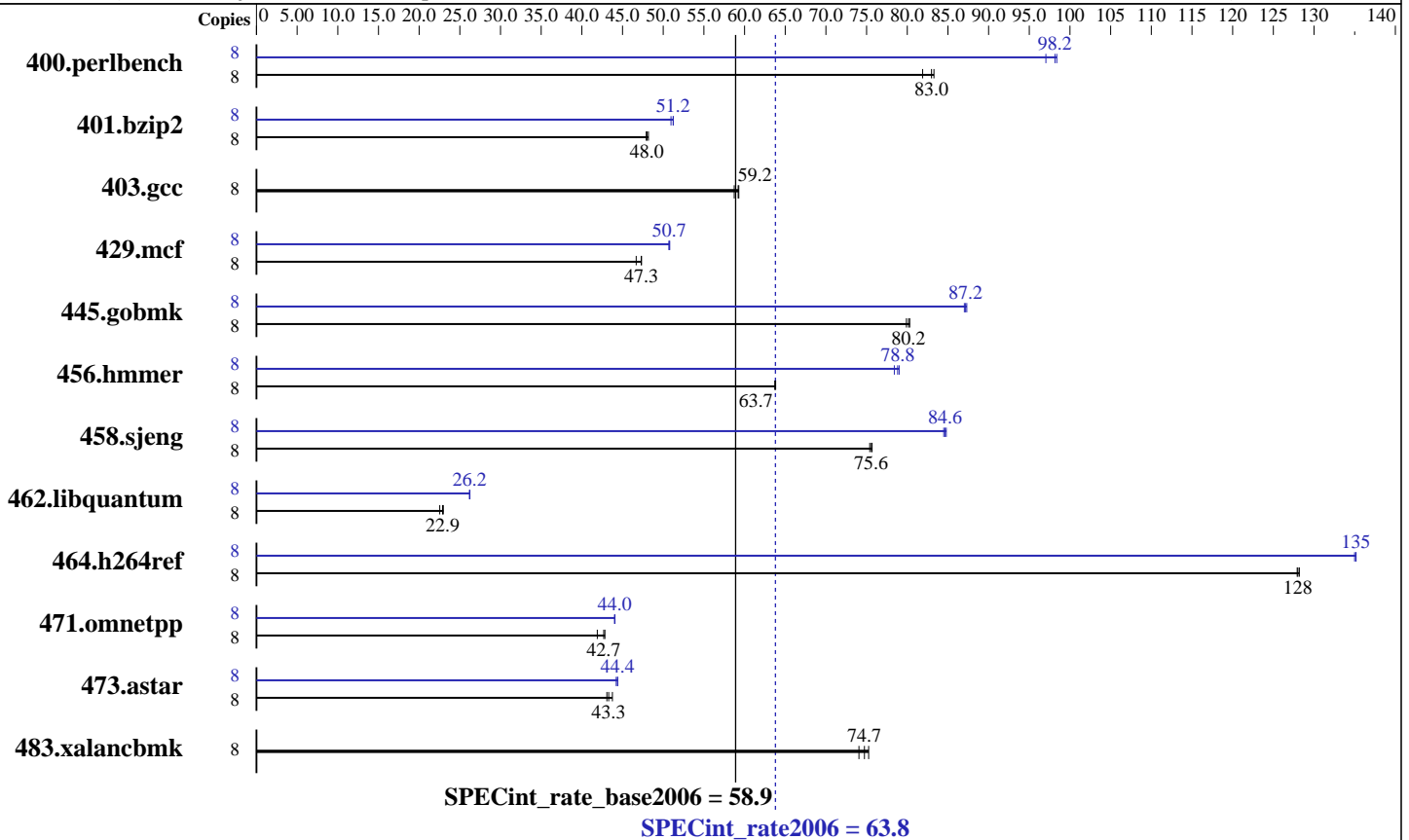
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jul-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smpt
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	939	83.3	954	81.9	<u>942</u>	<u>83.0</u>	8	<u>796</u>	<u>98.2</u>	794	98.4	805	97.0
401.bzip2	8	1611	47.9	<u>1608</u>	<u>48.0</u>	1602	48.2	8	1515	51.0	<u>1507</u>	<u>51.2</u>	1507	51.2
403.gcc	8	1097	58.7	<u>1088</u>	<u>59.2</u>	1086	59.3	8	1097	58.7	<u>1088</u>	<u>59.2</u>	1086	59.3
429.mcf	8	1563	46.7	<u>1543</u>	<u>47.3</u>	1541	47.3	8	1436	50.8	1439	50.7	<u>1438</u>	<u>50.7</u>
445.gobmk	8	1050	79.9	1045	80.3	<u>1047</u>	<u>80.2</u>	8	961	87.3	964	87.0	<u>963</u>	<u>87.2</u>
456.hmmmer	8	1169	63.8	<u>1171</u>	<u>63.7</u>	1171	63.7	8	945	79.0	<u>947</u>	<u>78.8</u>	952	78.4
458.sjeng	8	1284	75.4	1279	75.7	<u>1281</u>	<u>75.6</u>	8	1142	84.8	<u>1144</u>	<u>84.6</u>	1146	84.5
462.libquantum	8	7357	22.5	<u>7245</u>	<u>22.9</u>	7217	23.0	8	6317	26.2	6331	26.2	<u>6323</u>	<u>26.2</u>
464.h264ref	8	<u>1383</u>	<u>128</u>	1384	128	1381	128	8	<u>1310</u>	<u>135</u>	1310	135	1312	135
471.omnetpp	8	1193	41.9	<u>1171</u>	<u>42.7</u>	1167	42.8	8	1136	44.0	1135	44.1	<u>1135</u>	<u>44.0</u>
473.astar	8	1304	43.1	<u>1297</u>	<u>43.3</u>	1284	43.8	8	1265	44.4	1270	44.2	<u>1265</u>	<u>44.4</u>
483.xalancbmk	8	745	74.1	734	75.3	<u>739</u>	<u>74.7</u>	8	745	74.1	734	75.3	<u>739</u>	<u>74.7</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml



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