



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY TX120, Intel Xeon processor 3070, 2.66 GHz

SPECint®\_rate2006 = 30.7

SPECint\_rate\_base2006 = 29.3

CPU2006 license: 22

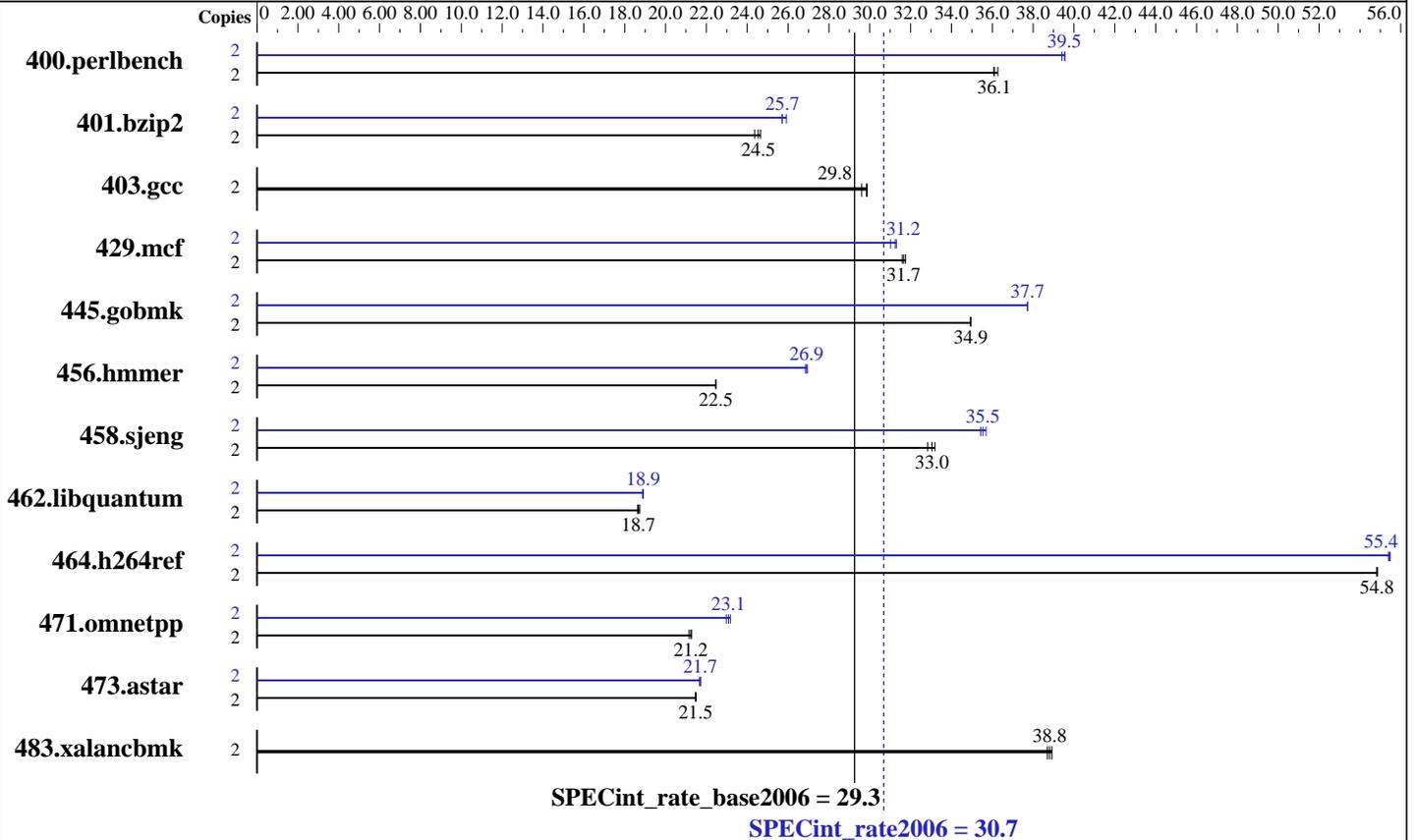
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2007

Hardware Availability: May-2007

Software Availability: Mar-2007



### Hardware

CPU Name: Intel Xeon 3070  
 CPU Characteristics: 1067 MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)  
 Disk Subsystem: Seagate ST973401SS (SAS, 73GB 10000rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	541	36.1	539	36.3	<u>541</u>	<u>36.1</u>	2	494	39.6	496	39.4	<u>494</u>	<u>39.5</u>
401.bzip2	2	<u>787</u>	<u>24.5</u>	792	24.4	783	24.6	2	744	25.9	<u>750</u>	<u>25.7</u>	751	25.7
403.gcc	2	<u>540</u>	<u>29.8</u>	539	29.9	544	29.6	2	<u>540</u>	<u>29.8</u>	539	29.9	544	29.6
429.mcf	2	577	31.6	<u>576</u>	<u>31.7</u>	574	31.8	2	588	31.0	<u>584</u>	<u>31.2</u>	583	31.3
445.gobmk	2	600	34.9	600	34.9	<u>600</u>	<u>34.9</u>	2	<u>556</u>	<u>37.7</u>	556	37.7	556	37.7
456.hmmer	2	831	22.4	<u>831</u>	<u>22.5</u>	830	22.5	2	695	26.8	693	26.9	<u>694</u>	<u>26.9</u>
458.sjeng	2	<u>732</u>	<u>33.0</u>	737	32.8	729	33.2	2	683	35.4	678	35.7	<u>681</u>	<u>35.5</u>
462.libquantum	2	2211	18.7	2223	18.6	<u>2219</u>	<u>18.7</u>	2	2194	18.9	<u>2192</u>	<u>18.9</u>	2192	18.9
464.h264ref	2	<u>807</u>	<u>54.8</u>	807	54.8	807	54.9	2	799	55.4	<u>798</u>	<u>55.4</u>	798	55.5
471.omnetpp	2	587	21.3	591	21.1	<u>589</u>	<u>21.2</u>	2	<u>542</u>	<u>23.1</u>	539	23.2	544	23.0
473.astar	2	654	21.5	<u>654</u>	<u>21.5</u>	653	21.5	2	648	21.7	646	21.7	<u>647</u>	<u>21.7</u>
483.xalancbmk	2	355	38.9	357	38.7	<u>356</u>	<u>38.8</u>	2	355	38.9	357	38.7	<u>356</u>	<u>38.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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