SPEC® CINT2006 Result
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Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor 5130, 2.00 GHz)

$SPECint®\_rate2006 =$ 50.4

$SPECint\_rate\_base2006 =$ 46.4

Copies

400.perlbench	4 / 4 — 63.7 / 53.9
401.bzip2	4 / 4 — 36.0 / 33.9
403.gcc	4 — 45.8
429.mcf	4 / 4 — 46.5 / 45.0
445.gobmk	4 / 4 — 57.0 / 52.0
456.hmmer	4 / 4 — 52.9 / 41.8
458.sjeng	4 / 4 — 53.4 / 47.6
462.libquantum	4 / 4 — 50.8 / 45.9
464.h264ref	4 / 4 — 85.9 / 81.3
471.omnetpp	4 / 4 — 36.1 / 34.0
473.astar	4 / 4 — 36.3 / 33.8
483.xalancbmk	4 — 59.1

SPECint_rate_base2006 = 46.4

SPECint_rate2006 = 50.4

Hardware

CPU Name: Intel Xeon 5130
CPU Characteristics: Dual Core, 2.00 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (8 * 1GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel: No
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15

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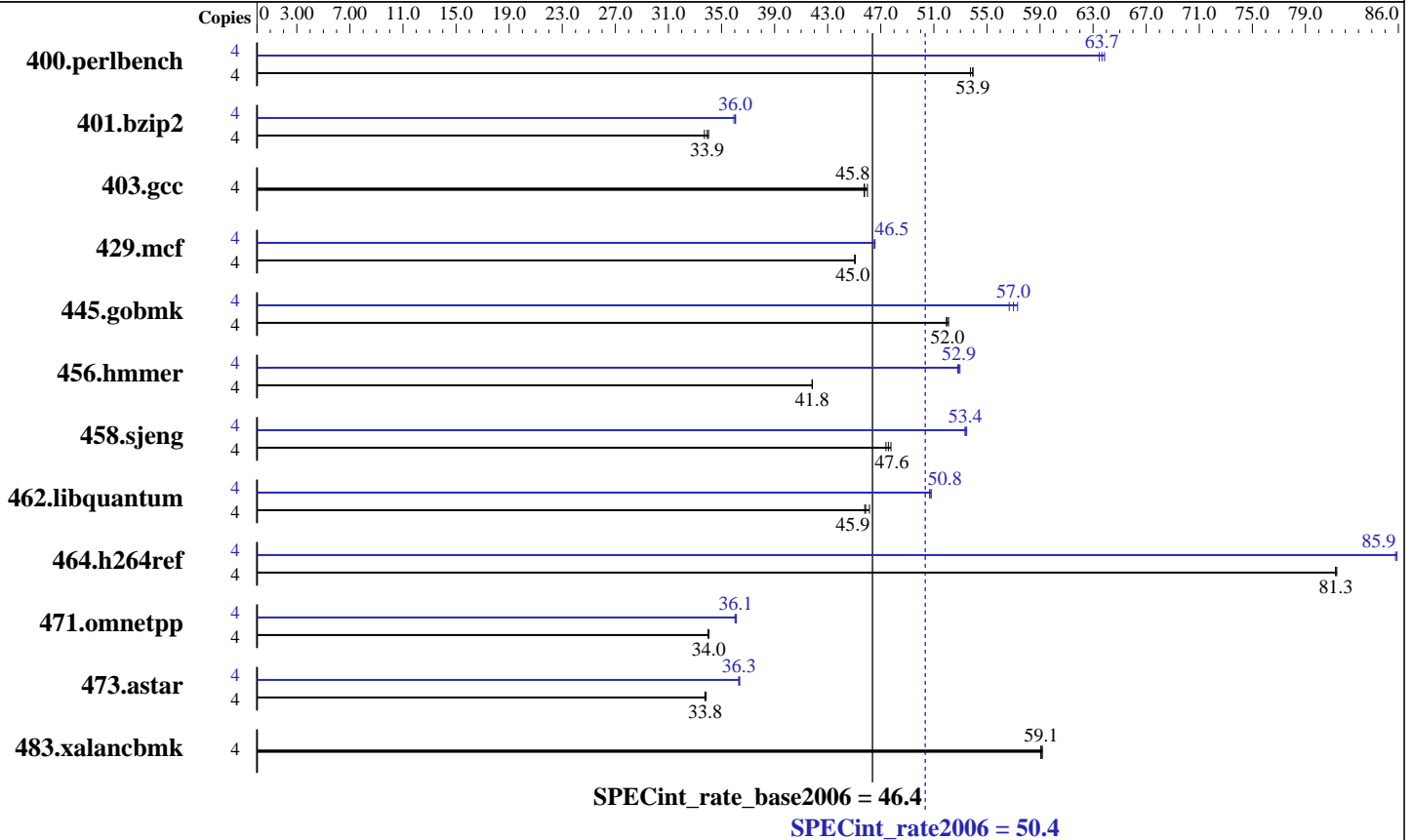SPECint_rate_base2006 = 46.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007

Results Table

Benchmark		Base							Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	727	53.8	**725**	**53.9**	724	53.9	4	616	63.5	612	63.9	**614**	**63.7**
401.bzip2	4	1145	33.7	1134	34.0	**1138**	**33.9**	4	1074	35.9	**1071**	**36.0**	1070	36.1
403.gcc	4	700	46.0	**703**	**45.8**	704	45.8	4	700	46.0	**703**	**45.8**	704	45.8
429.mcf	4	809	45.1	**810**	**45.0**	810	45.0	4	**784**	**46.5**	784	46.5	783	46.6
445.gobmk	4	**807**	**52.0**	805	52.1	808	51.9	4	732	57.3	740	56.7	**736**	**57.0**
456.hmmer	4	892	41.9	**892**	**41.8**	892	41.8	4	705	53.0	**706**	**52.9**	707	52.8
458.sjeng	4	**1017**	**47.6**	1013	47.8	1021	47.4	4	907	53.4	**907**	**53.4**	905	53.5
462.libquantum	4	**1807**	**45.9**	1810	45.8	1796	46.1	4	**1632**	**50.8**	1636	50.7	1632	50.8
464.h264ref	4	1089	81.3	1088	81.4	**1089**	**81.3**	4	**1031**	**85.9**	1031	85.9	1032	85.8
471.omnetpp	4	**735**	**34.0**	736	34.0	734	34.1	4	**693**	**36.1**	692	36.1	694	36.0
473.astar	4	830	33.8	**831**	**33.8**	832	33.7	4	**773**	**36.3**	772	36.4	774	36.3
483.xalancbmk	4	466	59.2	467	59.0	**467**	**59.1**	4	466	59.2	467	59.0	**467**	**59.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:     Enabled
Adjacent Sector Prefetch: Disabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
taskset was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.0.023/bin/icc
        -L/opt/intel/cce/10.0.023/lib
        -I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
        -L/opt/intel/cce/10.0.023/lib
        -I/opt/intel/cce/10.0.023/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast`

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.35.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.35.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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