SPEC® CFP2006 Result

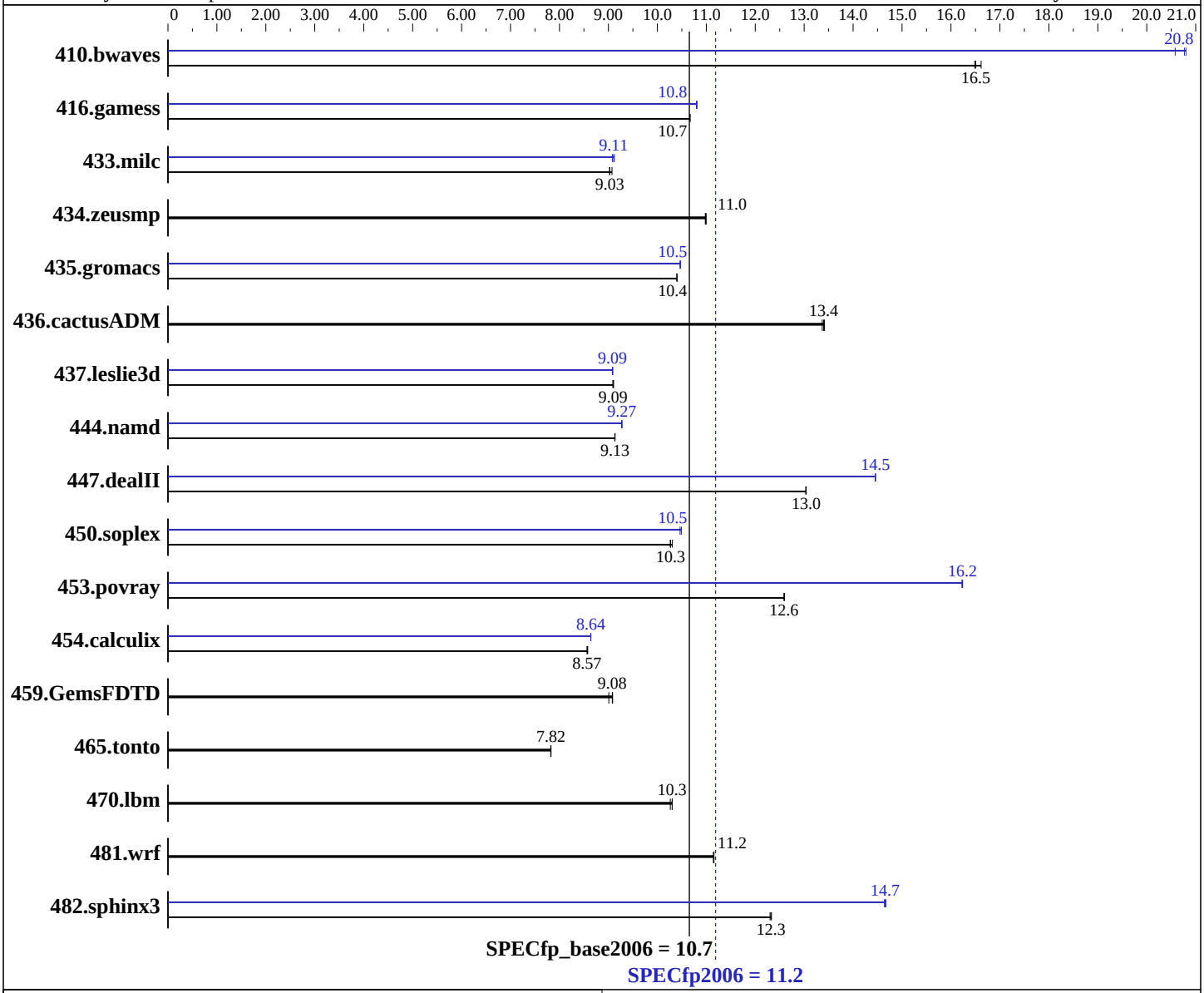
Supermicro
Motherboard PDSMU

SPECfp®2006 = 11.2

SPECfp_base2006 = 10.7

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2007
Hardware Availability: Dec-2006
Software Availability: Mar-2007



Hardware

CPU Name:	Intel Core 2 Duo E4300
CPU Characteristics:	1.8 GHz, 800 MHz bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

Continued on next page

Software

Operating System:	Windows Server 2003 Enterprise Edition w/ SP1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit

Continued on next page

SPEC CFP2006 Result

Supermicro Motherboard PDSMU

SPECfp2006 = 11.2

SPECfp_base2006 = 10.7

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2007
Hardware Availability: Dec-2006
Software Availability: Mar-2007

L3 Cache: None
Other Cache: None
Memory: 4 GB (2 X 2GB ECC, CL4, 533MHz, UBDIMM)
Disk Subsystem: 250GB SATA, 7200RPM
Other Hardware: None

Other Software: SmartHeap Library Version 8.0

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	818	16.6	**823**	**16.5**	824	16.5	653	20.8	660	20.6	**654**	**20.8**
416.gamess	**1836**	**10.7**	1836	10.7	1835	10.7	1813	10.8	**1812**	**10.8**	1812	10.8
433.milc	1012	9.07	**1017**	**9.03**	1017	9.02	1006	9.12	1011	9.08	**1008**	**9.11**
434.zeusmp	827	11.0	829	11.0	**828**	**11.0**	827	11.0	829	11.0	**828**	**11.0**
435.gromacs	686	10.4	687	10.4	**686**	**10.4**	682	10.5	**682**	**10.5**	682	10.5
436.cactusADM	891	13.4	**892**	**13.4**	894	13.4	891	13.4	**892**	**13.4**	894	13.4
437.leslie3d	1032	9.11	**1034**	**9.09**	1034	9.09	**1035**	**9.09**	1034	9.09	1035	9.08
444.namd	**878**	**9.13**	878	9.13	878	9.13	865	9.27	864	9.28	**865**	**9.27**
447.dealII	877	13.0	878	13.0	**877**	**13.0**	792	14.5	**791**	**14.5**	791	14.5
450.soplex	809	10.3	**812**	**10.3**	813	10.3	795	10.5	**795**	**10.5**	797	10.5
453.povray	422	12.6	**423**	**12.6**	423	12.6	328	16.2	328	16.2	**328**	**16.2**
454.calculix	962	8.58	**963**	**8.57**	963	8.56	**955**	**8.64**	955	8.64	955	8.64
459.GemsFDTD	1178	9.01	**1169**	**9.08**	1168	9.09	1178	9.01	**1169**	**9.08**	1168	9.09
465.tonto	1257	7.83	**1258**	**7.82**	1258	7.82	1257	7.83	**1258**	**7.82**	1258	7.82
470.lbm	1339	10.3	1333	10.3	**1334**	**10.3**	1339	10.3	1333	10.3	**1334**	**10.3**
481.wrf	1001	11.2	**1001**	**11.2**	1002	11.1	1001	11.2	**1001**	**11.2**	1002	11.1
482.sphinx3	1580	12.3	**1582**	**12.3**	1584	12.3	1331	14.6	1328	14.7	**1330**	**14.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-815TQ-R450U case.
For a general system, a 420W (minimum) ATX12V power supply [8-pin +12V AND 24-pin is recommended
to assure system stability].
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon3000/3010/PDSMU.cfm
The system bus runs at 800 MHz.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: basepeak = yes

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxB -Qipo -O3
-Qprec-div- /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
libguide.lib libguide40.lib -link /FORCE:MULTIPLE

416.gamess: Same as 410.bwaves

434.zeusmp: basepeak = yes

437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
-link /FORCE:MULTIPLE

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib libguide.lib libguide40.lib -link /FORCE:MULTIPLE`

436.cactusADM: `basepeak = yes`

454.calculix: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000 -link /FORCE:MULTIPLE`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 13:00:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 July 2007.