SPEC® CFP2006 Result

HITACHI

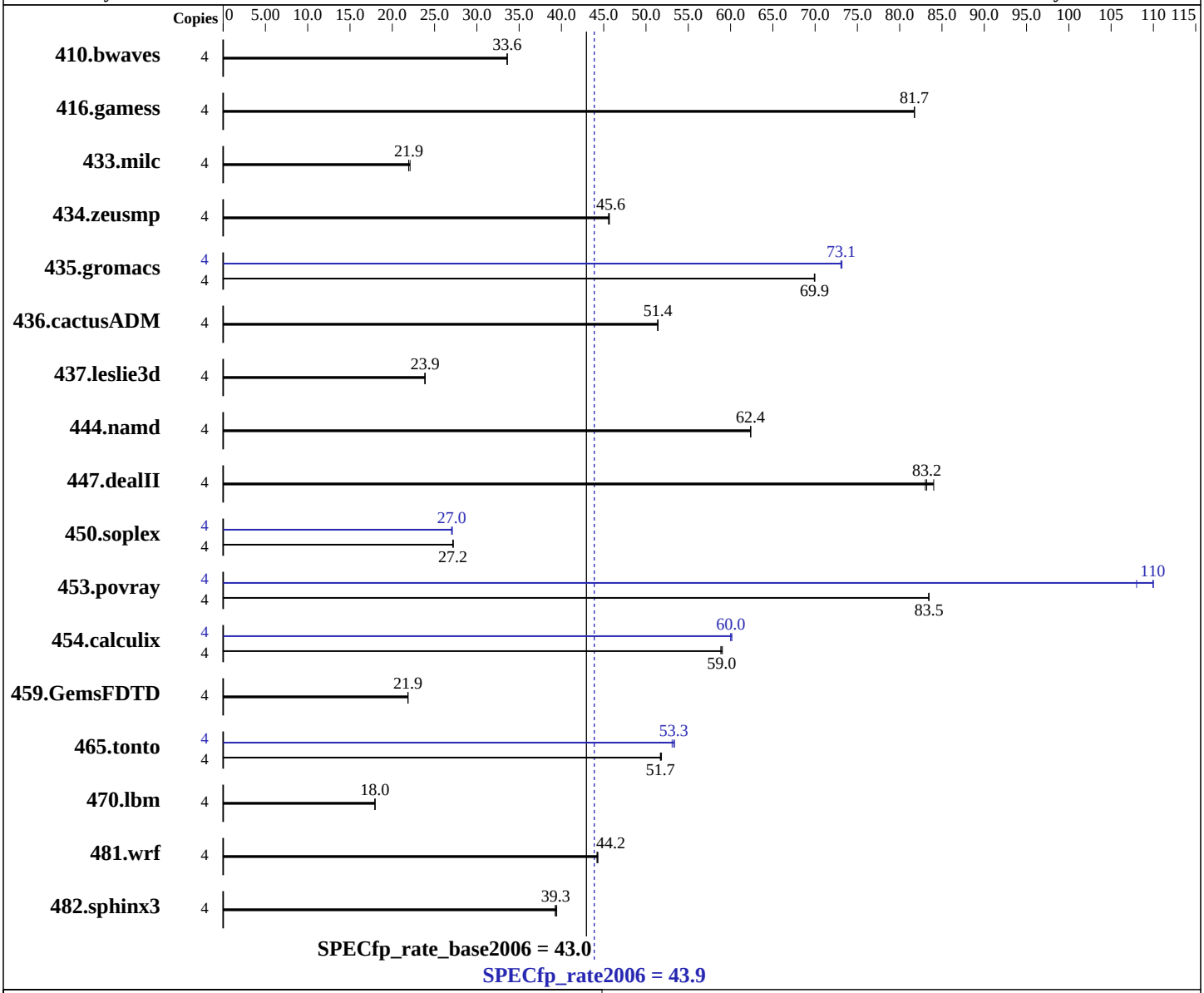
BladeSymphony BS1000 (Intel Xeon 5160)

SPECfp®_rate2006 = 43.9

SPECfp_rate_base2006 = 43.0

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jun-2007
Hardware Availability: Sep-2006
Software Availability: Mar-2007



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	1333MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Red Hat Enterprise Linux AS release 4 (Nahant Update 3) Kernel 2.6.9-34.ELsmp on an x86_64
Compiler:	Intel C++ Compiler for EM64T version 9.1 build 20070109 Intel Fortran Compiler for EM64T version 9.1 build 20070109
Auto Parallel:	No

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L3 Cache: None
Other Cache: None
Memory: 16 GB(8 x 2 GB PC2-4200F CAS 4-4-4)
Disk Subsystem: 1 x 73GB 10000rpm SAS
Other Hardware: None

File System: ext3
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1619	33.6	**1618**	**33.6**	1618	33.6	4	1619	33.6	**1618**	**33.6**	1618	33.6
416.gamess	4	958	81.7	958	81.7	**958**	**81.7**	4	958	81.7	958	81.7	**958**	**81.7**
433.milc	4	**1673**	**21.9**	1674	21.9	1661	22.1	4	**1673**	**21.9**	1674	21.9	1661	22.1
434.zeusmp	4	798	45.6	797	45.7	**798**	**45.6**	4	798	45.6	797	45.7	**798**	**45.6**
435.gromacs	4	409	69.9	**408**	**69.9**	408	70.0	4	391	73.0	390	73.2	**391**	**73.1**
436.cactusADM	4	930	51.4	**930**	**51.4**	930	51.4	4	930	51.4	**930**	**51.4**	930	51.4
437.leslie3d	4	**1575**	**23.9**	1574	23.9	1577	23.8	4	**1575**	**23.9**	1574	23.9	1577	23.8
444.namd	4	514	62.4	**514**	**62.4**	514	62.4	4	514	62.4	**514**	**62.4**	514	62.4
447.dealII	4	551	83.0	**550**	**83.2**	545	84.0	4	551	83.0	**550**	**83.2**	545	84.0
450.soplex	4	**1228**	**27.2**	1226	27.2	1229	27.1	4	**1234**	**27.0**	1234	27.0	1231	27.1
453.povray	4	255	83.5	255	83.4	**255**	**83.5**	4	197	108	**194**	**110**	193	110
454.calculix	4	561	58.8	**560**	**59.0**	559	59.0	4	549	60.2	**550**	**60.0**	550	60.0
459.GemsFDTD	4	1943	21.8	1942	21.9	**1942**	**21.9**	4	1943	21.8	1942	21.9	**1942**	**21.9**
465.tonto	4	**761**	**51.7**	761	51.7	759	51.8	4	737	53.4	741	53.1	**739**	**53.3**
470.lbm	4	3062	18.0	3061	18.0	**3061**	**18.0**	4	3062	18.0	3061	18.0	**3061**	**18.0**
481.wrf	4	1008	44.3	**1010**	**44.2**	1010	44.2	4	1008	44.3	**1010**	**44.2**	1010	44.2
482.sphinx3	4	1985	39.3	**1983**	**39.3**	1976	39.4	4	1985	39.3	**1983**	**39.3**	1976	39.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Continued on next page

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -prof_gen(pass 1) -prof_use(pass 2) -fast

453.povray: Same as 450.soplex

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:

Continued on next page

Peak Optimization Flags (Continued)

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/ic91_fp.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/ic91_fp.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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