



SPEC® CFP2006 Result

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HITACHI

BladeSymphony BS1000 (Intel Xeon 5160)

SPECfp®_rate2006 = 41.9

CPU2006 license: 872

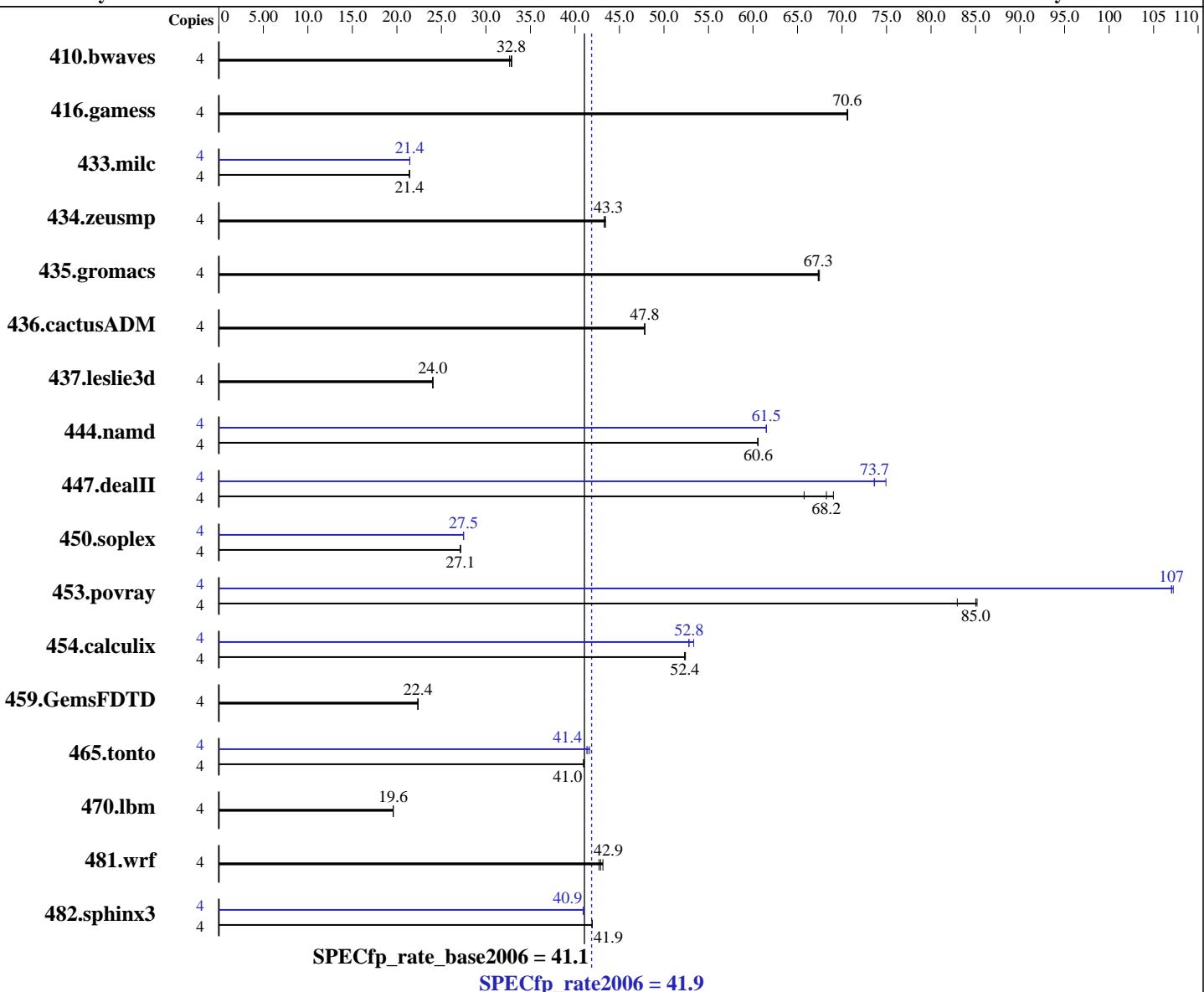
Test date: May-2007

Test sponsor: HITACHI

Hardware Availability: Sep-2006

Tested by: HITACHI

Software Availability: Dec-2006



Hardware		Software	
CPU Name:	Intel Xeon 5160	Operating System:	Microsoft Windows Server 2003 R2, Enterprise x64 Edition
CPU Characteristics:	1333MHz system bus	Compiler:	Intel C++ Compiler for IA32 version 9.1 Build 20061103Z
CPU MHz:	3000		Intel Fortran Compiler for IA32 version 9.1 Build 20061103Z
FPU:	Integrated		Microsoft Visual Studio .NET 2003 (for libraries)
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip	Auto Parallel:	No
CPU(s) orderable:	1, 2 chips	File System:	NTFS
Primary Cache:	32 KB I + 32 KB D on chip per core		Continued on next page
Secondary Cache:	4 MB I+D on chip per chip		

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L3 Cache: None
Other Cache: None
Memory: 16 GB(8 x 2 GB PC2-4200F)
Disk Subsystem: 1 x 73GB 10000rpm SAS
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1665	32.7	1652	32.9	1655	32.8	4	1665	32.7	1652	32.9	1655	32.8
416.gamess	4	1109	70.6	1109	70.6	1110	70.6	4	1109	70.6	1109	70.6	1110	70.6
433.milc	4	1714	21.4	1715	21.4	1716	21.4	4	1711	21.5	1712	21.4	1713	21.4
434.zeusmp	4	840	43.3	841	43.3	838	43.4	4	840	43.3	841	43.3	838	43.4
435.gromacs	4	424	67.3	423	67.5	424	67.3	4	424	67.3	423	67.5	424	67.3
436.cactusADM	4	999	47.8	999	47.8	999	47.8	4	999	47.8	999	47.8	999	47.8
437.leslie3d	4	1564	24.0	1564	24.0	1565	24.0	4	1564	24.0	1564	24.0	1565	24.0
444.namd	4	530	60.6	530	60.5	530	60.6	4	522	61.5	522	61.5	522	61.5
447.dealII	4	696	65.7	663	69.0	671	68.2	4	611	74.9	621	73.7	622	73.6
450.soplex	4	1229	27.1	1229	27.1	1230	27.1	4	1213	27.5	1214	27.5	1214	27.5
453.povray	4	250	85.2	250	85.0	257	82.9	4	199	107	199	107	199	107
454.calculix	4	630	52.4	631	52.3	630	52.4	4	625	52.8	619	53.3	625	52.8
459.GemsFDTD	4	1899	22.4	1898	22.4	1897	22.4	4	1899	22.4	1898	22.4	1897	22.4
465.tonto	4	961	41.0	961	41.0	959	41.0	4	950	41.4	952	41.3	945	41.7
470.lbm	4	2806	19.6	2807	19.6	2806	19.6	4	2806	19.6	2807	19.6	2806	19.6
481.wrf	4	1036	43.1	1046	42.7	1042	42.9	4	1036	43.1	1046	42.7	1042	42.9
482.sphinx3	4	1859	41.9	1859	41.9	1859	41.9	4	1905	40.9	1904	41.0	1904	40.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

 icl -Qvc7.1 -Qc99

C++ benchmarks:

 icl -Qvc7.1

Fortran benchmarks:

 ifort

Benchmarks using both Fortran and C:

 icl -Qvc7.1 -Qc99 ifort



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Base Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
 444.namd: -TP
 447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
   -DBOOST_NO_INTRINSIC_WCHAR_T
 453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
 481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:

```
-fast /F9500000000 shlw32m.lib           -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F9500000000 shlw32m.lib
   -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
-fast /F9500000000           -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:

```
-fast /F9500000000           -link /FORCE:MULTIPLE
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc7.1 -Qc99 ifort
```

Peak Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
 444.namd: -TP
 447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
   -DBOOST_NO_INTRINSIC_WCHAR_T
```

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Peak Portability Flags (Continued)

453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
/F9500000000 shlw32m.lib -link /FORCE:MULTIPLE
470.lbm: basepeak = yes
482.sphinx3: Same as 433.milc

C++ benchmarks:

ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F9500000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
/F9500000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F9500000000
-link /FORCE:MULTIPLE
481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/ic91.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/ic91.xml>

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For other inquiries, please contact webmaster@spec.org.

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