SPEC® CFP2006 Result
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Bull SAS

NovaScale B260 (Intel Xeon processor E5310,1.60GHz)

$SPECfp®\_rate2006 =$ 37.7
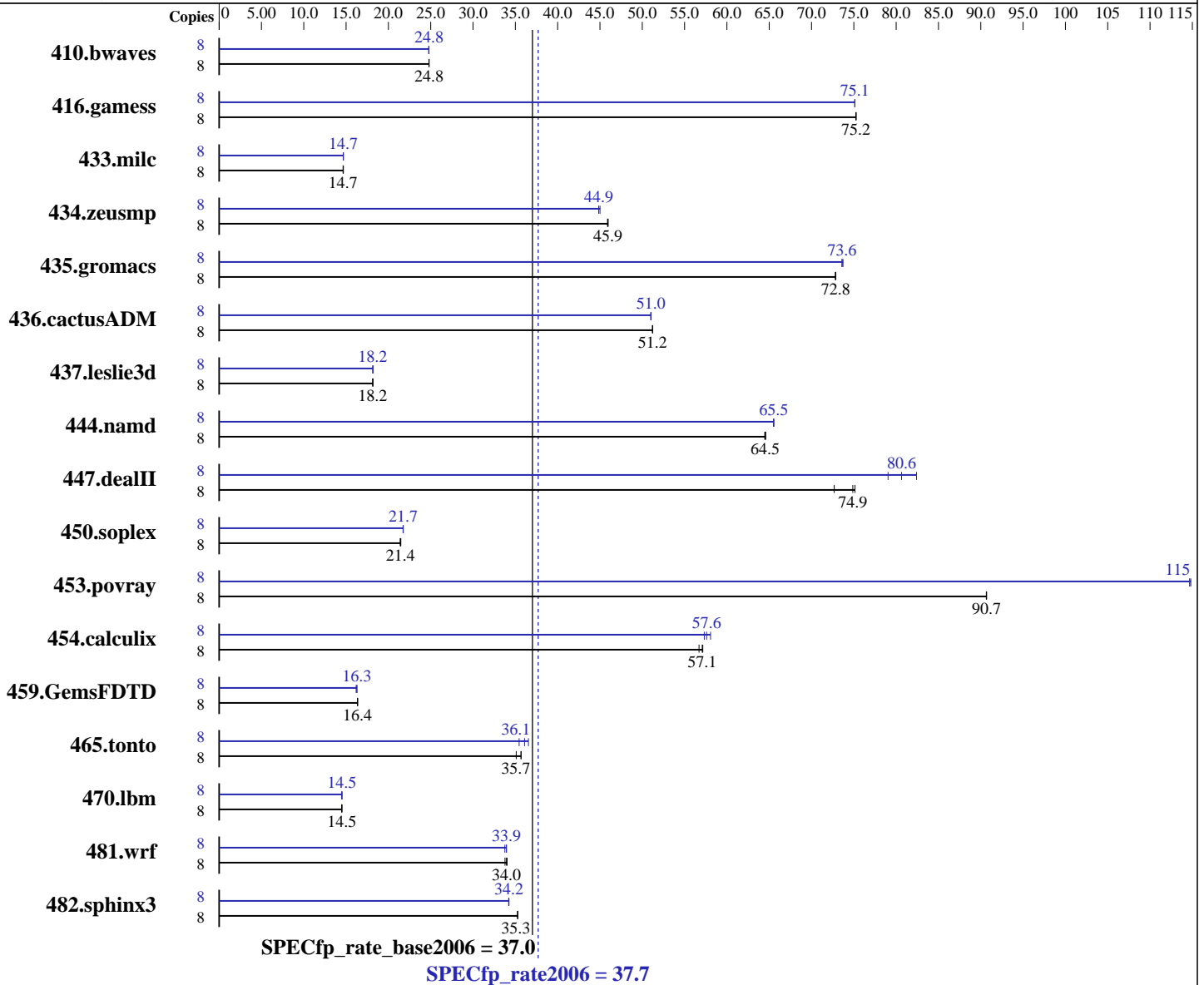
$SPECfp\_rate\_base2006 =$ 37.0

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Apr-2007
Hardware Availability: Jan-2007
Software Availability: Dec-2006

SPECfp_rate_base2006 = 37.0

SPECfp_rate2006 = 37.7

Hardware

CPU Name: Intel Xeon E5310
CPU Characteristics: 1.60 GHz, 8 MB L2, 1066 MHz bus
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1 to 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System: Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.033 Build no 20061103Z
Intel Fortran Compiler for IA32 version 9.1
Package ID W_FC_C_9.1.033 Build no 20061103Z
Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel: No
File System: NTFS
System State: Default

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 10000 RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**4384**	**24.8**	4383	24.8	4386	24.8	8	**4385**	**24.8**	4391	24.8	4385	24.8
416.gamess	8	2082	75.2	**2082**	**75.2**	2081	75.3	8	2086	75.1	2085	75.1	**2085**	**75.1**
433.milc	8	**5012**	**14.7**	5014	14.6	5002	14.7	8	5006	14.7	5004	14.7	**5004**	**14.7**
434.zeusmp	8	1586	45.9	1584	46.0	**1585**	**45.9**	8	**1623**	**44.9**	1618	45.0	1623	44.9
435.gromacs	8	784	72.8	**784**	**72.8**	784	72.9	8	775	73.7	**776**	**73.6**	776	73.6
436.cactusADM	8	**1867**	**51.2**	1866	51.2	1867	51.2	8	**1874**	**51.0**	1874	51.0	1873	51.0
437.leslie3d	8	**4142**	**18.2**	4144	18.1	4139	18.2	8	**4141**	**18.2**	4137	18.2	4141	18.2
444.namd	8	**994**	**64.5**	995	64.5	993	64.6	8	979	65.5	979	65.5	**979**	**65.5**
447.dealII	8	1259	72.7	**1222**	**74.9**	1218	75.1	8	**1135**	**80.6**	1158	79.0	1111	82.4
450.soplex	8	3118	21.4	3108	21.5	**3114**	**21.4**	8	3070	21.7	3066	21.8	**3069**	**21.7**
453.povray	8	469	90.7	**469**	**90.7**	469	90.7	8	**371**	**115**	371	115	371	115
454.calculix	8	**1156**	**57.1**	1164	56.7	1155	57.2	8	1137	58.1	1151	57.3	**1146**	**57.6**
459.GemsFDTD	8	**5191**	**16.4**	5194	16.3	5185	16.4	8	5244	16.2	5212	16.3	**5215**	**16.3**
465.tonto	8	2243	35.1	2206	35.7	**2207**	**35.7**	8	2221	35.4	2154	36.5	**2181**	**36.1**
470.lbm	8	7576	14.5	**7580**	**14.5**	7593	14.5	8	7574	14.5	7581	14.5	**7580**	**14.5**
481.wrf	8	2645	33.8	2626	34.0	**2632**	**34.0**	8	2647	33.8	**2632**	**33.9**	2632	34.0
482.sphinx3	8	**4419**	**35.3**	4421	35.3	4419	35.3	8	4553	34.2	4558	34.2	**4554**	**34.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

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Base Portability Flags

```
   436.cactusADM: -Qlowercase /assume:underscore
       444.namd: -TP
       447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
               -DBOOST_NO_INTRINSIC_WCHAR_T
       453.povray: -DSPEC_CPU_WINDOWS_ICL
       454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
         481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:
```
    -fast /F950000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
    -fast -Qcxx_features /F950000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
    -fast /F950000000          -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
    -fast /F950000000          -link /FORCE:MULTIPLE
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
    icl -Qvc7.1
```

Fortran benchmarks:
```
    ifort
```

Benchmarks using both Fortran and C:
```
    icl -Qvc7.1 -Qc99 ifort
```

Peak Portability Flags

```
   436.cactusADM: -Qlowercase /assume:underscore
       444.namd: -TP
       447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
               -DBOOST_NO_INTRINSIC_WCHAR_T
```

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Test sponsor:	Bull SAS	**Hardware Availability:**	Jan-2007
Tested by:	Bull SAS	**Software Availability:**	Dec-2006

Peak Portability Flags (Continued)

```
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
   481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Peak Optimization Flags

C benchmarks:
```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
            -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
            -link /FORCE:MULTIPLE
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:42:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 May 2007.