SPEC® CFP2006 Result
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Supermicro Motherboard PDSBA+

$SPECfp^{®}\_rate2006 =$ 17.4
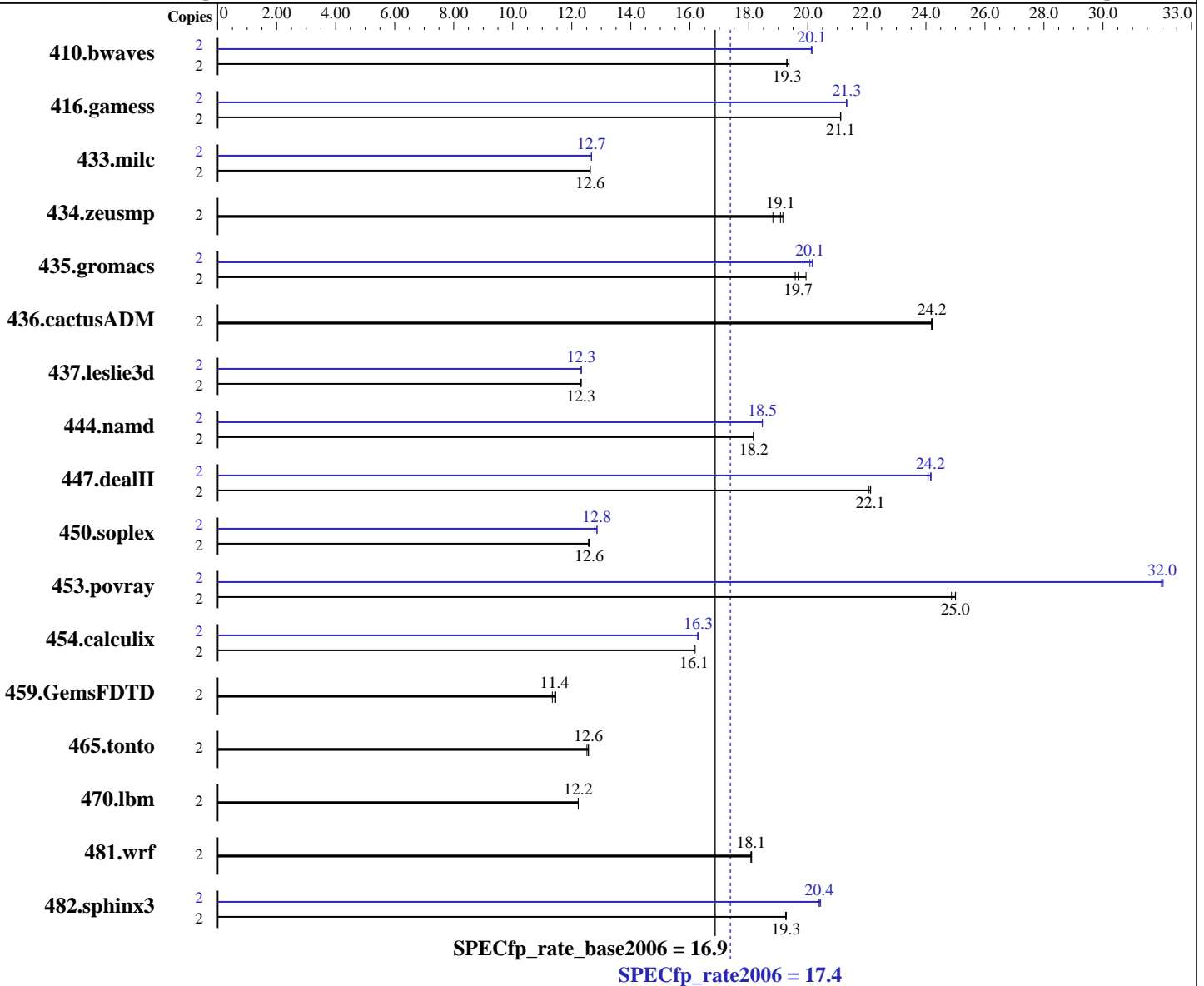
SPECfp_rate_base2006 = 16.9

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: May-2007
Hardware Availability: Apr-2007
Software Availability: Apr-2007

Copies		
410.bwaves	2	20.1
	2	19.3
416.gamess	2	21.3
	2	21.1
433.milc	2	12.7
	2	12.6
434.zeusmp	2	19.1
435.gromacs	2	20.1
	2	19.7
436.cactusADM	2	24.2
437.leslie3d	2	12.3
	2	12.3
444.namd	2	18.5
	2	18.2
447.dealII	2	24.2
	2	22.1
450.soplex	2	12.8
	2	12.6
453.povray	2	32.0
	2	25.0
454.calculix	2	16.3
	2	16.1
459.GemsFDTD	2	11.4
465.tonto	2	12.6
470.lbm	2	12.2
481.wrf	2	18.1
482.sphinx3	2	20.4
	2	19.3

SPECfp_rate_base2006 = 16.9
SPECfp_rate2006 = 17.4

Hardware

CPU Name: Intel Core 2 Duo E4300
CPU Characteristics: 1.8GHz, 800MHz bus
CPU MHz: 1800
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per chip

Continued on next page

Software

Operating System: Windows XP Professional w/ SP2
Compiler: Intel C++ Compiler for IA32 version 9.1
Build no 20070322Z
Intel Fortran Compiler for IA32 version 9.1
Build no 20070322Z
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 32-bit

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L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2X 1GB ECC, CL4, 533MHz, UnBuffer)
Disk Subsystem:	150GB SATA, 7200RPM
Other Hardware:	None

Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	**1408**	**19.3**	1404	19.4	1410	19.3	2	**1350**	**20.1**	1350	20.1	1351	20.1
416.gamess	2	1855	21.1	**1855**	**21.1**	1854	21.1	2	1838	21.3	**1837**	**21.3**	1837	21.3
433.milc	2	1455	12.6	1454	12.6	**1455**	**12.6**	2	1449	12.7	**1449**	**12.7**	1450	12.7
434.zeusmp	2	967	18.8	950	19.2	**954**	**19.1**	2	967	18.8	950	19.2	**954**	**19.1**
435.gromacs	2	716	19.9	**726**	**19.7**	730	19.6	2	709	20.1	**712**	**20.1**	720	19.8
436.cactusADM	2	988	24.2	987	24.2	**987**	**24.2**	2	988	24.2	987	24.2	**987**	**24.2**
437.leslie3d	2	**1527**	**12.3**	1527	12.3	1527	12.3	2	**1526**	**12.3**	1526	12.3	1526	12.3
444.namd	2	**883**	**18.2**	883	18.2	884	18.2	2	869	18.5	869	18.5	**869**	**18.5**
447.dealII	2	1034	22.1	**1034**	**22.1**	1037	22.1	2	**947**	**24.2**	950	24.1	946	24.2
450.soplex	2	1328	12.6	**1325**	**12.6**	1325	12.6	2	**1298**	**12.8**	1297	12.9	1305	12.8
453.povray	2	428	24.9	426	25.0	**426**	**25.0**	2	332	32.0	**333**	**32.0**	333	32.0
454.calculix	2	1022	16.1	1020	16.2	**1022**	**16.1**	2	1013	16.3	1015	16.3	**1013**	**16.3**
459.GemsFDTD	2	**1858**	**11.4**	1871	11.3	1852	11.5	2	**1858**	**11.4**	1871	11.3	1852	11.5
465.tonto	2	1573	12.5	**1567**	**12.6**	1566	12.6	2	1573	12.5	**1567**	**12.6**	1566	12.6
470.lbm	2	**2249**	**12.2**	2249	12.2	2249	12.2	2	**2249**	**12.2**	2249	12.2	2249	12.2
481.wrf	2	1235	18.1	**1236**	**18.1**	1236	18.1	2	1235	18.1	**1236**	**18.1**	1236	18.1
482.sphinx3	2	2023	19.3	2025	19.2	**2024**	**19.3**	2	1912	20.4	1908	20.4	**1912**	**20.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC733T-645 case,
To ensure system stability, a 450W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)] is required.
Product description located as of http://www.supermicro.com/products/motherboard/Core2Duo/965/PDSBA+.cfm
The system bus runs at 800 MHz

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
     444.namd: -TP
    447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
               -DBOOST_NO_INTRINSIC_WCHAR_T
    453.povray: -DSPEC_CPU_WINDOWS_ICL
   454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
       481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:
```
-fast /F950000000 shlw32m.lib              -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F950000000 shlw32m.lib
           -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-fast /F950000000              -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
-fast /F950000000              -link /FORCE:MULTIPLE
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

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Peak Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
   444.namd: -TP
  447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
             -DBOOST_NO_INTRINSIC_WCHAR_T
  453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
     481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Peak Optimization Flags

C benchmarks:

```
   433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
             shlw32m.lib          -link /FORCE:MULTIPLE

   470.lbm: basepeak = yes

 482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxB -Qipo -O3
             -Qprec-div- /F950000000 shlw32m.lib
                        -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
   -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
   /F950000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
  410.bwaves: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
             libguide.lib libguide40.lib          -link /FORCE:MULTIPLE

  416.gamess: Same as 410.bwaves

  434.zeusmp: basepeak = yes

  437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
                        -link /FORCE:MULTIPLE

  459.GemsFDTD: basepeak = yes

   465.tonto: basepeak = yes
```

Benchmarks using both Fortran and C:

```
  435.gromacs: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
             shlw32m.lib libguide.lib libguide40.lib
                        -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
              -link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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