SPEC® CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 2950 (Intel Xeon processor E5310, 1.60 GHz)

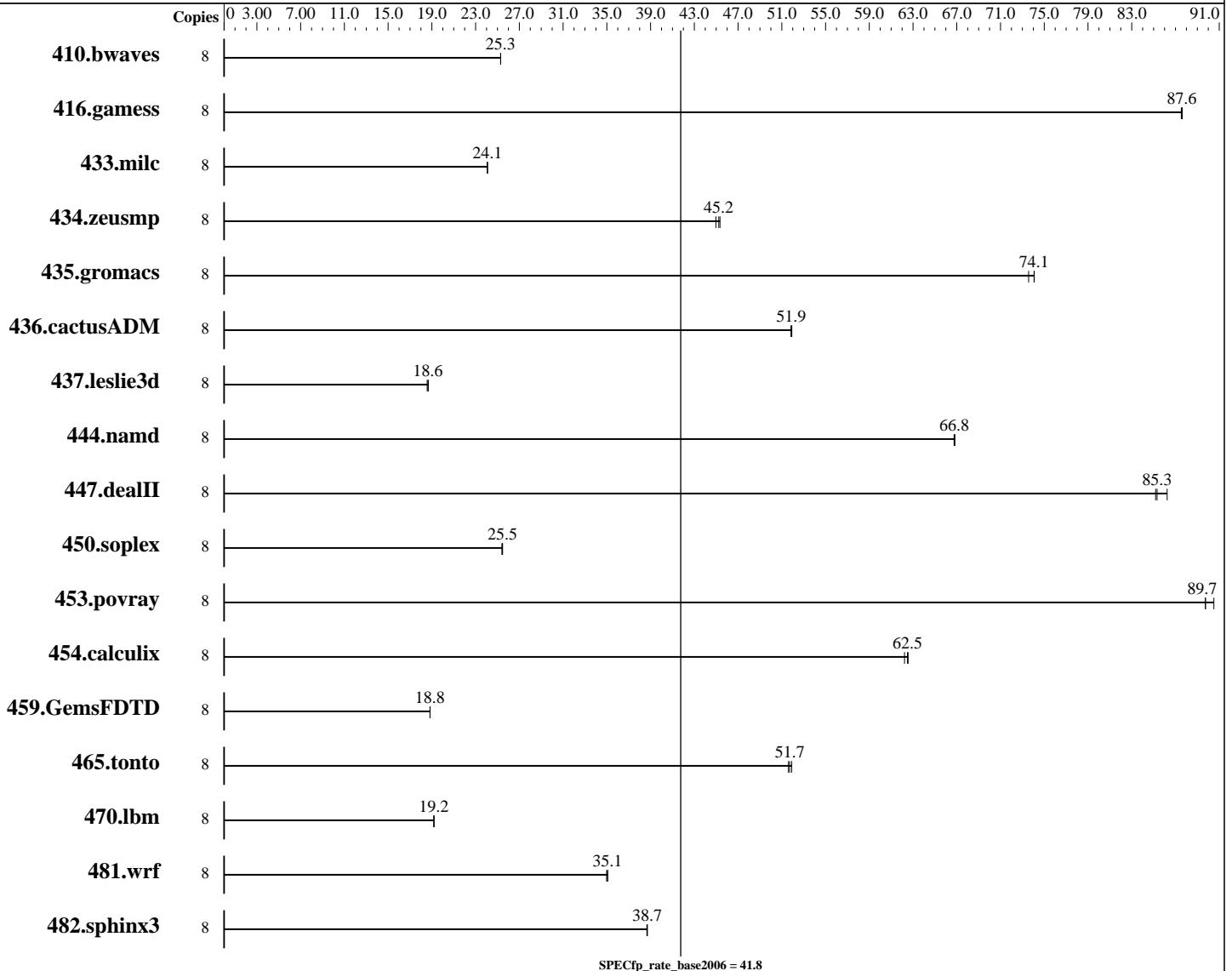$SPECfp®\_rate2006 =$ Not Run

$SPECfp\_rate\_base2006 =$ 41.8

CPU2006 license: 55	**Test date:** Mar-2007
Test sponsor: Dell Inc.	**Hardware Availability:** Dec-2006
Tested by: Dell Inc.	**Software Availability:** Nov-2006

Copies 0 3.00 7.00 11.0 15.0 19.0 23.0 27.0 31.0 35.0 39.0 43.0 47.0 51.0 55.0 59.0 63.0 67.0 71.0 75.0 79.0 83.0 91.0

Benchmark	Copies	Value
410.bwaves	8	25.3
416.gamess	8	87.6
433.milc	8	24.1
434.zeusmp	8	45.2
435.gromacs	8	74.1
436.cactusADM	8	51.9
437.leslie3d	8	18.6
444.namd	8	66.8
447.dealII	8	85.3
450.soplex	8	25.5
453.povray	8	89.7
454.calculix	8	62.5
459.GemsFDTD	8	18.8
465.tonto	8	51.7
470.lbm	8	19.2
481.wrf	8	35.1
482.sphinx3	8	38.7

SPECfp_rate_base2006 = 41.8

Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066 MHz Bus Speed
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 2950 (Intel Xeon processor E5310, 1.60 GHz)

$SPECfp\_rate2006 =$ Not Run

$SPECfp\_rate\_base2006 =$ 41.8

CPU2006 license: 55	**Test date:** Mar-2007
Test sponsor: Dell Inc.	**Hardware Availability:** Dec-2006
Tested by: Dell Inc.	**Software Availability:** Nov-2006

L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)	Base Pointers:	64-bit
Disk Subsystem:	1 x 73 GB SAS 15k RPM	Peak Pointers:	Not Applicable
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4299	25.3	4298	25.3	**4299**	**25.3**							
416.gamess	8	1788	87.6	1790	87.5	**1789**	**87.6**							
433.milc	8	3050	24.1	3051	24.1	**3050**	**24.1**							
434.zeusmp	8	**1609**	**45.2**	1619	45.0	1605	45.3							
435.gromacs	8	771	74.1	776	73.6	**771**	**74.1**							
436.cactusADM	8	1844	51.8	1841	51.9	**1843**	**51.9**							
437.leslie3d	8	4027	18.7	**4034**	**18.6**	4049	18.6							
444.namd	8	**961**	**66.8**	960	66.8	961	66.8							
447.dealII	8	**1073**	**85.3**	1074	85.2	1061	86.2							
450.soplex	8	2620	25.5	**2621**	**25.5**	2626	25.4							
453.povray	8	470	90.5	**474**	**89.7**	474	89.7							
454.calculix	8	1061	62.2	**1056**	**62.5**	1055	62.6							
459.GemsFDTD	8	4506	18.8	**4507**	**18.8**	4507	18.8							
465.tonto	8	1517	51.9	**1522**	**51.7**	1525	51.6							
470.lbm	8	5727	19.2	5729	19.2	**5728**	**19.2**							
481.wrf	8	**2548**	**35.1**	2555	35.0	2548	35.1							
482.sphinx3	8	4033	38.7	4029	38.7	**4030**	**38.7**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
    Hardware Prefetcher disabled in BIOS
    Adjacent Cache Line Prefetch disabled in BIOS
    "ulimit -s unlimited" set
```

Base Compiler Invocation

C benchmarks:
```
    icc
```

C++ benchmarks:
```
    icpc
```

Dell Inc.

PowerEdge 2950 (Intel Xeon processor E5310, 1.60 GHz)

$SPECfp\_rate2006 =$ Not Run

$SPECfp\_rate\_base2006 =$ 41.8

CPU2006 license: 55	**Test date:** Mar-2007
Test sponsor: Dell Inc.	**Hardware Availability:** Dec-2006
Tested by: Dell Inc.	**Software Availability:** Nov-2006

Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Base Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
      450.soplex: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
     459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
     482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-fast
```

Fortran benchmarks:
```
-fast
```

Benchmarks using both Fortran and C:
```
-fast
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell-ic91-flags.xml

Dell Inc.

PowerEdge 2950 (Intel Xeon processor E5310, 1.60 GHz)

$SPECfp_rate2006 = $ Not Run

$SPECfp_rate_base2006 = $ 41.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2007
Hardware Availability: Dec-2006
Software Availability: Nov-2006

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:56:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 April 2007.