SPEC® CFP2006 Result

Hewlett-Packard Company

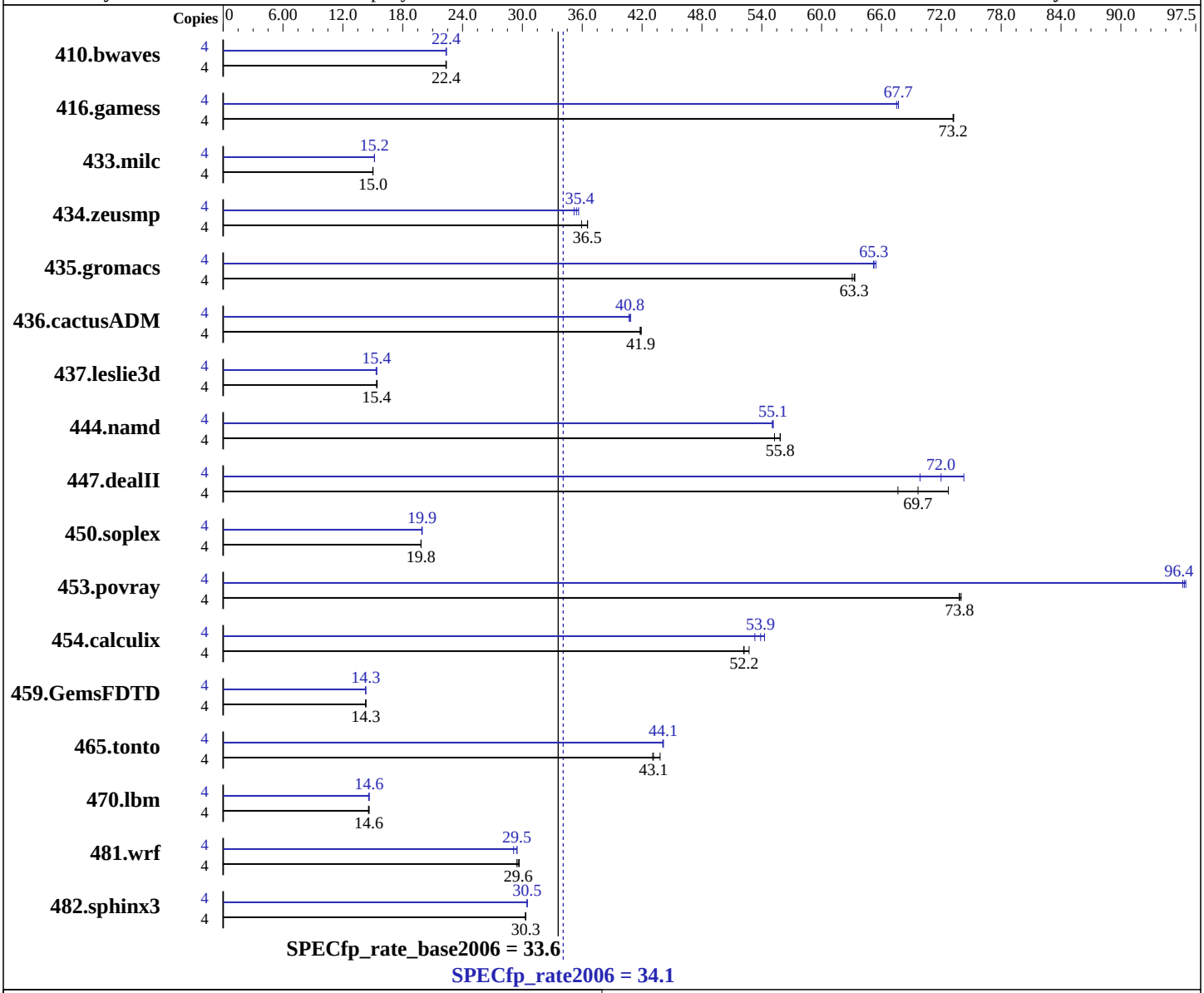
ProLiant BL480c
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 34.1

SPECfp_rate_base2006 = 33.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300F CL5)	Base Pointers:	64-bit
Disk Subsystem:	1x72 GB 10k SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2434	22.3	2429	22.4	**2431**	**22.4**	4	**2430**	**22.4**	2428	22.4	2433	22.3
416.gamess	4	1069	73.2	1070	73.2	**1070**	**73.2**	4	**1157**	**67.7**	1157	67.7	1160	67.5
433.milc	4	2447	15.0	**2444**	**15.0**	2444	15.0	4	2420	15.2	2420	15.2	**2420**	**15.2**
434.zeusmp	4	**997**	**36.5**	996	36.5	1013	35.9	4	1021	35.6	1035	35.2	**1028**	**35.4**
435.gromacs	4	453	63.1	451	63.3	**451**	**63.3**	4	**438**	**65.3**	436	65.5	438	65.2
436.cactusADM	4	1144	41.8	**1142**	**41.9**	1140	41.9	4	**1172**	**40.8**	1170	40.9	1175	40.7
437.leslie3d	4	2435	15.4	**2442**	**15.4**	2443	15.4	4	**2445**	**15.4**	2452	15.3	2442	15.4
444.namd	4	574	55.9	580	55.3	**575**	**55.8**	4	583	55.1	**582**	**55.1**	582	55.2
447.dealII	4	676	67.6	**657**	**69.7**	629	72.7	4	**636**	**72.0**	616	74.3	655	69.9
450.soplex	4	1681	19.8	**1681**	**19.8**	1682	19.8	4	1671	20.0	1675	19.9	**1673**	**19.9**
453.povray	4	288	73.8	288	74.0	**288**	**73.8**	4	221	96.2	**221**	**96.4**	220	96.5
454.calculix	4	626	52.7	632	52.2	**632**	**52.2**	4	608	54.3	**612**	**53.9**	619	53.3
459.GemsFDTD	4	**2968**	**14.3**	2966	14.3	2969	14.3	4	**2968**	**14.3**	2968	14.3	2975	14.3
465.tonto	4	914	43.1	898	43.8	**912**	**43.1**	4	892	44.1	893	44.1	**892**	**44.1**
470.lbm	4	3753	14.6	**3765**	**14.6**	3773	14.6	4	**3754**	**14.6**	3753	14.6	3769	14.6
481.wrf	4	**1511**	**29.6**	1505	29.7	1517	29.5	4	1534	29.1	**1517**	**29.5**	1516	29.5
482.sphinx3	4	2569	30.3	**2570**	**30.3**	2573	30.3	4	**2558**	**30.5**	2559	30.5	2557	30.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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