SPEC® CFP2006 Result

Itautec

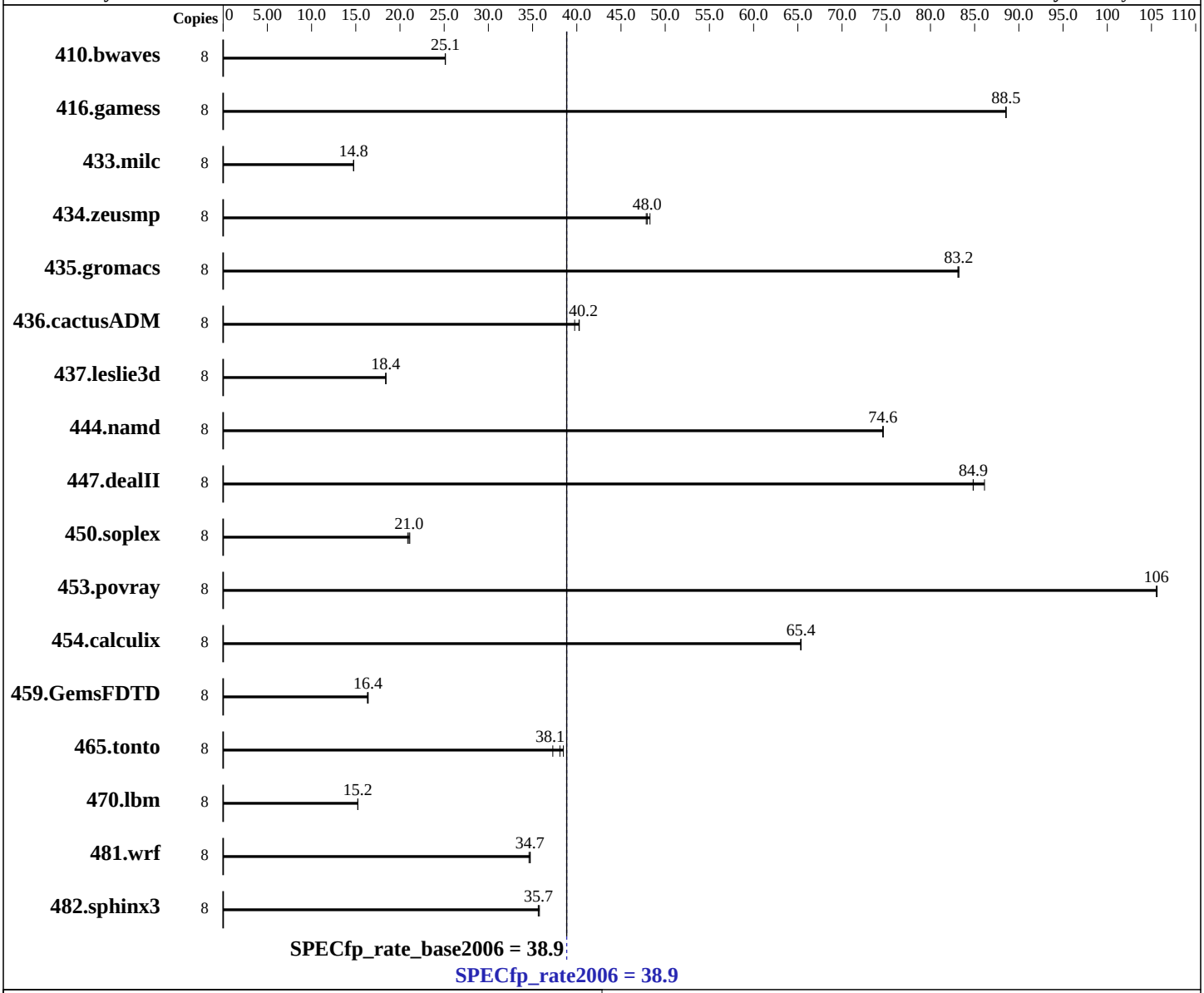
Servidor Itautec LX201 (Intel Xeon E5320)

SPECfp®_rate2006 = 38.9

SPECfp_rate_base2006 = 38.9

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS

Continued on next page

SPEC CFP2006 Result

Itautec

Servidor Itautec LX201 (Intel Xeon E5320)

SPECfp_rate2006 = 38.9

SPECfp_rate_base2006 = 38.9

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006

L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 120 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4329	25.1	4325	25.1	**4326**	**25.1**	8	4329	25.1	4325	25.1	**4326**	**25.1**
416.gamess	8	1769	88.5	**1769**	**88.5**	1769	88.6	8	1769	88.5	**1769**	**88.5**	1769	88.6
433.milc	8	4980	14.7	4973	14.8	**4974**	**14.8**	8	4980	14.7	4973	14.8	**4974**	**14.8**
434.zeusmp	8	1508	48.3	**1518**	**48.0**	1521	47.9	8	1508	48.3	**1518**	**48.0**	1521	47.9
435.gromacs	8	686	83.2	**687**	**83.2**	687	83.1	8	686	83.2	**687**	**83.2**	687	83.1
436.cactusADM	8	2373	40.3	2404	39.8	**2375**	**40.2**	8	2373	40.3	2404	39.8	**2375**	**40.2**
437.leslie3d	8	4088	18.4	4086	18.4	**4087**	**18.4**	8	4088	18.4	4086	18.4	**4087**	**18.4**
444.namd	8	860	74.6	**860**	**74.6**	859	74.7	8	860	74.6	**860**	**74.6**	859	74.7
447.dealII	8	1063	86.1	1079	84.8	**1079**	**84.9**	8	1063	86.1	1079	84.8	**1079**	**84.9**
450.soplex	8	3160	21.1	**3174**	**21.0**	3197	20.9	8	3160	21.1	**3174**	**21.0**	3197	20.9
453.povray	8	403	106	403	106	**403**	**106**	8	403	106	403	106	**403**	**106**
454.calculix	8	**1010**	**65.4**	1011	65.3	1010	65.4	8	**1010**	**65.4**	1011	65.3	1010	65.4
459.GemsFDTD	8	5175	16.4	**5180**	**16.4**	5202	16.3	8	5175	16.4	**5180**	**16.4**	5202	16.3
465.tonto	8	2111	37.3	2045	38.5	**2067**	**38.1**	8	2111	37.3	2045	38.5	**2067**	**38.1**
470.lbm	8	7217	15.2	**7214**	**15.2**	7212	15.2	8	7217	15.2	**7214**	**15.2**	7212	15.2
481.wrf	8	2583	34.6	2572	34.7	**2575**	**34.7**	8	2583	34.6	2572	34.7	**2575**	**34.7**
482.sphinx3	8	**4369**	**35.7**	4358	35.8	4375	35.6	8	**4369**	**35.7**	4358	35.8	4375	35.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `basepeak = yes`

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:48:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 March 2007.