



# SPEC® CFP2006 Result

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**Itautec**

**SPECfp®\_rate2006 = 37.8**

Servidor Itautec MX221 (Intel Xeon E5310)

**SPECfp\_rate\_base2006 = 37.8**

**CPU2006 license:** 9001

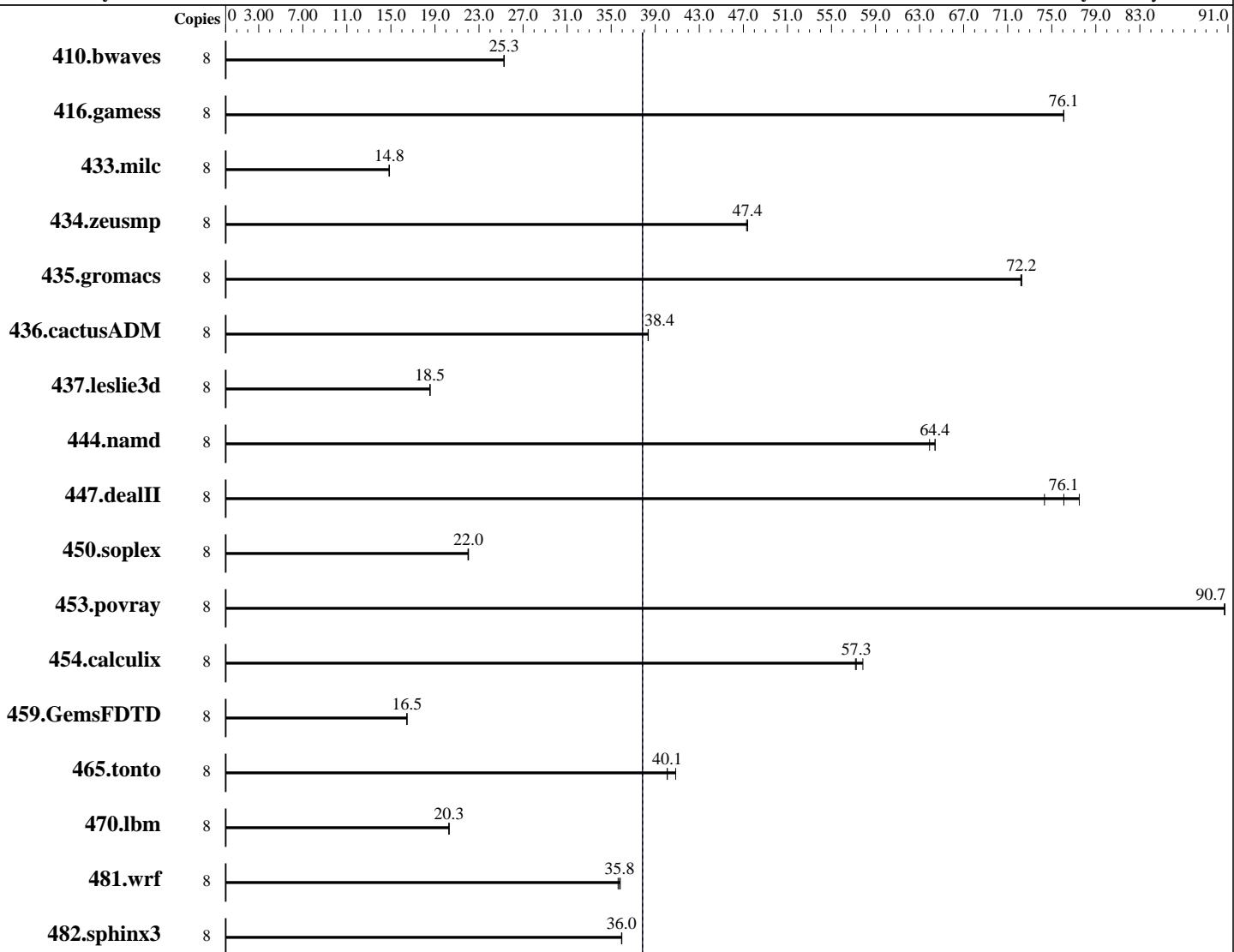
**Test date:** Feb-2007

**Test sponsor:** Itautec

**Hardware Availability:** Feb-2007

**Tested by:** Itautec

**Software Availability:** May-2006



**SPECfp\_rate\_base2006 = 37.8**

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## Hardware

CPU Name: Intel Xeon E5310  
CPU Characteristics: 1066MHz system bus  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
CPU(s) orderable: 1-2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

## Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)  
Compiler: Intel C++ Compiler for IA32 version 9.1  
Package ID W\_CC\_C\_9.1.025 Build no 20060519Z  
Intel Fortran Compiler for IA32 version 9.1  
Package ID W\_FC\_C\_9.1.025 Build no 20060519Z  
Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)  
Auto Parallel: No  
File System: NTFS

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L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)  
 Disk Subsystem: 73 GB SCSI, 10000RPM  
 Other Hardware: None

System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: Microquill SmartHeap Library v.8.0 for SMP

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<b>4301</b>	<b>25.3</b>	4299	25.3	4304	25.3	8	<b>4301</b>	<b>25.3</b>	4299	25.3	4304	25.3
416.gamess	8	2059	76.1	2058	76.1	<b>2059</b>	<b>76.1</b>	8	2059	76.1	2058	76.1	<b>2059</b>	<b>76.1</b>
433.milc	8	4948	14.8	4942	14.9	<b>4947</b>	<b>14.8</b>	8	4948	14.8	4942	14.9	<b>4947</b>	<b>14.8</b>
434.zeusmp	8	1536	47.4	<b>1537</b>	<b>47.4</b>	1539	47.3	8	1536	47.4	<b>1537</b>	<b>47.4</b>	1539	47.3
435.gromacs	8	<b>791</b>	<b>72.2</b>	790	72.3	791	72.2	8	<b>791</b>	<b>72.2</b>	790	72.3	791	72.2
436.cactusADM	8	2492	38.4	2493	38.3	<b>2492</b>	<b>38.4</b>	8	2492	38.4	2493	38.3	<b>2492</b>	<b>38.4</b>
437.leslie3d	8	4057	18.5	4050	18.6	<b>4055</b>	<b>18.5</b>	8	4057	18.5	4050	18.6	<b>4055</b>	<b>18.5</b>
444.namd	8	1004	63.9	<b>996</b>	<b>64.4</b>	996	64.4	8	1004	63.9	<b>996</b>	<b>64.4</b>	996	64.4
447.dealII	8	1181	77.5	<b>1203</b>	<b>76.1</b>	1231	74.3	8	1181	77.5	<b>1203</b>	<b>76.1</b>	1231	74.3
450.soplex	8	3027	22.0	3033	22.0	<b>3028</b>	<b>22.0</b>	8	3027	22.0	3033	22.0	<b>3028</b>	<b>22.0</b>
453.povray	8	<b>469</b>	<b>90.7</b>	469	90.7	469	90.7	8	<b>469</b>	<b>90.7</b>	469	90.7	469	90.7
454.calculix	8	1141	57.9	1154	57.2	<b>1153</b>	<b>57.3</b>	8	1141	57.9	1154	57.2	<b>1153</b>	<b>57.3</b>
459.GemsFDTD	8	5162	16.4	<b>5159</b>	<b>16.5</b>	5155	16.5	8	5162	16.4	<b>5159</b>	<b>16.5</b>	5155	16.5
465.tonto	8	1964	40.1	1927	40.9	<b>1964</b>	<b>40.1</b>	8	1964	40.1	1927	40.9	<b>1964</b>	<b>40.1</b>
470.lbm	8	5425	20.3	<b>5422</b>	<b>20.3</b>	5419	20.3	8	5425	20.3	<b>5422</b>	<b>20.3</b>	5419	20.3
481.wrf	8	<b>2499</b>	<b>35.8</b>	2495	35.8	2508	35.6	8	<b>2499</b>	<b>35.8</b>	2495	35.8	2508	35.6
482.sphinx3	8	4335	36.0	4339	35.9	<b>4337</b>	<b>36.0</b>	8	4335	36.0	4339	35.9	<b>4337</b>	<b>36.0</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: May-2006

## Base Compiler Invocation (Continued)

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icl -Qvc7.1 -Qc99 ifort

## Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
-DBOOST\_NO\_INTRINSIC\_WCHAR\_T  
453.povray: -DSPEC\_CPU\_WINDOWS\_ICL  
454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Base Optimization Flags

C benchmarks:  
-fast /F950000000 sh1SMPMt.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
-fast -Qcxx\_features /F950000000 sh1SMPMt.lib  
-link /FORCE:MULTIPLE

Fortran benchmarks:  
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:  
-fast /F950000000 -link /FORCE:MULTIPLE

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes  
470.lbm: basepeak = yes  
482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaute-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaute-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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