SPEC® CFP2006 Result
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Hewlett-Packard Company

ProLiant DL380 G5
(2.66 GHz, Intel Xeon processor X5355)

$SPECfp^®\_rate2006 =$ 58.8

$SPECfp\_rate\_base2006 =$ 57.9

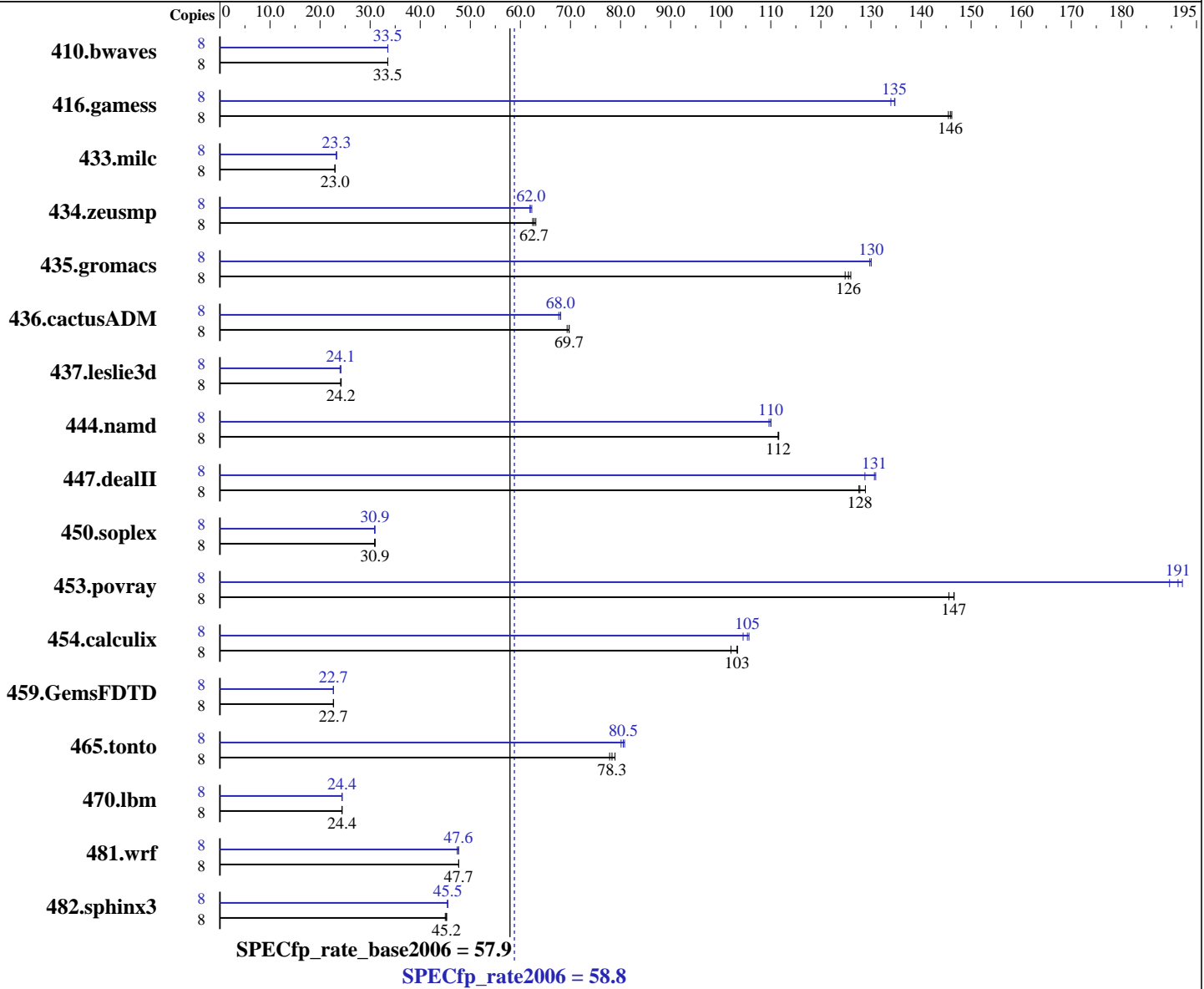|	
CPU2006 license: 3	**Test date:** Jan-2007
Test sponsor: Hewlett-Packard Company	**Hardware Availability:** Jan-2007
Tested by: Hewlett-Packard Company	**Software Availability:** Nov-2006

Copies

Benchmark	Copies	Value (blue)	Value (black)
410.bwaves	8 / 8	33.5	33.5
416.gamess	8 / 8	135	146
433.milc	8 / 8	23.3	23.0
434.zeusmp	8 / 8	62.0	62.7
435.gromacs	8 / 8	130	126
436.cactusADM	8 / 8	68.0	69.7
437.leslie3d	8 / 8	24.1	24.2
444.namd	8 / 8	110	112
447.dealII	8 / 8	131	128
450.soplex	8 / 8	30.9	30.9
453.povray	8 / 8	191	147
454.calculix	8 / 8	105	103
459.GemsFDTD	8 / 8	22.7	22.7
465.tonto	8 / 8	80.5	78.3
470.lbm	8 / 8	24.4	24.4
481.wrf	8 / 8	47.6	47.7
482.sphinx3	8 / 8	45.5	45.2

SPECfp_rate_base2006 = 57.9

SPECfp_rate2006 = 58.8

Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 4x2 MB L2 cache (shared)
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

Continued on next page

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	4x36 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
410.bwaves	8	3242	33.5	**3243**	**33.5**	3244	33.5	8	3243	33.5	**3243**	**33.5**	3241	33.5
416.gamess	8	**1074**	**146**	1072	146	1077	145	8	1169	134	1162	135	**1163**	**135**
433.milc	8	**3195**	**23.0**	3194	23.0	3197	23.0	8	3154	23.3	3155	23.3	**3155**	**23.3**
434.zeusmp	8	1166	62.4	**1160**	**62.7**	1154	63.1	8	**1175**	**62.0**	1169	62.3	1176	61.9
435.gromacs	8	458	125	**455**	**126**	453	126	8	**439**	**130**	440	130	439	130
436.cactusADM	8	1379	69.3	**1371**	**69.7**	1371	69.7	8	**1406**	**68.0**	1413	67.7	1405	68.1
437.leslie3d	8	3104	24.2	**3111**	**24.2**	3119	24.1	8	**3122**	**24.1**	3134	24.0	3114	24.2
444.namd	8	575	112	576	111	**575**	**112**	8	583	110	585	110	**583**	**110**
447.dealII	8	717	128	**716**	**128**	710	129	8	**700**	**131**	711	129	699	131
450.soplex	8	2153	31.0	**2156**	**30.9**	2159	30.9	8	2156	30.9	**2157**	**30.9**	2159	30.9
453.povray	8	290	147	292	146	**290**	**147**	8	224	190	221	192	**222**	**191**
454.calculix	8	**639**	**103**	647	102	639	103	8	632	104	**627**	**105**	625	106
459.GemsFDTD	8	3739	22.7	**3743**	**22.7**	3746	22.7	8	3750	22.6	**3743**	**22.7**	3740	22.7
465.tonto	8	**1006**	**78.3**	1012	77.8	998	78.9	8	974	80.8	**977**	**80.5**	983	80.1
470.lbm	8	**4508**	**24.4**	4508	24.4	4507	24.4	8	4507	24.4	4505	24.4	**4506**	**24.4**
481.wrf	8	**1874**	**47.7**	1873	47.7	1875	47.7	8	**1876**	**47.6**	1874	47.7	1886	47.4
482.sphinx3	8	3465	45.0	3443	45.3	**3448**	**45.2**	8	3432	45.4	**3428**	**45.5**	3427	45.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"ulimit -s unlimited" set
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Base Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
      447.dealII: -DSPEC_CPU_LP64
      450.soplex: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
      482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-fast
```

Fortran benchmarks:
```
-fast
```

Benchmarks using both Fortran and C:
```
-fast
```

Peak Compiler Invocation

C benchmarks:
```
icc
```

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Peak Compiler Invocation (Continued)

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:
```
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32
```

C++ benchmarks:
```
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32
```

Fortran benchmarks:
```
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32
```

Benchmarks using both Fortran and C:
```
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For other inquiries, please contact webmaster@spec.org.

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