SPEC® CFP2006 Result

Hewlett-Packard Company

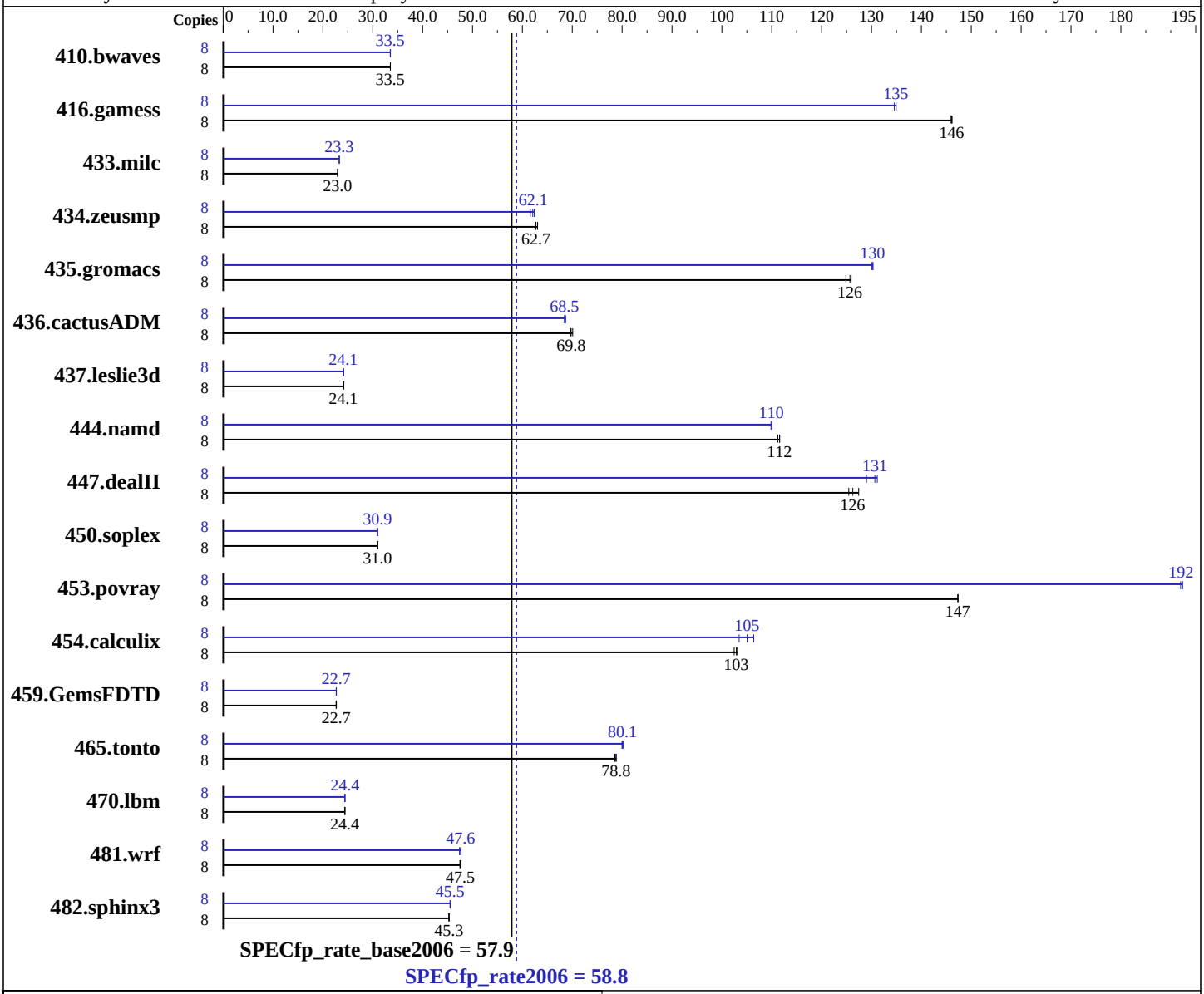
ProLiant BL460c
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 58.8

SPECfp_rate_base2006 = 57.9

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB SAS, 10 K RPM	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3243	33.5	3242	33.5	**3243**	**33.5**	8	3244	33.5	**3244**	**33.5**	3241	33.5
416.gamess	8	**1072**	**146**	1072	146	1073	146	8	1164	135	1161	135	**1161**	**135**
433.milc	8	**3198**	**23.0**	3194	23.0	3199	23.0	8	**3156**	**23.3**	3152	23.3	3156	23.3
434.zeusmp	8	1155	63.0	1163	62.6	**1161**	**62.7**	8	**1173**	**62.1**	1182	61.6	1167	62.4
435.gromacs	8	**455**	**126**	454	126	457	125	8	438	130	439	130	**439**	**130**
436.cactusADM	8	1371	69.7	**1370**	**69.8**	1364	70.1	8	**1396**	**68.5**	1397	68.4	1392	68.7
437.leslie3d	8	3107	24.2	**3119**	**24.1**	3120	24.1	8	**3117**	**24.1**	3117	24.1	3111	24.2
444.namd	8	**575**	**112**	575	112	577	111	8	584	110	**584**	**110**	583	110
447.dealII	8	718	127	730	125	**725**	**126**	8	709	129	698	131	**700**	**131**
450.soplex	8	**2155**	**31.0**	2157	30.9	2154	31.0	8	**2156**	**30.9**	2158	30.9	2154	31.0
453.povray	8	**289**	**147**	289	147	290	147	8	**221**	**192**	222	192	221	192
454.calculix	8	**641**	**103**	640	103	644	102	8	638	103	620	106	**628**	**105**
459.GemsFDTD	8	3748	22.6	3737	22.7	**3742**	**22.7**	8	**3742**	**22.7**	3744	22.7	3737	22.7
465.tonto	8	999	78.8	**999**	**78.8**	1002	78.5	8	**983**	**80.1**	981	80.2	984	80.0
470.lbm	8	4505	24.4	**4503**	**24.4**	4503	24.4	8	4502	24.4	4502	24.4	**4502**	**24.4**
481.wrf	8	1882	47.5	**1880**	**47.5**	1873	47.7	8	1886	47.4	**1876**	**47.6**	1875	47.6
482.sphinx3	8	3440	45.3	**3444**	**45.3**	3445	45.3	8	**3426**	**45.5**	3425	45.5	3426	45.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"ulimit -s unlimited" set
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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