SPEC® CFP2006 Result
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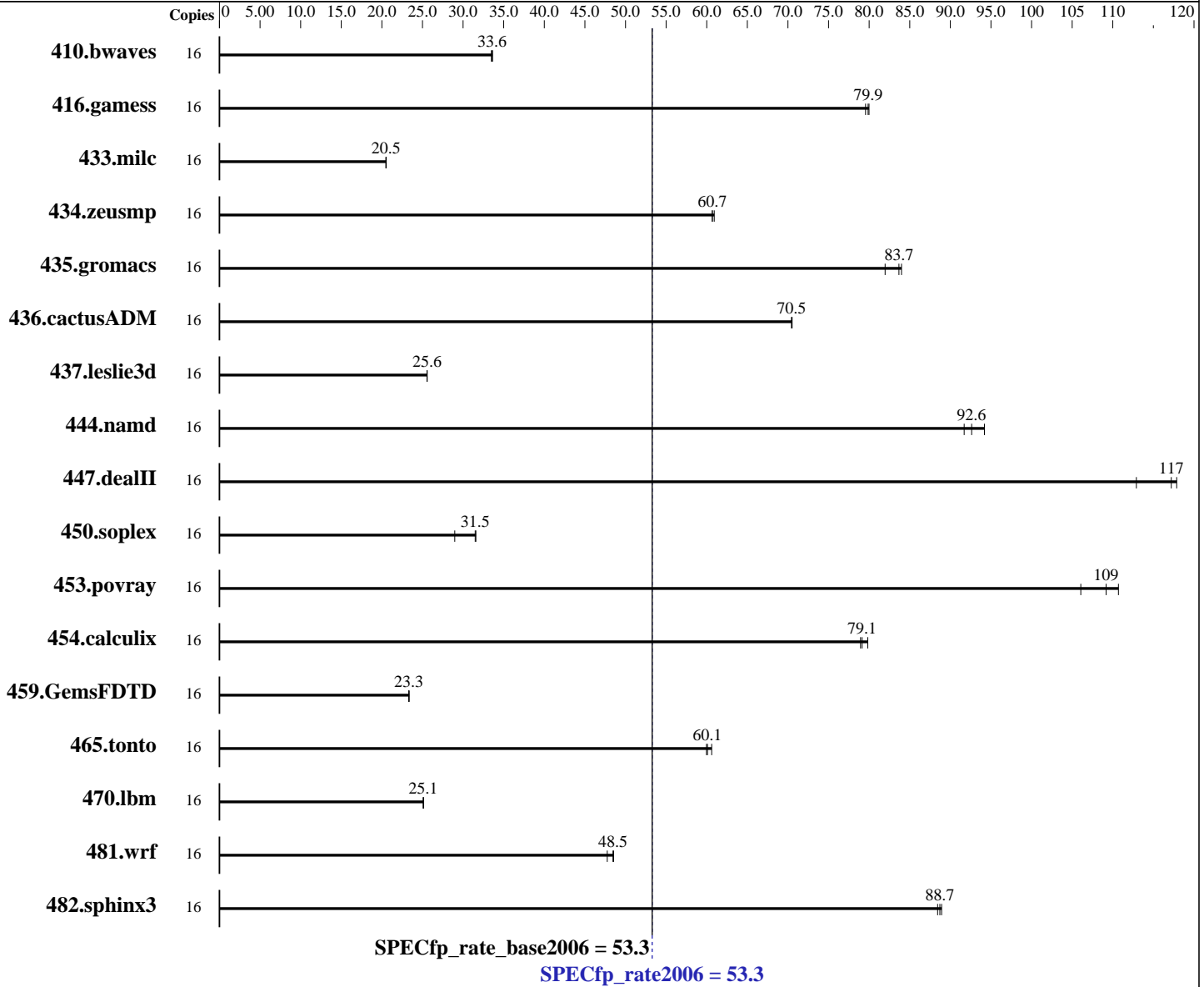
Servidor Itautec ZX440 (Intel Xeon 7140M processor)

$SPECfp^®\_rate2006 =$ 53.3

$SPECfp\_rate\_base2006 =$ 53.3

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jan-2007
Hardware Availability: Jul-2006
Software Availability: May-2006

Benchmark	Copies	Result
410.bwaves	16	33.6
416.gamess	16	79.9
433.milc	16	20.5
434.zeusmp	16	60.7
435.gromacs	16	83.7
436.cactusADM	16	70.5
437.leslie3d	16	25.6
444.namd	16	92.6
447.dealII	16	117
450.soplex	16	31.5
453.povray	16	109
454.calculix	16	79.1
459.GemsFDTD	16	23.3
465.tonto	16	60.1
470.lbm	16	25.1
481.wrf	16	48.5
482.sphinx3	16	88.7

SPECfp_rate_base2006 = 53.3
SPECfp_rate2006 = 53.3

Hardware

CPU Name:	Intel Xeon 7140M
CPU Characteristics:	Dual Core, 3.40 GHz, 800MHz System Bus
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1-4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core

Continued on next page

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.025 Build no 20060519Z
Auto Parallel:	No
File System:	NTFS
System State:	Default

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L3 Cache:	16 MB I+D on chip per chip	Base Pointers:	32-bit
Other Cache:	None	Peak Pointers:	Not Applicable
Memory:	16 GB (16x1GB DDR2-RAM, PC2-3200R, CAS 3-3-3)	Other Software:	Microquill SmartHeap Library v.8.0 for SMP
Disk Subsystem:	36 GB SCSI, 10000RPM		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	**6474**	**33.6**	6494	33.5	6467	33.6	16	**6474**	**33.6**	6494	33.5	6467	33.6
416.gamess	16	3917	80.0	**3922**	**79.9**	3938	79.6	16	3917	80.0	**3922**	**79.9**	3938	79.6
433.milc	16	7168	20.5	7152	20.5	**7158**	**20.5**	16	7168	20.5	7152	20.5	**7158**	**20.5**
434.zeusmp	16	2390	60.9	2399	60.7	**2399**	**60.7**	16	2390	60.9	2399	60.7	**2399**	**60.7**
435.gromacs	16	**1365**	**83.7**	1360	84.0	1394	82.0	16	**1365**	**83.7**	1360	84.0	1394	82.0
436.cactusADM	16	**2714**	**70.5**	2712	70.5	2714	70.5	16	**2714**	**70.5**	2712	70.5	2714	70.5
437.leslie3d	16	**5884**	**25.6**	5886	25.6	5879	25.6	16	**5884**	**25.6**	5886	25.6	5879	25.6
444.namd	16	1362	94.2	1399	91.7	**1385**	**92.6**	16	1362	94.2	1399	91.7	**1385**	**92.6**
447.dealII	16	1621	113	**1562**	**117**	1553	118	16	1621	113	**1562**	**117**	1553	118
450.soplex	16	4226	31.6	**4238**	**31.5**	4605	29.0	16	4226	31.6	**4238**	**31.5**	4605	29.0
453.povray	16	769	111	803	106	**780**	**109**	16	769	111	803	106	**780**	**109**
454.calculix	16	1653	79.8	1672	78.9	**1668**	**79.1**	16	1653	79.8	1672	78.9	**1668**	**79.1**
459.GemsFDTD	16	7265	23.4	7283	23.3	**7281**	**23.3**	16	7265	23.4	7283	23.3	**7281**	**23.3**
465.tonto	16	2626	60.0	2598	60.6	**2620**	**60.1**	16	2626	60.0	2598	60.6	**2620**	**60.1**
470.lbm	16	8751	25.1	8761	25.1	**8754**	**25.1**	16	8751	25.1	8761	25.1	**8754**	**25.1**
481.wrf	16	3743	47.7	**3688**	**48.5**	3682	48.5	16	3743	47.7	**3688**	**48.5**	3682	48.5
482.sphinx3	16	**3515**	**88.7**	3507	88.9	3526	88.4	16	**3515**	**88.7**	3507	88.9	3526	88.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
  Bios Settings:
    Hardware Prefetch enabled:
    prefetch data in order to shorten execution
    cycles and maximize data processing efficiency.

    Snoop Filter enabled:
    Preserves cache coherency while minimizing snoops to remote nodes.

    Memory Array set to High-Performance Memory Array:
    Memory is up to 4-way interleaved (Memory RAID disabled).
```

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Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
    444.namd: -TP
  447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
              -DBOOST_NO_INTRINSIC_WCHAR_T
   453.povray: -DSPEC_CPU_WINDOWS_ICL
  454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
      481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:
```
-fast /F950000000 shlSMPMt.lib           -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F950000000 shlSMPMt.lib
          -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-fast /F950000000              -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
-fast /F950000000              -link /FORCE:MULTIPLE
```

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

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Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.02.xml

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For other inquiries, please contact webmaster@spec.org.

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