SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro
Motherboard X7DBR-8

SPECint®_rate2006 =   Not Run
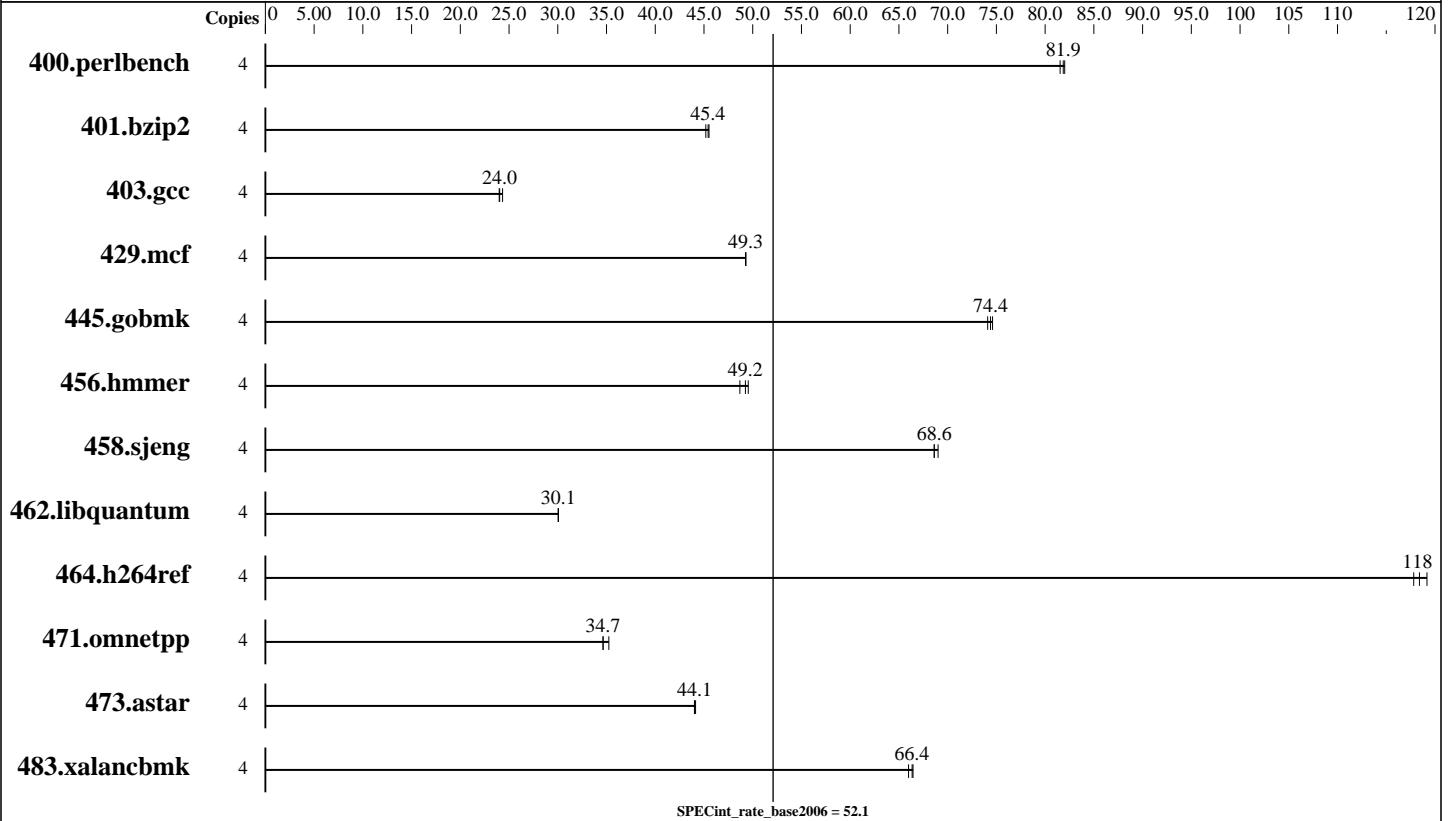
SPECint_rate_base2006 = 52.1

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Oct-2006
Hardware Availability: May-2006
Software Availability: Oct-2006

Copies		
400.perlbench	4	81.9
401.bzip2	4	45.4
403.gcc	4	24.0
429.mcf	4	49.3
445.gobmk	4	74.4
456.hmmer	4	49.2
458.sjeng	4	68.6
462.libquantum	4	30.1
464.h264ref	4	118
471.omnetpp	4	34.7
473.astar	4	44.1
483.xalancbmk	4	66.4

SPECint_rate_base2006 = 52.1

Hardware
CPU Name: Intel Xeon 5160
CPU Characteristics: 3.0GHz 1333 System Bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 X 2GB ECC, CL5, 667MHz, FBDIMM)
Disk Subsystem: 160GB IDE, 7200RPM
Other Hardware: None

Software
Operating System: Windows Server 2003 Enterprise Edition W/ SP1
Compiler: Intel C++ Compiler for IA32 version 9.1
Build no 20060816
Microsoft Visual Studio .Net 2003 (for libraries)
SmartHeap Library Version 7.4
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: None

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DBR-8

SPECint_rate2006 =   Not Run

SPECint_rate_base2006 = 52.1

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Oct-2006
Hardware Availability: May-2006
Software Availability: Oct-2006

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**477**	**81.9**	477	82.0	479	81.5							
401.bzip2	4	854	45.2	848	45.5	**850**	**45.4**							
403.gcc	4	1342	24.0	1323	24.3	**1340**	**24.0**							
429.mcf	4	740	49.3	**740**	**49.3**	740	49.3							
445.gobmk	4	562	74.6	**564**	**74.4**	566	74.1							
456.hmmer	4	**758**	**49.2**	753	49.5	767	48.7							
458.sjeng	4	**705**	**68.6**	705	68.6	701	69.0							
462.libquantum	4	2758	30.1	**2758**	**30.1**	2757	30.1							
464.h264ref	4	**748**	**118**	743	119	751	118							
471.omnetpp	4	709	35.2	**721**	**34.7**	722	34.6							
473.astar	4	**637**	**44.1**	636	44.1	638	44.0							
483.xalancbmk	4	418	66.0	**416**	**66.4**	415	66.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC816S-R700 case,
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBR-8.cfm
The system bus runs at 1333 MHz

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Supermicro Motherboard X7DBR-8

SPECint_rate2006 =   Not Run

SPECint_rate_base2006 = 52.1

CPU2006 license: 001176		**Test date:**	Oct-2006
Test sponsor: Supermicro		**Hardware Availability:**	May-2006
Tested by: Supermicro		**Software Availability:**	Oct-2006

Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
        -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:09:46 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 December 2006.