



SPEC® CFP2006 Result

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Fujitsu Siemens Computers
CELSIUS R640, Intel Xeon 5160 processor

SPECfp®2006 = 17.7
SPECfp_base2006 = 17.1

CPU2006 license: 22

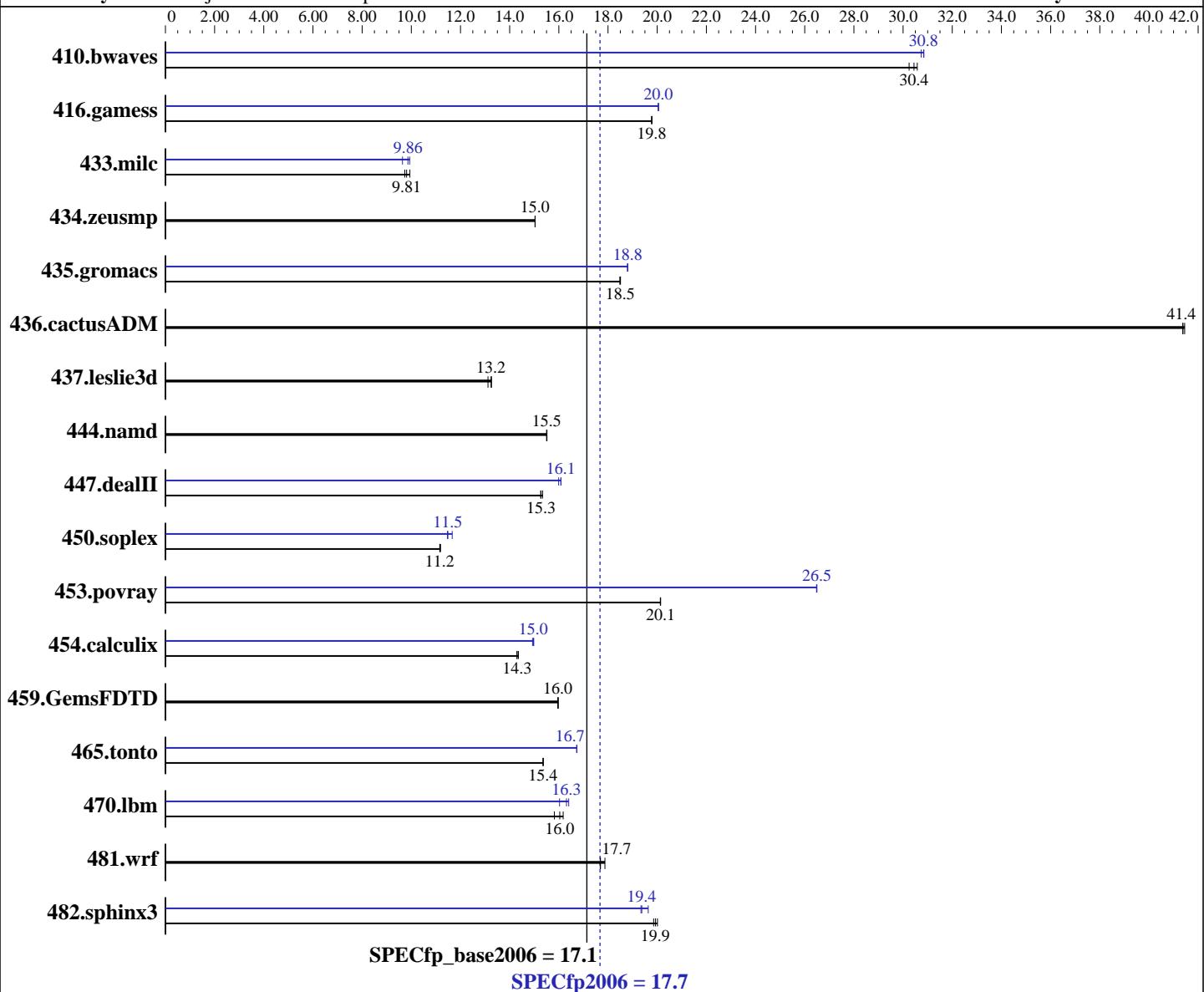
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2006

Hardware Availability: Sep-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon 5160
CPU Characteristics: Dual Core, 3.0 GHz
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip

Software

Operating System: Windows XP Professional x64 Edition
Compiler: Intel C++ Compiler for EM64T version 9.1
- Build 20061104, Package-ID W_CC_C_9.1.033
Intel Fortran Compiler for EM64T version 9.1
- Build 20061104, Package-ID W_FC_C_9.1.033
Microsoft Visual Studio 2005 (libr. & linker)
Auto Parallel: Yes
File System: NTFS
System State: Default

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L3 Cache:	None	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	64-bit
Memory:	8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)	Other Software:	Smart Heap Library, Version 8
Disk Subsystem:	SATA II 7200 rpm		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	449	30.2	446	30.4	444	30.6	442	30.7	441	30.8	441	30.8
416.gamess	990	19.8	990	19.8	990	19.8	977	20.0	976	20.1	977	20.0
433.milc	923	9.94	936	9.81	943	9.74	931	9.86	952	9.64	924	9.93
434.zeusmp	605	15.0	605	15.0	605	15.0	605	15.0	605	15.0	605	15.0
435.gromacs	386	18.5	386	18.5	386	18.5	380	18.8	380	18.8	380	18.8
436.cactusADM	289	41.4	289	41.4	288	41.5	289	41.4	289	41.4	288	41.5
437.leslie3d	710	13.2	709	13.3	717	13.1	710	13.2	709	13.3	717	13.1
444.namd	517	15.5	517	15.5	517	15.5	517	15.5	517	15.5	517	15.5
447.dealII	746	15.3	750	15.3	749	15.3	715	16.0	711	16.1	711	16.1
450.soplex	745	11.2	747	11.2	747	11.2	715	11.7	727	11.5	726	11.5
453.povray	264	20.1	264	20.1	264	20.1	201	26.5	201	26.5	201	26.5
454.calculix	577	14.3	577	14.3	575	14.4	551	15.0	553	14.9	551	15.0
459.GemsFDTD	665	16.0	665	16.0	664	16.0	665	16.0	665	16.0	664	16.0
465.tonto	641	15.4	641	15.4	640	15.4	588	16.7	588	16.7	588	16.7
470.lbm	857	16.0	869	15.8	849	16.2	838	16.4	842	16.3	857	16.0
481.wrf	631	17.7	625	17.9	632	17.7	631	17.7	625	17.9	632	17.7
482.sphinx3	978	19.9	982	19.9	974	20.0	993	19.6	1006	19.4	1008	19.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
 icl -Qvc8 -Qc99

C++ benchmarks:
 icl -Qvc8

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc8 -Qc99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -D_Complex= -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -D_Complex= -DSPEC_CPU_P64
436.cactusADM: -D_Complex= -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -D_Complex= -DSPEC_CPU_P64 -DBOOST_NO_INTRINSIC_WCHAR_T
-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER
-Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -D_Complex= -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -D_Complex= -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-fast -Qparallel -F950000000 shlw32M.lib

C++ benchmarks:

-fast -Qparallel -Qcxx-features -F950000000 shlw32M.lib

Fortran benchmarks:

-fast -Qparallel -F950000000 shlw32M.lib

Benchmarks using both Fortran and C:

-fast -Qparallel -F950000000 shlw32M.lib



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Peak Compiler Invocation

C benchmarks:

 icl -Qvc8 -Qc99

C++ benchmarks:

 icl -Qvc8

Fortran benchmarks:

 ifort

Benchmarks using both Fortran and C:

 icl -Qvc8 -Qc99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

 -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F9500000000 shlw32M.lib

C++ benchmarks:

 444.namd: basepeak = yes

 447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
 -F9500000000 shlw32M.lib

 450.soplex: Same as 447.dealII

 453.povray: Same as 447.dealII

Fortran benchmarks:

 410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel
 -F9500000000 shlw32M.lib

 416.gamess: -fast -F9500000000 shlw32M.lib

 434.zeusmp: basepeak = yes

 437.leslie3d: basepeak = yes

 459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000
shlw32M.lib

436.cactusADM: basepeak = yes

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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