



# SPEC® CFP2006 Result

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## Hewlett-Packard Company

HP Integrity rx6600 (1.6GHz/24MB Dual-Core Intel Itanium 2)

SPECfp®2006 = 18.1

SPECfp\_base2006 = 17.3

CPU2006 license: 03

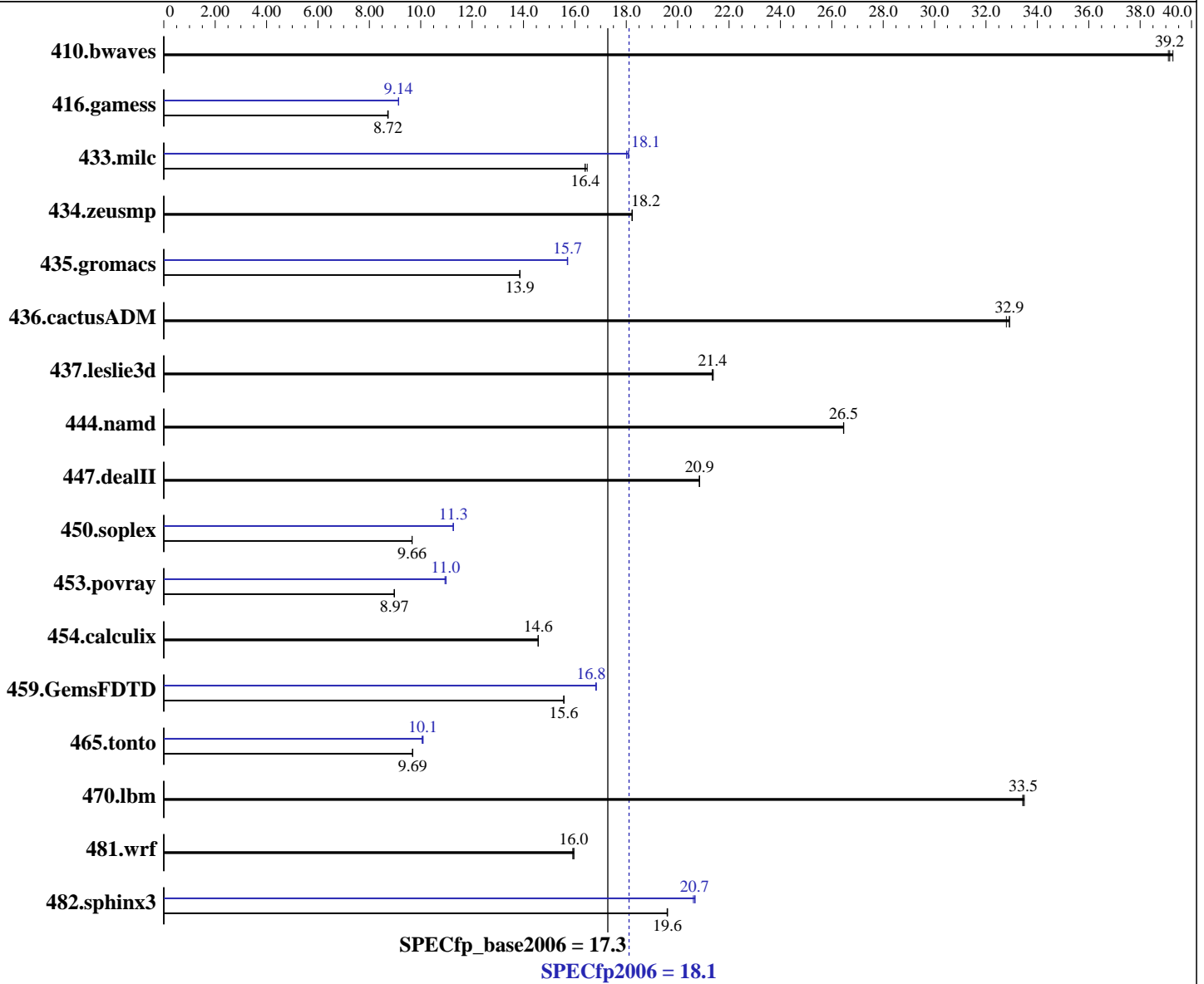
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Aug-2006

Hardware Availability: Sep-2006

Software Availability: Sep-2006



### Hardware

CPU Name: Dual-Core Intel Itanium 2 9050  
 CPU Characteristics: 1.6GHz/24MB, 533MHz FSB  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1-4 chips  
 Primary Cache: 16 KB I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I + 256 KB D on chip per core

Continued on next page

### Software

Operating System: HPUX11i-TCOE B.11.23.0609  
 Compiler: HP C/aC++ Developer's Bundle C.11.23.12  
 HP Fortran90 Compiler B.11.23.32  
 Auto Parallel: No  
 File System: vxfs  
 System State: Multi-user  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: None



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L3 Cache: 12 MB I+D on chip per core  
Other Cache: None  
Memory: 24 GB (24x1GB DIMMs)  
Disk Subsystem: 73GB 10K RPM SAS  
Other Hardware: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	348	39.1	<b>347</b>	<b>39.2</b>	346	39.3	348	39.1	<b>347</b>	<b>39.2</b>	346	39.3
416.gamess	2247	8.71	<b>2244</b>	<b>8.72</b>	2241	8.74	2142	9.14	<b>2143</b>	<b>9.14</b>	2143	9.14
433.milc	560	16.4	557	16.5	<b>560</b>	<b>16.4</b>	507	18.1	<b>508</b>	<b>18.1</b>	510	18.0
434.zeusmp	499	18.2	<b>499</b>	<b>18.2</b>	499	18.2	499	18.2	<b>499</b>	<b>18.2</b>	499	18.2
435.gromacs	515	13.9	515	13.9	<b>515</b>	<b>13.9</b>	454	15.7	<b>454</b>	<b>15.7</b>	455	15.7
436.cactusADM	<b>363</b>	<b>32.9</b>	363	32.9	364	32.8	<b>363</b>	<b>32.9</b>	363	32.9	364	32.8
437.leslie3d	440	21.3	440	21.4	<b>440</b>	<b>21.4</b>	440	21.3	440	21.4	<b>440</b>	<b>21.4</b>
444.namd	303	26.5	303	26.5	<b>303</b>	<b>26.5</b>	303	26.5	303	26.5	<b>303</b>	<b>26.5</b>
447.dealII	549	20.8	<b>549</b>	<b>20.9</b>	548	20.9	549	20.8	<b>549</b>	<b>20.9</b>	548	20.9
450.soplex	<b>863</b>	<b>9.66</b>	863	9.66	863	9.67	740	11.3	<b>740</b>	<b>11.3</b>	741	11.3
453.povray	593	8.98	593	8.97	<b>593</b>	<b>8.97</b>	486	10.9	484	11.0	<b>485</b>	<b>11.0</b>
454.calculix	566	14.6	<b>566</b>	<b>14.6</b>	566	14.6	566	14.6	<b>566</b>	<b>14.6</b>	566	14.6
459.GemsFDTD	<b>681</b>	<b>15.6</b>	682	15.6	681	15.6	<b>630</b>	<b>16.8</b>	630	16.8	631	16.8
465.tonto	1017	9.68	1016	9.69	<b>1016</b>	<b>9.69</b>	975	10.1	<b>976</b>	<b>10.1</b>	979	10.1
470.lbm	411	33.4	<b>411</b>	<b>33.5</b>	410	33.5	411	33.4	<b>411</b>	<b>33.5</b>	410	33.5
481.wrf	702	15.9	700	16.0	<b>700</b>	<b>16.0</b>	702	15.9	700	16.0	<b>700</b>	<b>16.0</b>
482.sphinx3	995	19.6	994	19.6	<b>994</b>	<b>19.6</b>	942	20.7	<b>943</b>	<b>20.7</b>	946	20.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

The system had the September 2006 HP-UX 11i v2 Technical Computing Operating Environment (TCOE) and compilers installed, along with the following patches:

```

PHSS_34858 linker + fdp cumulative patch
PHSS_34853 Math Library Cumulative Patch
PHSS_34854 Integrity Unwind Library
PHSS_34855 HP C Compiler (A.06.12)
PHSS_34856 aC++ Compiler (A.06.12)
PHSS_34857 u2comp/be/plugin library patch
PHSS_34395 FORTRAN I/O Library [libIO77]
PHSS_34397 FORTRAN Intrinsics [libF90 B.11.23.17]
PHSS_34399 Fortran Product Patch, v3.1 to v3.1.1
PHKL_34020 Perfmon enhancements and Itanium Dual-Core

```

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## Operating System Notes (Continued)

The following kernel tunables were set, in addition to the defaults set by the Technical Computing OE:

```
dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
maxssiz=401604608
```

## Platform Notes

The "cpuconfig" EFI command was used prior to booting to deconfigure processors.

Although two cores were enabled during testing, the SPEC CPU2006 benchmarks used only one core.

## Base Compiler Invocation

C benchmarks:

```
/opt/ansic/bin/cc -Ae
```

C++ benchmarks:

```
/opt/aCC/bin/aCC -Aa
```

Fortran benchmarks:

```
/opt/fortran90/bin/f90
```

Benchmarks using both Fortran and C:

```
/opt/ansic/bin/cc -Ae /opt/fortran90/bin/f90
```

## Base Portability Flags

```
453.povray: -DSPEC_CPU_NEED_INVHYP
```

```
454.calculix: -DSPEC_CPU_NOZMODIFIER
```

```
481.wrf: -DNOUNDERSCORE +noppu
```

## Base Optimization Flags

C benchmarks:

```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
```

C++ benchmarks:

```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
```

Continued on next page

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Page 3



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## Base Optimization Flags (Continued)

Fortran benchmarks:

+Ofaster -Wl,-a,archive\_shared -Wl,+pd,64M -Wl,+pi,64M -Wl,-N

Benchmarks using both Fortran and C:

+Ofaster(-hp\_cc) +Otype\_safety=ansi -Wl,-a,archive\_shared -Wl,+pd,64M  
-Wl,+pi,64M +Ofaster(-hp\_f90) -Wl,-N

## Peak Compiler Invocation

C benchmarks:

/opt/ansic/bin/cc -Ae

C++ benchmarks:

/opt/aCC/bin/aCC -Aa

Fortran benchmarks:

/opt/fortran90/bin/f90

Benchmarks using both Fortran and C:

/opt/ansic/bin/cc -Ae /opt/fortran90/bin/f90

## Peak Portability Flags

453.povray: -DSPEC\_CPU\_NEED\_INVHYP

454.calculix: -DSPEC\_CPU\_NOZMODIFIER

481.wrf: -DNOUNDERSCORE +noppu

## Peak Optimization Flags

C benchmarks:

433.milc: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
+Otype\_safety=ansi -Wl,-a,archive\_shared -Wl,+pd,64M  
-Wl,+pi,64M +Onoparmsoverlap -Wl,-N

470.lbm: basepeak = yes

482.sphinx3: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
+Otype\_safety=ansi -Wl,-a,archive\_shared -Wl,+pd,64M  
-Wl,+pi,64M +Onoparmsoverlap

Continued on next page

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Page 4



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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
+Otype\_safety=ansi -Wl,-a,archive\_shared -Wl,+pd,64M  
-Wl,+pi,64M +Onoparmsoverlap -Wl,-N

453.povray: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
+Otype\_safety=ansi -Wl,-a,archive\_shared -Wl,+pd,64M  
-Wl,+pi,64M

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: +Ofaster -Wl,-a,archive\_shared -Wl,+pd,64M -Wl,+pi,64M  
+Odataprefetch=direct -Wl,-N

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
-Wl,-a,archive\_shared -Wl,+pd,64M -Wl,+pi,64M  
+Odataprefetch=direct -Wl,-N

465.tonto: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster  
-Wl,-a,archive\_shared -Wl,+pd,64M -Wl,+pi,64M  
+Odataprefetch=direct

Benchmarks using both Fortran and C:

435.gromacs: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)  
+Ofaster(-hp\_cc) +Otype\_safety=ansi -Wl,-a,archive\_shared  
-Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap +Ofaster(-hp\_f90)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090715.06.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.06.html)



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You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090715.06.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.06.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 09:56:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 19 September 2006.