



SPEC[®] CFP2006 Result

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Sun Microsystems Sun Fire 3800

SPECfp[®]2006 = --
SPECfp_base2006 = 2.86

CPU2006 license: 6

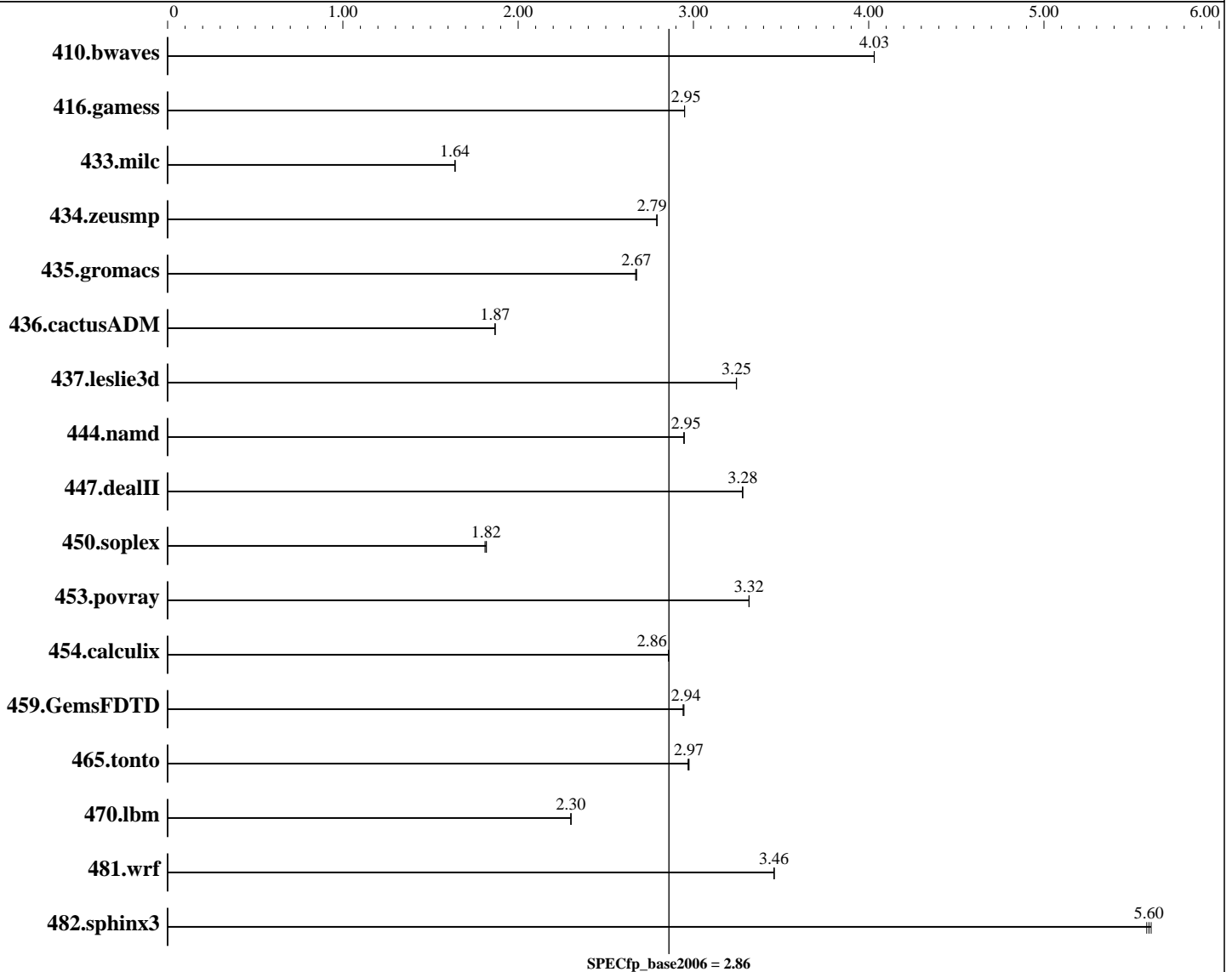
Test sponsor: Sun Microsystems

Tested by: Sun Microsystems

Test date: Mar-2006

Hardware Availability: Nov-2001

Software Availability: May-2006



Hardware

CPU Name: UltraSPARC III Cu
 CPU Characteristics: 900
 CPU MHz: Integrated
 FPU: 1 core, 1 chip, 1 core/chip
 CPU(s) enabled: 2,4,6,8 chips
 CPU(s) orderable: 32 KB I + 64 KB D on chip per chip
 Primary Cache: 8 MB I+D off chip per chip
 Secondary Cache:

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Software

Operating System: Solaris 10 1/06
 Compiler: Sun Studio 11 with patch 120760-03
 Auto Parallel: No
 File System: ufs
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: None



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L3 Cache: None
Other Cache: None
Memory: 4 GB (8 x 512 MB DIMM, 4-way interleaved)
Disk Subsystem: 2x36GB 10,000RPM SCSI (RAID 1)
Other Hardware: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	3371	4.03	3371	4.03	3371	4.03						
416.gamess	6638	2.95	6639	2.95	6639	2.95						
433.milc	5598	1.64	5598	1.64	5598	1.64						
434.zeusmp	3260	2.79	3260	2.79	3260	2.79						
435.gromacs	2674	2.67	2669	2.68	2669	2.67						
436.cactusADM	6396	1.87	6396	1.87	6396	1.87						
437.leslie3d	2897	3.25	2896	3.25	2897	3.24						
444.namd	2723	2.95	2721	2.95	2721	2.95						
447.dealII	3489	3.28	3486	3.28	3486	3.28						
450.soplex	4590	1.82	4606	1.81	4585	1.82						
453.povray	1604	3.32	1604	3.32	1604	3.32						
454.calculix	2885	2.86	2885	2.86	2885	2.86						
459.GemsFDTD	3607	2.94	3606	2.94	3601	2.95						
465.tonto	3308	2.97	3309	2.97	3314	2.97						
470.lbm	5970	2.30	5970	2.30	5971	2.30						
481.wrf	3228	3.46	3228	3.46	3227	3.46						
482.sphinx3	3473	5.61	3489	5.59	3481	5.60						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack limit set to 131072

Platform Notes

Only 1 CPU enabled by the system controller
(prior to boot)

Base Compiler Invocation

C benchmarks:
cc

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Base Compiler Invocation (Continued)

C++ benchmarks:
cc

Fortran benchmarks:
f90

Benchmarks using both Fortran and C:
cc f90

Base Optimization Flags

C benchmarks:
-g -fast -xpagesize=4M

C++ benchmarks:
-g0 -library=stlport4 -fast -xpagesize=4M

Fortran benchmarks:
-g -fast -xpagesize=4M

Benchmarks using both Fortran and C:
-g -fast(cc) -fast(f90) -xpagesize=4M

Base Other Flags

C benchmarks:
-V

C++ benchmarks:
-verbose=version

Fortran benchmarks:
-V

Benchmarks using both Fortran and C:
-V

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/sun-studio.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/sun-studio.xml>



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For other inquiries, please contact webmaster@spec.org.

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