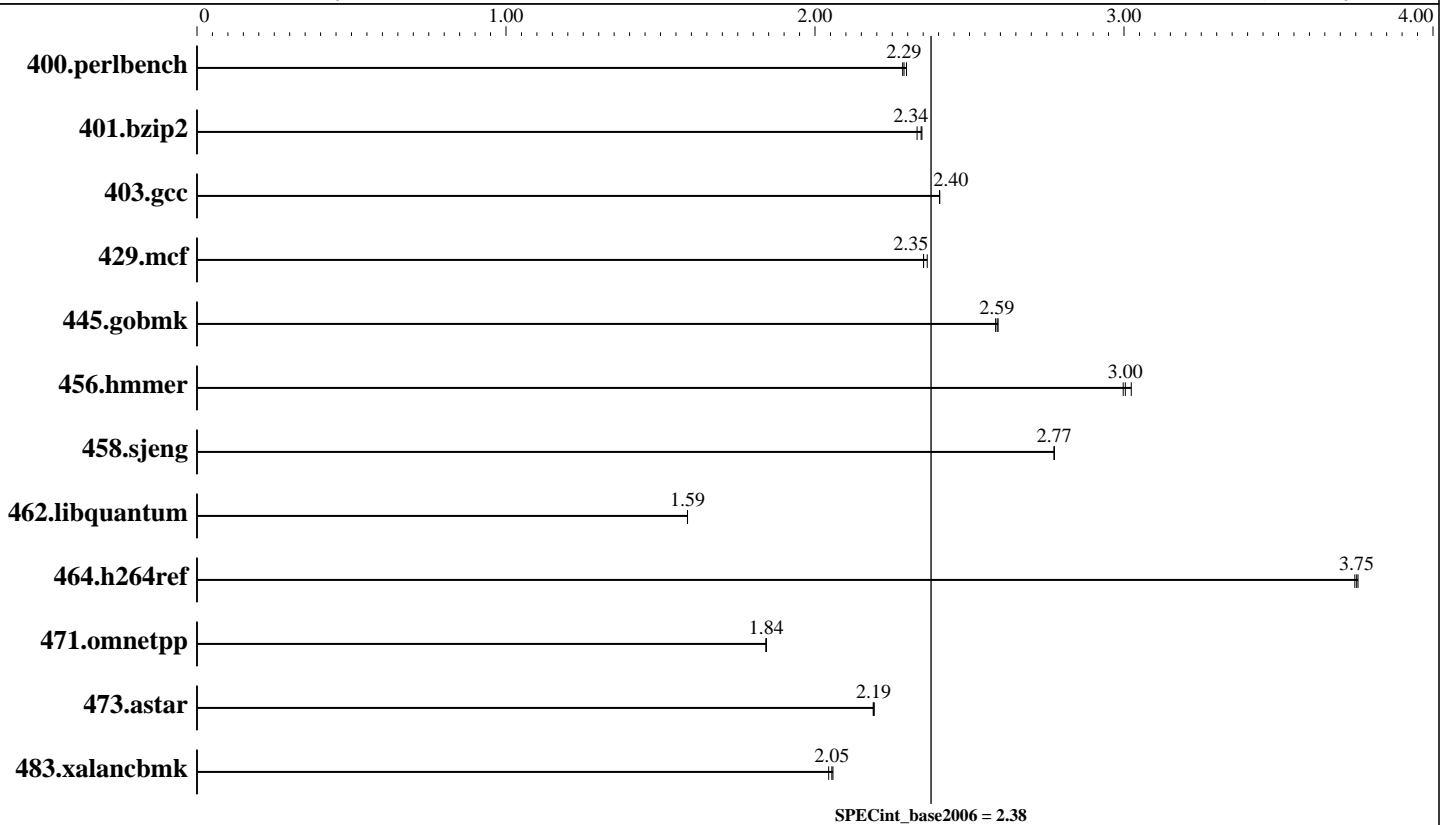# SPEC® CINT2006 Result
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Sun Microsystems
Sun Fire 280R

SPECint®2006 =   --

SPECint_base2006 =   2.38

Benchmark	Result
400.perlbench	2.29
401.bzip2	2.34
403.gcc	2.40
429.mcf	2.35
445.gobmk	2.59
456.hmmer	3.00
458.sjeng	2.77
462.libquantum	1.59
464.h264ref	3.75
471.omnetpp	1.84
473.astar	2.19
483.xalancbmk	2.05

SPECint_base2006 = 2.38

Hardware
CPU Name: UltraSPARC III
CPU Characteristics:
CPU MHz: 750
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 64 KB D on chip per chip
Secondary Cache: 8 MB I+D off chip per chip
L3 Cache: None
Other Cache: None
Memory: 4 GB (2-way interleaved)
Disk Subsystem: 2x36GB 10,000RPM FC-AL (RAID 1)
Other Hardware: None

Software
Operating System: Solaris 10 1/06
Compiler: Sun Studio 11 with patch 120760-03
Auto Parallel: No
File System: ufs
System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: None

SPEC CINT2006 Result
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Sun Microsystems
Sun Fire 280R

SPECint2006 =   --

SPECint_base2006 =   2.38

CPU2006 license: 6
Test sponsor: Sun Microsystems
Tested by: Sun Microsystems

Test date: Mar-2006
Hardware Availability: Sep-2000
Software Availability: May-2006

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4255	2.30	4278	2.28	**4270**	**2.29**						
401.bzip2	4113	2.35	**4118**	**2.34**	4141	2.33						
403.gcc	3350	2.40	3349	2.40	**3350**	**2.40**						
429.mcf	3859	2.36	3879	2.35	**3879**	**2.35**						
445.gobmk	**4051**	**2.59**	4059	2.58	4046	2.59						
456.hmmer	3086	3.02	**3106**	**3.00**	3113	3.00						
458.sjeng	4361	2.77	**4361**	**2.77**	4363	2.77						
462.libquantum	13055	1.59	**13053**	**1.59**	13052	1.59						
464.h264ref	5890	3.76	**5897**	**3.75**	5906	3.75						
471.omnetpp	3395	1.84	**3392**	**1.84**	3392	1.84						
473.astar	3208	2.19	**3206**	**2.19**	3203	2.19						
483.xalancbmk	**3359**	**2.05**	3353	2.06	3375	2.04						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

    Stack limit set to 131072

Platform Notes

    Only 1 cpu enabled by firmware
    (prior to boot)

Base Compiler Invocation

C benchmarks:
    cc

C++ benchmarks:
    CC

Base Portability Flags

    400.perlbench: -DSPEC_CPU_SOLARIS_SPARC
          403.gcc: -DSPEC_CPU_SOLARIS
    462.libquantum: -DSPEC_CPU_SOLARIS
    483.xalancbmk: -DSPEC_CPU_SOLARIS

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Test date: Mar-2006
Hardware Availability: Sep-2000
Software Availability: May-2006

Base Optimization Flags

C benchmarks:
```
-g -fast -xpagesize=4M
```

C++ benchmarks:
```
-g0 -library=stlport4 -fast -xpagesize=4M
```

Base Other Flags

C benchmarks:
```
-V
```

C++ benchmarks:
```
-verbose=version
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/sun-studio.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/sun-studio.xml

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For other inquiries, please contact webmaster@spec.org.

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