



SPEC[®] CINT2006 Result

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Apple Computer, Inc.

SPECint[®]2006 = 10.1

iMac 20-inch 2.0GHz Intel Core Duo

SPECint_base2006 = 10.1

CPU2006 license: 77

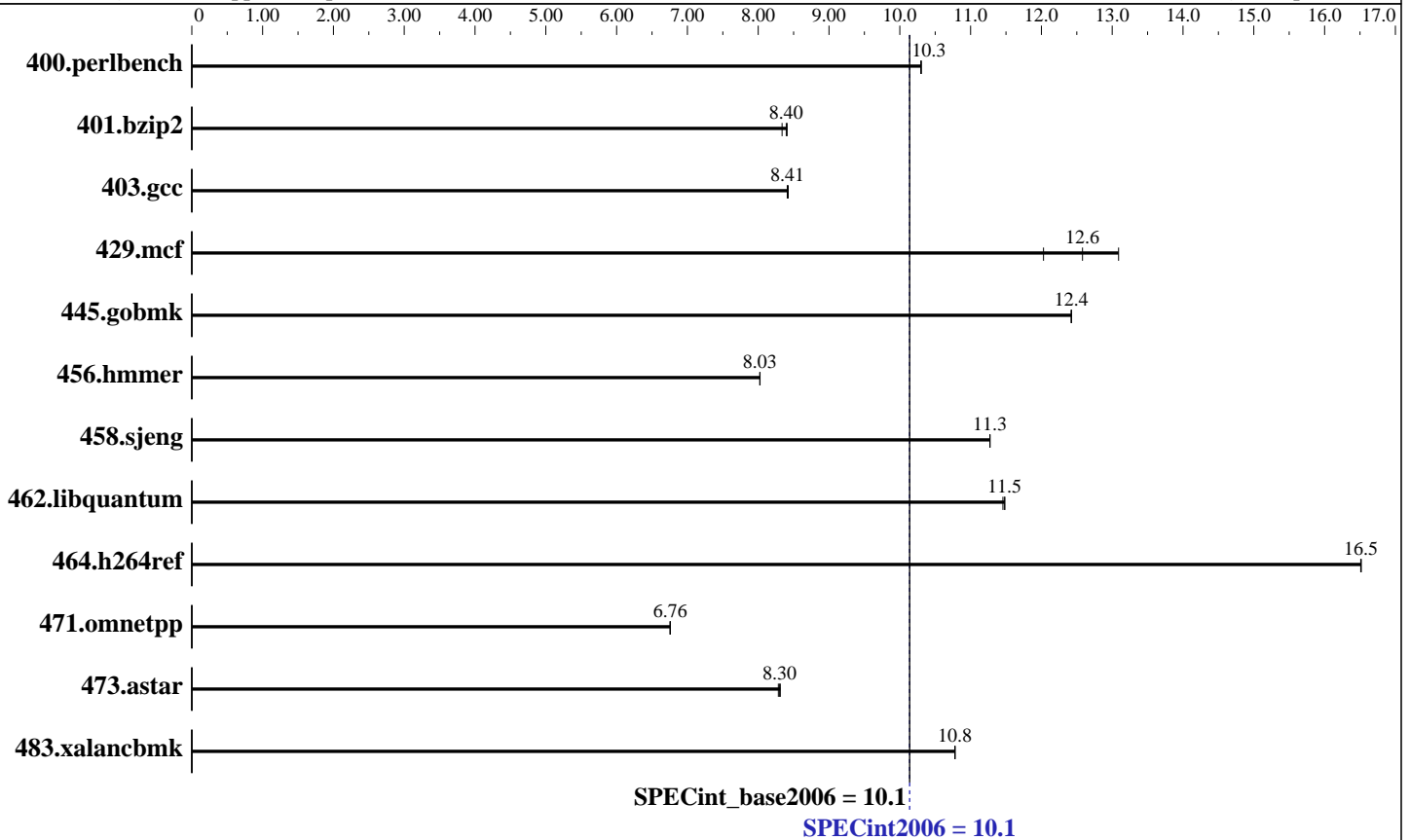
Test sponsor: Apple Computer, Inc.

Tested by: Apple Computer, Inc.

Test date: Apr-2006

Hardware Availability: Jan-2006

Software Availability: Apr-2006



Hardware

CPU Name: Intel Core Duo T2500
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 1 GB, DDR2 667 MHz, CL5
 Disk Subsystem: SATA Maxtor 6L250M0, 250 GB
 Other Hardware: None

Software

Operating System: Mac OS X, v10.4.6
 Compiler: Intel C++ Compiler for Mac OS X, v9.1
 Intel Fortran Compiler for Mac OS X, v9.1 (Build 20060323)
 Auto Parallel: No
 File System: HFS+
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	949	10.3	948	10.3	948	10.3	949	10.3	948	10.3	948	10.3
401.bzip2	1149	8.40	1148	8.41	1157	8.34	1149	8.40	1148	8.41	1157	8.34
403.gcc	957	8.41	956	8.42	957	8.41	957	8.41	956	8.42	957	8.41
429.mcf	725	12.6	697	13.1	758	12.0	725	12.6	697	13.1	758	12.0
445.gobmk	845	12.4	844	12.4	844	12.4	845	12.4	844	12.4	844	12.4
456.hammer	1162	8.03	1163	8.02	1163	8.03	1162	8.03	1163	8.02	1163	8.03
458.sjeng	1073	11.3	1073	11.3	1073	11.3	1073	11.3	1073	11.3	1073	11.3
462.libquantum	1809	11.5	1805	11.5	1804	11.5	1809	11.5	1805	11.5	1804	11.5
464.h264ref	1340	16.5	1340	16.5	1340	16.5	1340	16.5	1340	16.5	1340	16.5
471.omnetpp	925	6.76	925	6.75	925	6.76	925	6.76	925	6.75	925	6.76
473.astar	847	8.28	845	8.31	846	8.30	847	8.28	845	8.31	846	8.30
483.xalancbmk	640	10.8	640	10.8	640	10.8	640	10.8	640	10.8	640	10.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Memory Details :

DIMM0/Bank0: 512MB, DDR2 SDRAM, 667 MHz, CL5

DIMM1/Bank1: 512MB, DDR2 SDRAM, 667 MHz, CL5

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

403.gcc: -DSPEC_CPU_MACOSX
462.libquantum: -DSPEC_CPU_MACOSX
483.xalancbmk: -DSPEC_CPU_MACOSX

Base Optimization Flags

C benchmarks:
-fast

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Base Optimization Flags (Continued)

C++ benchmarks:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Peak Portability Flags

403.gcc: -DSPEC_CPU_MACOSX
462.libquantum: -DSPEC_CPU_MACOSX
483.xalancbmk: -DSPEC_CPU_MACOSX

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/macosx-iccifort-v91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/macosx-iccifort-v91-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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